

DQIIEngineering Drawings Digital Equipment Corporation

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NPR-SYNC LINE CONTROL

DF11 CONV CABLE (D1)

DATA SET CNTL (D2)

UNIBUS CONNECTORS

ADDRESS SELECTOR

CRYSTAL CLOCK

CABLE CONNECTOR

I/O CABLE ASSY

MODULE UTILIZATION (AA)

MODULE UTILIZATION (AB)

CC/BA & SHIFT CNTL (D5)

ADDRESS INTERRUPT CLOCK (D3)

PMG CHAR DET & SEQ CNTL (D8)

AB BUS SEL & BCC CNTL (D9)

INTERRUPT CONTROL MODULE

WIRED CHAR DET & NPR CNTL. (D6) D-CS-M7818-Ø-1 DQ11-AA/DQ11-AB CONNECTIONS (D7) D-IC-DQ11-Ø-7

BUS SEL CSR'S, SH. REG (D4)

DRAWING DIRECTORY

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CUSTOMER PRINT SET INDEX

SEQUENCE 7 SEQUENCE 7 NPR-SYNC LINE CONTROL B-DD-DQ11-Ø NPR-SYNC LINE CONTROL

THIS IS PRINT SET

		PF	lin'	r si	EΤ
	UNIT VARIATIONS	0	-KA		
VAR	TITLE	DQ11	DQ11-		
DQ11-AA	BASIC SYNC CONTROL	X			
DQ11-BA	DATA SET CONTROL	X			
DQ11-AB	BCC CONTROL	X			
DQ11-BB	PROTOCOL CONTROL	X			
DQ11-KA	CRYSTAL CLOCK (M4050)		X		
DQ11-DA	DQ11-AA+BA+DF11-A	X			L
DQII_EA	DQ11-AA+BA+DF11 -G	Х			
DQ11-DE	DQ11-AA+AB+BA+BB+DF11-A	X			
DQ11-EE	DQ11-AA+AB+BA+BB+DF11-G	Х			L
DQ11-DD	DQ11-AA+AB+BA+DF11-A	X			
DQ11-ED	DQ11-AA+AB+BA+DF11-G	X			
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D-CS-M92Ø-Ø-1
C-IA-700946 7- Ø-Ø
$K-WL-DQ11-\emptyset-12$
$D-IA-7009152-\emptyset-\emptyset$
C-IA-7009468-Ø-Ø
K-WL-DQ11-Ø-14
D-CS-H315-Ø-1
B-DD-DF11-Ø
A-AL-DO11-Ø-3
D-IA-7009563- <i>Ø</i> -Ø
A-WT-7009468-Ø

D-UA-DQ11-Ø-Ø

A-PL-D011-7-0

D-MU-D011-Ø-1

D-MU-DQ11-Ø-2

D-BD-DQ11-Ø-4

D-CS-M7815-Ø-1

D-CS-M7812-Ø-1

D-CS-M7813-Ø-1

D-CS-M7817-Ø-1

D-CS-M7816-Ø-1

D-IC-D011-Ø-6

D-CS-M105-0-1

D-CS-M7821-Ø-1

D-CS-M4050-Ø-1

 $D-CS-M971-\emptyset-1$

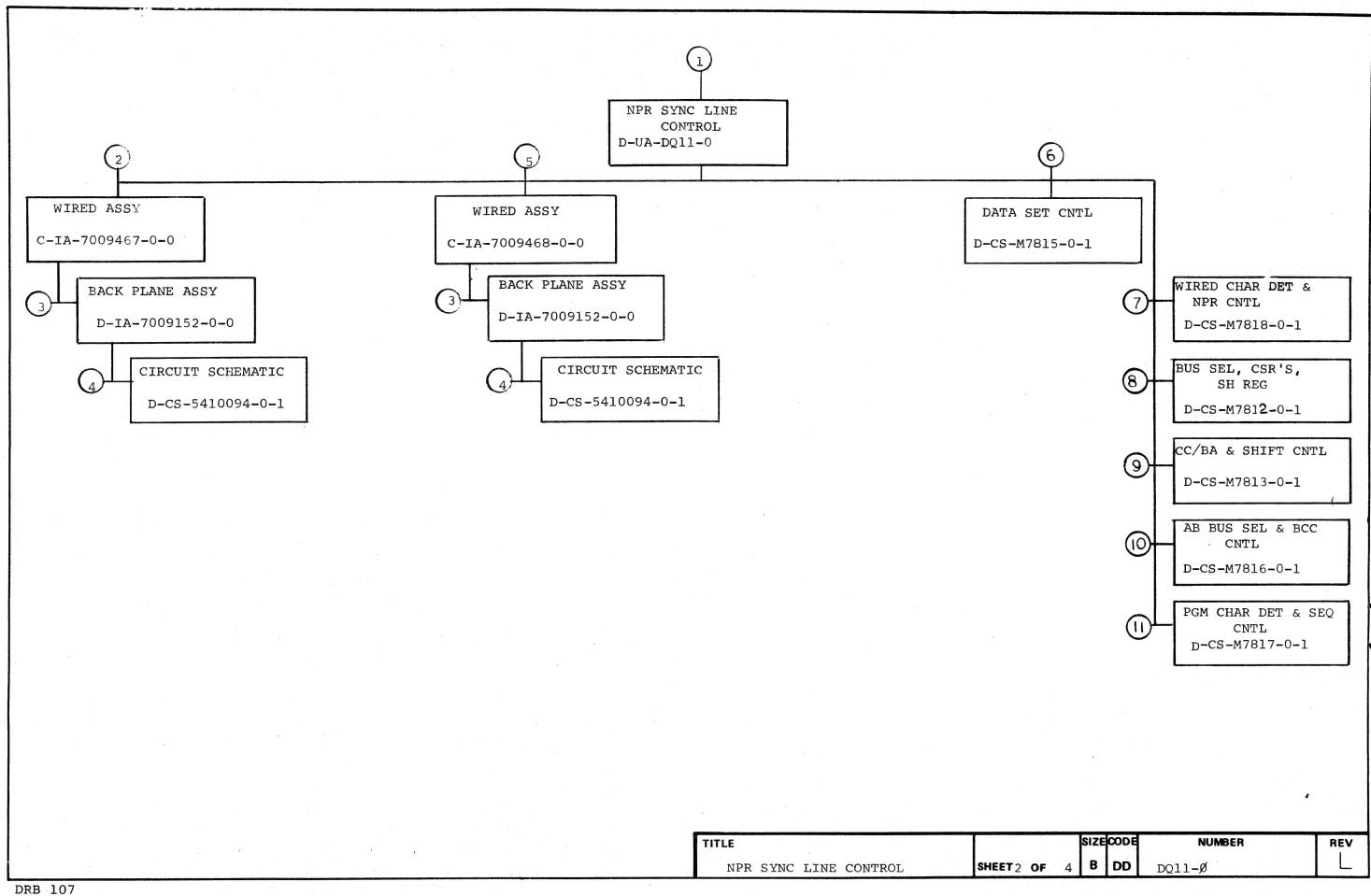
C-IA-BCØ8S-Ø-Ø

D-BS-DQ11-Ø-5

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JEC 16-(325) 136	REVISIONS	CHG. NO.	DQIIAA-2	DONAB-1	DQIIAB-2	DQ11-1	DQIIAB-3	DQIIAB-4	DOIIAA-3	2-1100	DQHAF-5	DQ11-3	
200		DA⊤E	2/74	3/74	4/74	4/74	8/74	3/75	4/75	6/75	2/12	9//2	

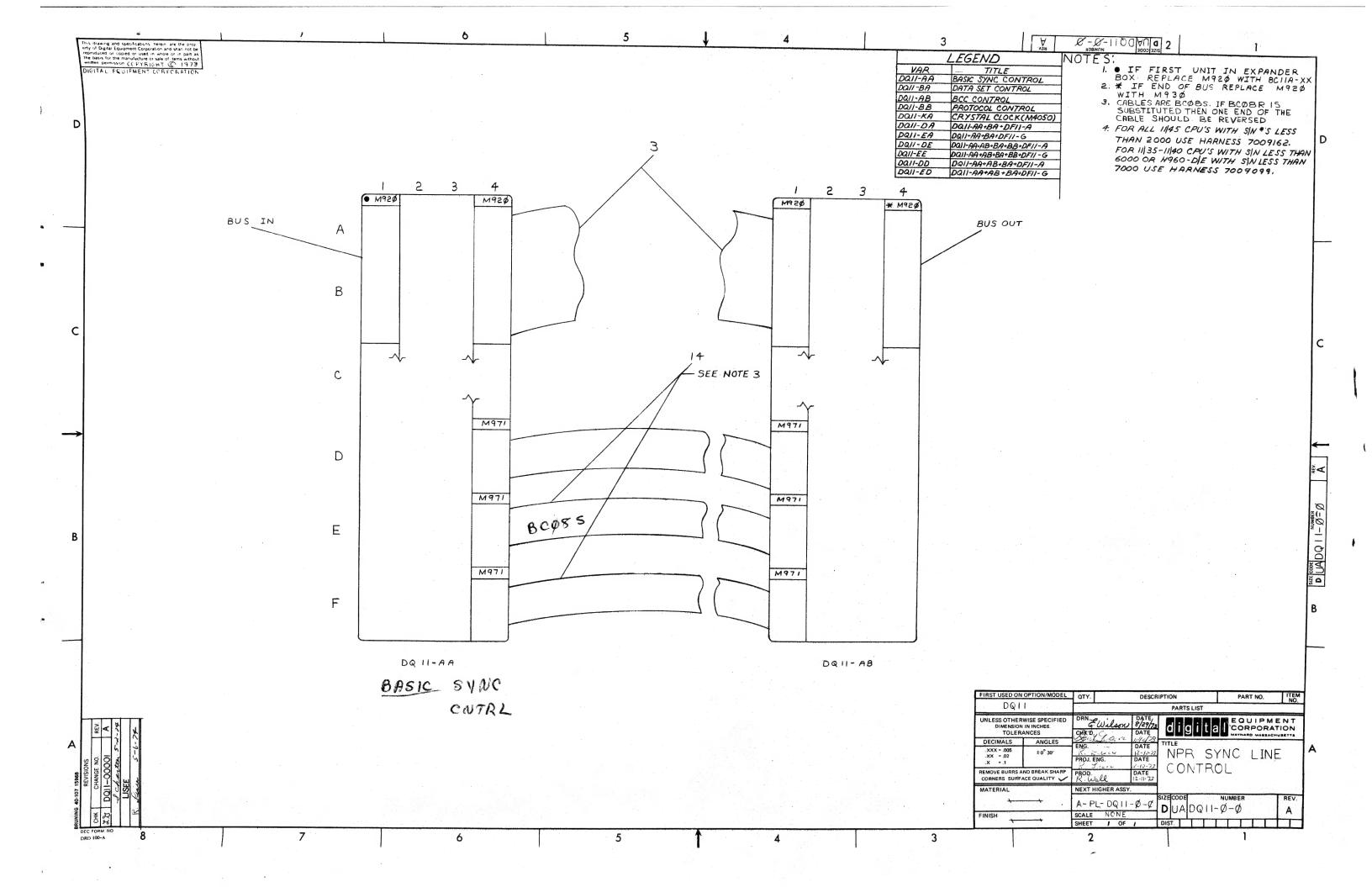
USED ON OPTION/MODEL	DRN. E.WILSON	DATE 9/4/73	TITLE			
11/40 11/45	CHK D.	DATE		NPR SYNC LINE	CONTROL	
11/43	PROJ ENG.	DATE 12-10-23		**		
	PROD. Wall	DATE 2-11-73	SIZE COD			RFV
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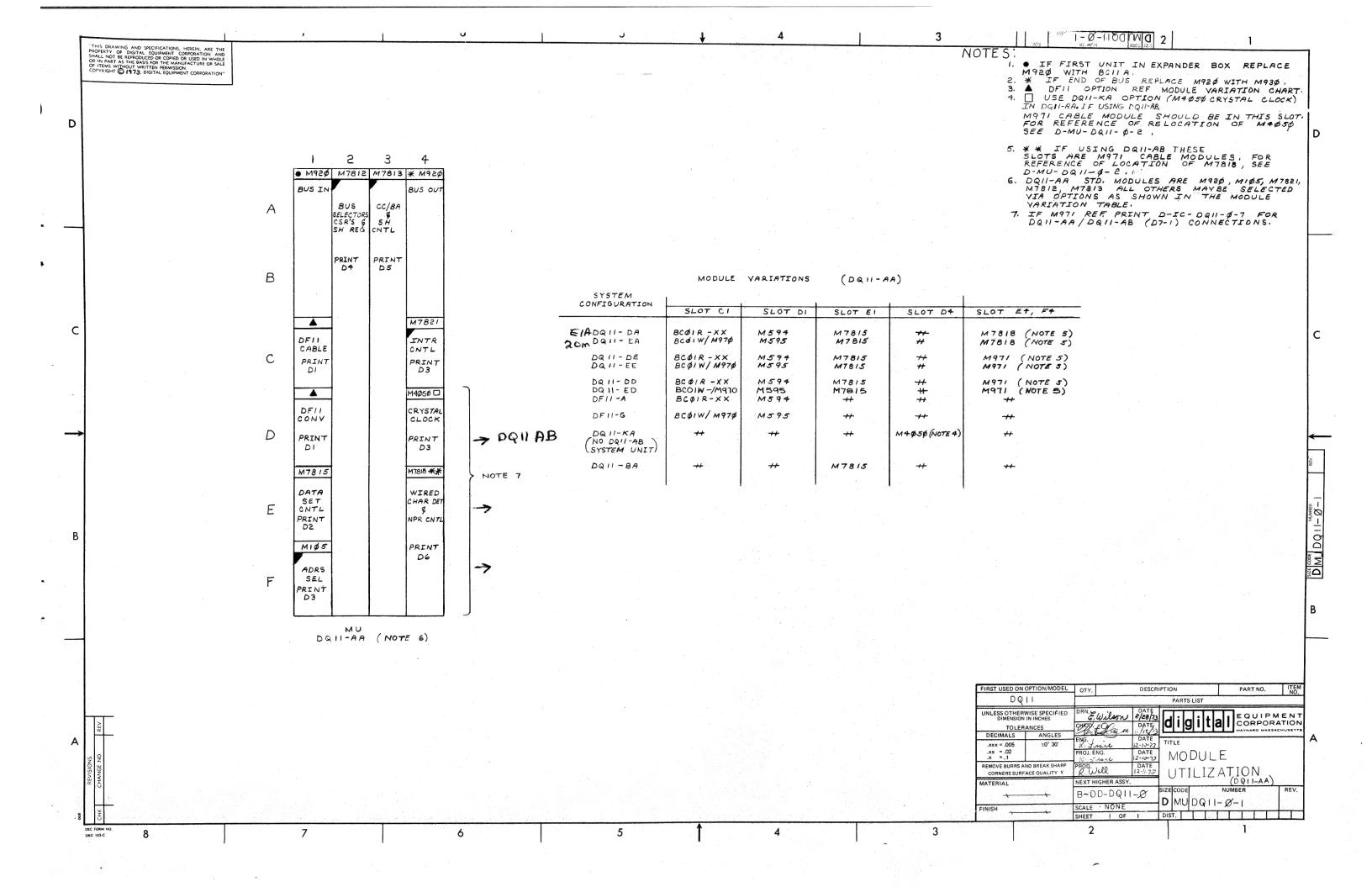
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DQ1-K		MFG. SET	FIND NO.	DRAWING NO.	RE	NO OF SHT	DESCRIPTION	OPTION NO./FILE DATE	Ø 4		MFG SET	ORAWING NO.	RE	NO OF SHT	DESCRIPTION	OPTIO NO. / FI DATE
			1	D-UA-DQ11-0-0			NPR SYNC LINE CONTROL		X	11	1	5 C-IA-7009468-0-0	*	1	WIRED ASSY (DQ11-AB)	
\perp	\perp			A-PL-DQ11-Ø-Ø		1	NPR SYNC LINE CONTROL		С		7	K-WL-DQ11-Ø-14		1	WIRE LIST (DQ11-AB)	
\sqcup		Ш		D-MU-DQ11-Ø-1		1	MODULE UTILIZATION (AA)		X			A-WT-7009468-0	#	-	AWT STATUS REV	†
-		\sqcup		D-MU-D011-Ø-2	*		MODULE UTILIZATION (AB)									+
++	-			A-AL-DQ11-Ø-3	A		DQ11 ACCESSORY LIST									
\vdash	-	\sqcup		B-DD-DF11-Ø	#		LEVEL CONVERTERS + CABLES									1
-		+		D-BD-DQ11-Ø-4	*		DF11 CONV/CABLE									
+		\vdash		D-BS-D011-Ø-5	*		ADDRESS INTERRUPT CLOCK			$\perp \perp$						
+		H		D-IC-DQ11-0/-6	*		UNIBUS CONNECTORS			\perp						
+		+		D-IC-D011-Ø-7	A		DO11-AA/DO11-AB CONNECTORS		X	\perp		6 D-CS-M7815-Ø-1	#	2	DATA SET CNTL	
-		╁╌┧		A-SP-DQ11-Ø-8	*		ENGINEERING SPEC	-		\perp	\perp	K-CO-M7815-Ø-4		1	X-Y COORDINATE HOLE LOCATION	I
	+-	\vdash		A-SP-DQ11-Ø-9	*		TEST PROCEDURE	-		\perp		D-AH-M7815-Ø-5		1	ASSY/HOLE LOCATION	
-		+		A-SP-D011-Ø-10	*	+	ACCEPTANCE PROCEDURE	-		\perp	_	B-MH-M7815-Ø-6		1	MODULE ECO HISTORY	
-	+	+	-	D-CS-M92Ø-Ø-1	#		INTERNAL BUS CONN									
-	+	H		D-CS-M105-Ø-1		1	ADDRESS SELECTOR	-		+						
X		H		D-CS-M7821-Ø-1		2	INTERRUPT CONTROL MODULE		X	+		7 D-CS-M7818-Ø-1	#	4	WIRED CHAR DET & NPR CNTL	
^ +	-	\vdash		D-CS-M4050-Ø-1		1	CRYSTAL CLOCK			+		K-CO-M7818-Ø-4		11	X-Y COORDINATE HOLE LOCATION	
-	-	H		D-CS-H315-Ø-1	#	1	MODEM TEST CONN			\perp	_	D-AH-M7818-Ø-5		1	ASSY/DRILLING HOLE LAYOUT	
\perp												B-MH-M7818-Ø-6	+	1	MODULE ECO HISTORY	
					+				X		\dashv	B D-CS-M7812-Ø-1	#	10	BUS, SEL CSR'S, SH REG	
				C-IA-BC08 5 -Ø-Ø	#		I/O CABLE ASSY					K-CO-M7812-Ø-4		1	X-Y COORDINATE HOLE LOCATION	
				D-IA-7009563-Ø-Ø	#	i	DAII- FODII-B OPTION HARNESS					D-AH-M7812-Ø-5		1	ASSY/DRILLING HOLE LAYOUT	
+	-	H		D-CS-M971-Ø-1	#_	-	CABLE CONNECTOR			\Box		В-МН-М7812-Ø-6		1	MODULE ECO HISTORY	(
+	+	H	-	A-SL-D011-Ø-11	*		SOFTWARE LIST	1		++	\dashv		_	-		-
	+			A-SP-D011-Ø-13	*		MODULE TEST PROCEDURE	+	X	++	-	D-CS-M7813-0-1	#	10	CC/BA & SHIFT CNTL	
	_	\Box		A-SP-DQTT-V-TS	+"-	1	MODULE TEST PROCEDURE	1		+	+	K-CO-M7813-Ø-4	— π	1	1	+
					1			-	+ +	+	-	D-AH-M7813-Ø-5		-	X-Y COORDINATE HOLE LOCATION	
		\Box	2	C-IA-7009467-0-0	B	1	WIRED ASSY(DQ11-AA)				+	B-MH-M7813-Ø-6		+ +	ASSY/DRILLING HOLE LAYOUT MODULE ECO HISTRY	+
+				K-WL-DQ11-Ø-12		1	WIRE LIST (DQ11-AA)			++	-	P-MH-M/813-8-8		1	MODULE ECO HISIRI	+
_				A-WT-7009467-0			AWT STATUS REV			++	_		-	 		-
					+"				x	11	1	0 D-CS-M7816-Ø-1	#	7	AB BUS SEL & BCC CNTL	+
			3	D-IA-7009152-0-0	#	1	BACK PLANE ASSY			++	-1	K-CO-M7816-Ø-4		1 1	X-Y COORDINATE HOLE LOCATION	1
				C-SC-1209583-0-1	1	i	CASTING THREE BLOCK			++		D-AH-M7816-Ø-5		1	ASSY/DRILLING HOLE LAYOUT	+
										+++	\top	B-MH-M7816-Ø-6		十十	MODULE ECO HISTORY	-
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_	4-4	\vdash		K-CO-5410094-0-4	+	1	X-Y COORDINATE HOLE LOCATION	 		+	\perp	K-CO-M7817-Ø-4		1	X-Y COORDINATE HOLE LOCATION	-
	+	\sqcup		E-AH-5410094-0-5	+	1	ASSY/DRILLING HOLE LAYOUT			11	_	D-AH-M7817-Ø-5		1	ASSY/DRILLING HOLE LAYOUT	4
	+	\vdash		B-MH-5410094-0-6	+	1	MODULE ECO HISTORY			++	+	B-MH-M7817-Ø-6		1	MODULE ECO HISTORY	+
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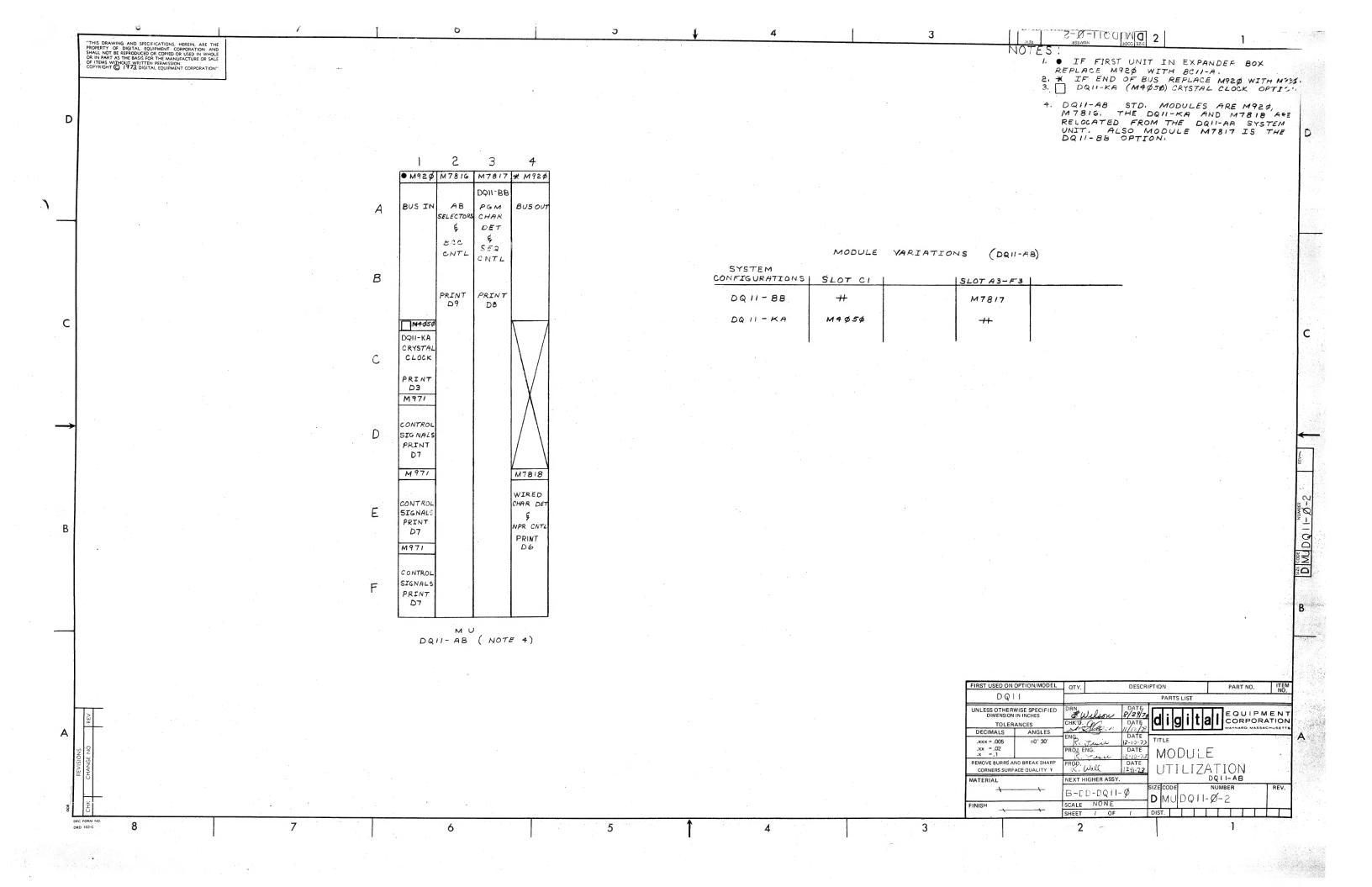
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		+	1	D-UA-DQ11-Ø-Ø	A	1	NPR SYNC LINE CONTROL					+					
				A-PL-D011-Ø-Ø		1	NPR SYNC LINE CONTROL (PL)	<u> </u>		_		-+		+-			-
				D-UA-BC01R-0-Ø			MODEM CABLE					1					
				D-UA-BC01W-25-Ø			MODEM CABLE (301/303)										
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+	_	+	2	C-IA-7009467-0-0	R	1	WIRED ASSY (DQ11-AA)				-	+		+			
		1				+ -	WIND ADDI (DQII-AA)				+			+			-
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			3	D-IA-7009152-0-0	#	1	BACK PLAIN ASSY										
+-				C-SC-1209583-0-1	ļ	1	CASTING THREE BLOCK					_		\perp			
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+	_	+	4	C-IA-7009468-0-0	++	1	WIDED ACCY (DOLL 2D)				-	+		-			
	-	+	4	C-1A-700940 C -0-0	11	1 1	WIRED ASSY (DQ11-AB)		$1 \rightarrow$		+	+		+			
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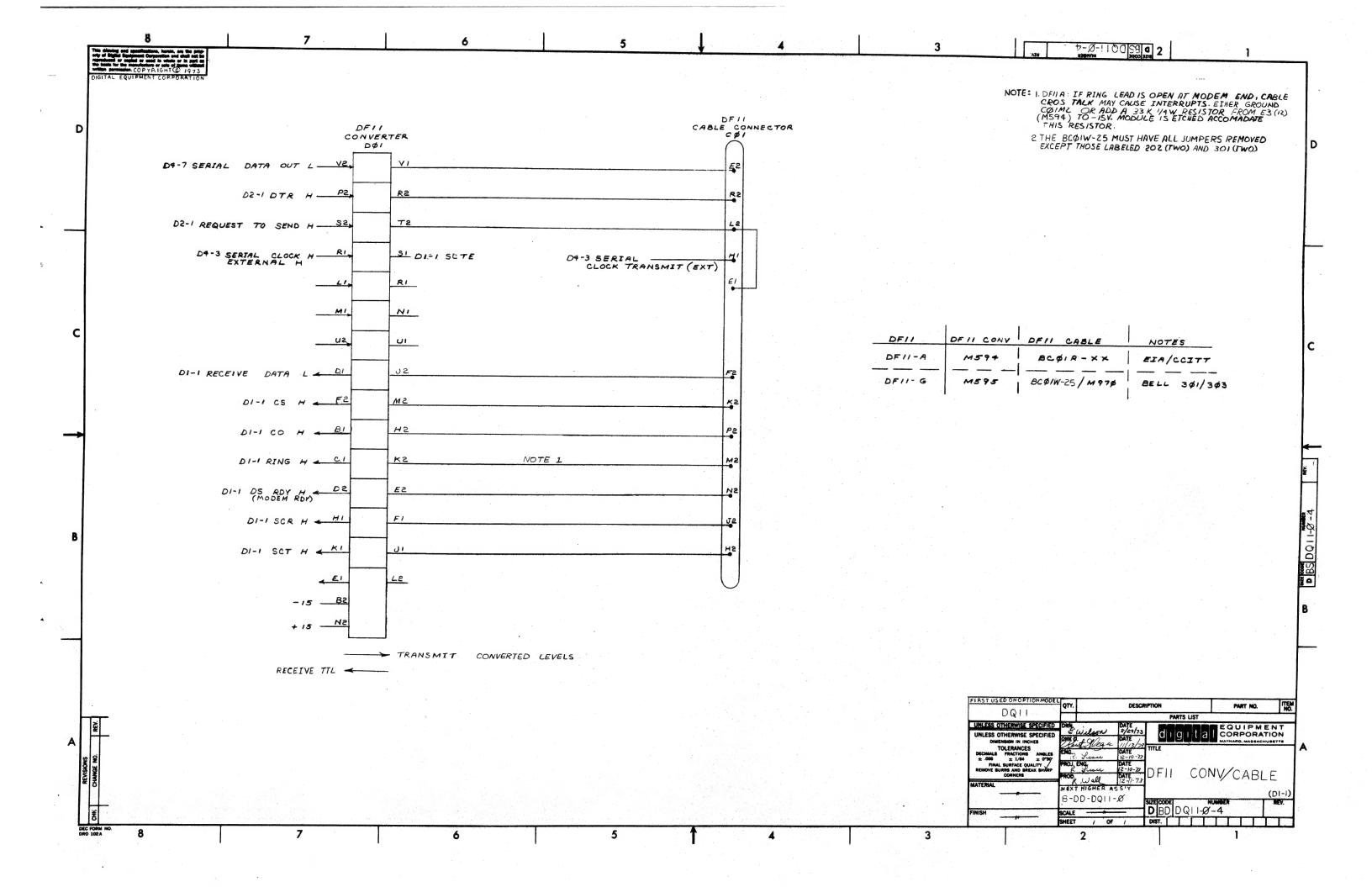


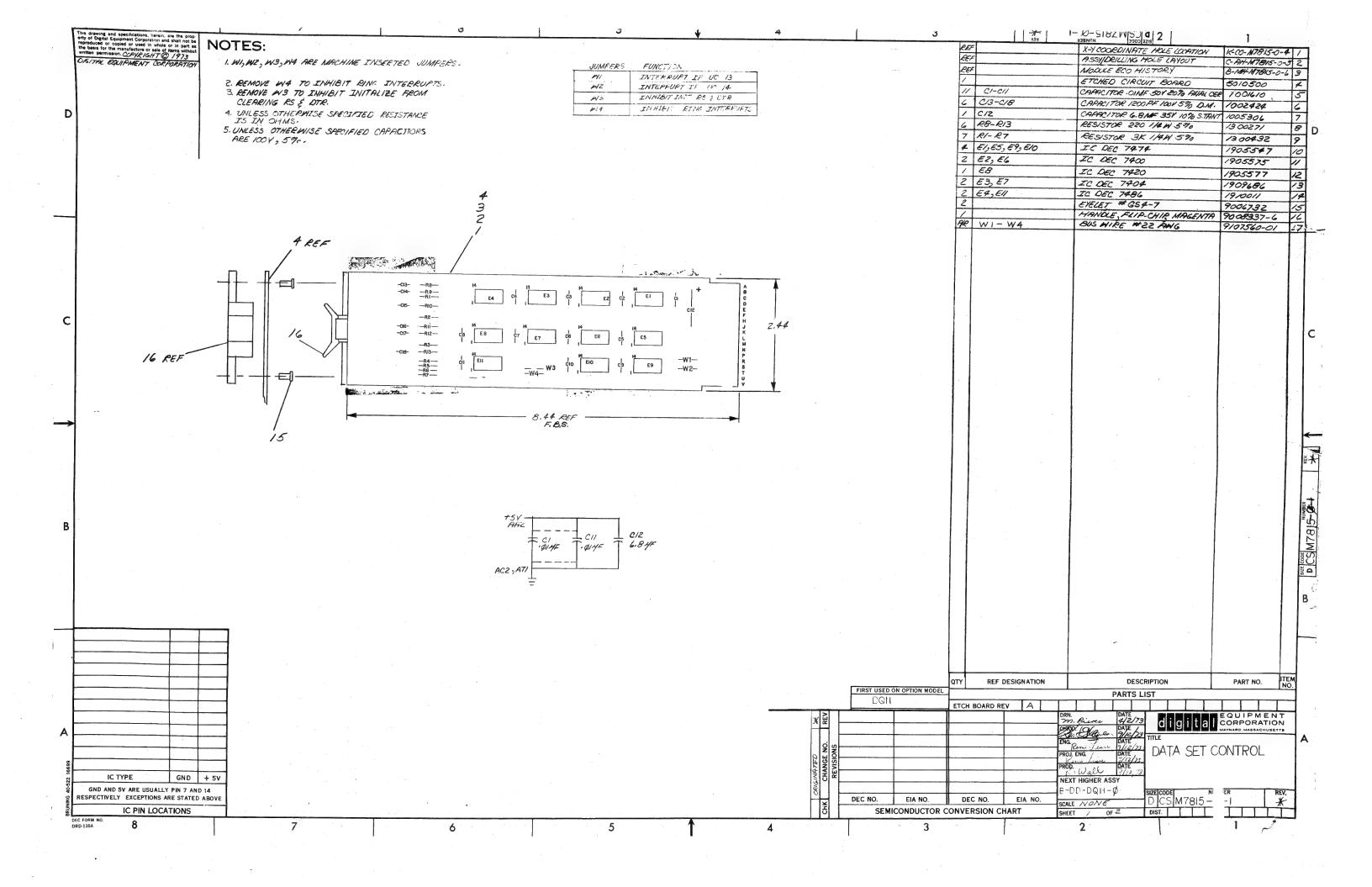
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DAT		PROD (ISSUED S	FOT	AA	BA	AB	1-BB	I-KA		I-DA	I-EA	DE	H	7	ш
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ITEM NO.	DWG NO. / PART NO.	DESCRIPTION		0 О	000	DО	30	00		DO	ÒО	Ø	0	DO	00
1	C-IA-7009 467- 0-0	WIRED ASSY (DQ11-AA)		1						1	1	1	1	1	1
2	C-IA-7009468-0-0	WIRED ASSY (DQ11-AB)				1						1	1	1	1
3	D-CS-M920-0-1	INTERNAL BUS CONN		1		1				1	1	2	2	2	2
4	D-CS-M105-0-1	ADDRESS SELECTOR		1						1	1	1	1	1	1
5	D-CS-M7821-0-1	INTERRUPT CONTROL MODULE		1			·			1	1	1	1	1	1
6	D-CS-M4050-0-1	CRYSTAL CLOCK						1							
7	D-CS-M7815-0-1	DATA SET CNTL			1		·			1	1	1	1	1	1
8	D-CS-M7818-0-1	WIRED CHAR DET & NPR CNTL		1						1	1	1	1	1	1
9	D-CS-M7812-0-1	BUS SEL, CSR'S, SH REG		1						1	1	1	1	1	1
10	D-CS-M7813-0-1	CC/BA & SHIFT CNTL		1						1	1	1	1	1	1
11	D-CS-M7816-0-1	AB BUS SEL & BCL CNTL				1						1	1	1	1
12	D-CS-M7817-0-1	PGM CHAR DET & SEQ CNTL					1					1	1		
13	D-CS-M971-0-1	CABLE CONNECTOR				6						6	6	6	6
14	C-IA-BC08 5 -1	I/O CABLE ASSY				3						3	3	3	3
15	B-DD-DF11-A	EIA/CCITT CONV-CABLE								1		1		1	
16	B-DD-DF11-G	301/303 CONV- CABLE									1		1		1
17	D-CS-H315-0-1	MODEM TEST CONNECTOR								1		1		1	
18	D-PS-12-11284-04	SW DUST COVERS (8 POS)		9						9	9	9	9	9	9
19	C-IA-5408778-0-0	PRIORITY JUMPER LEVEL #5		1						1	1	1	1	1	1
20	D-TA-7009099	POWER HARNESS		1		1				1	1	2	2	2	_2
21	D-IA-7009563-0-0	DALI-F DDII-B OPTION HARNESS		1		1						2	2	2	2
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TITL	E NPR SYNC LINE CO		SIZE C	PL		DQ1	N 1-0	имв -0	ER			1 .	EV. Δ	EC 0 DQ 000	NO. 01
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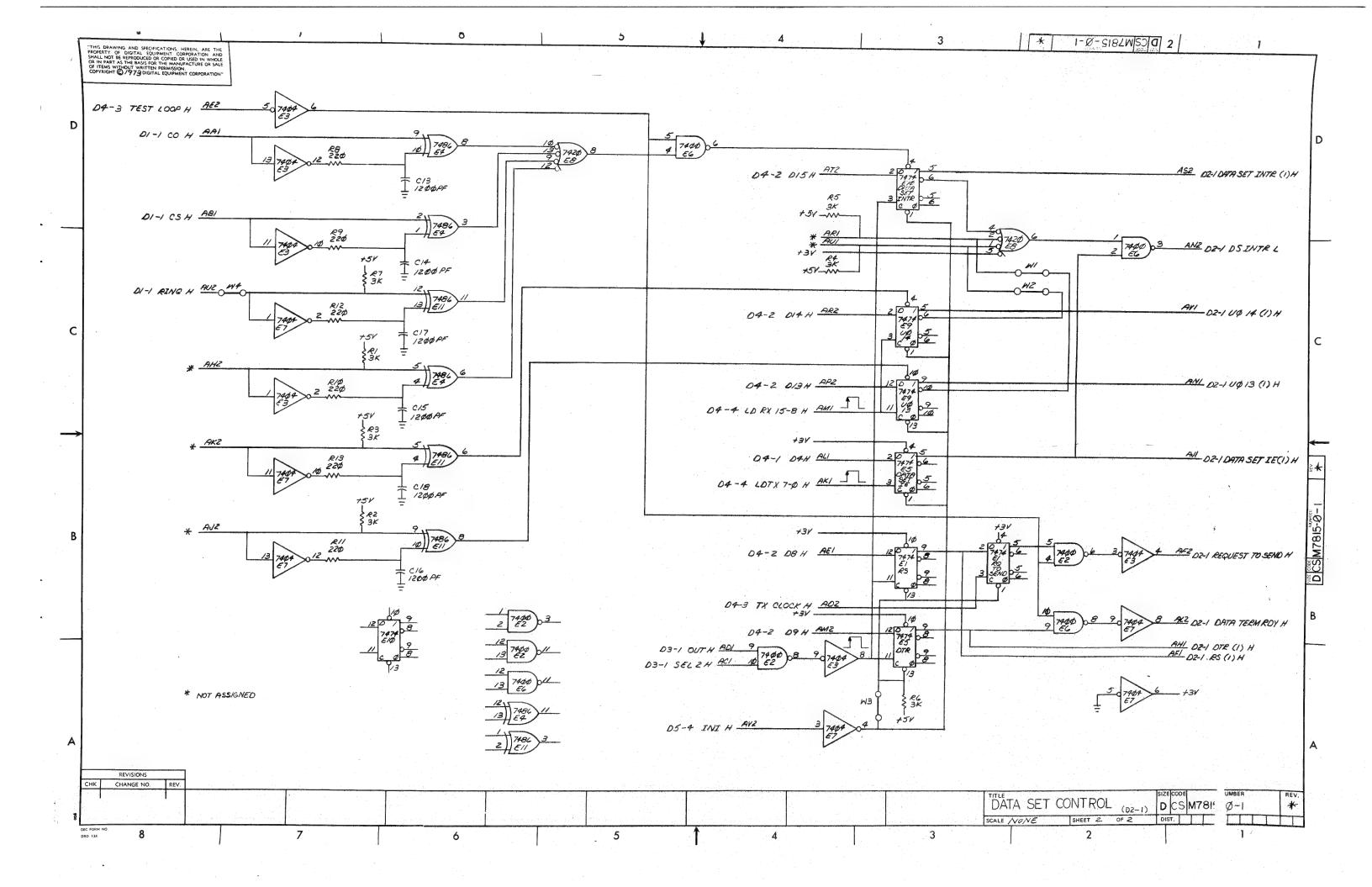
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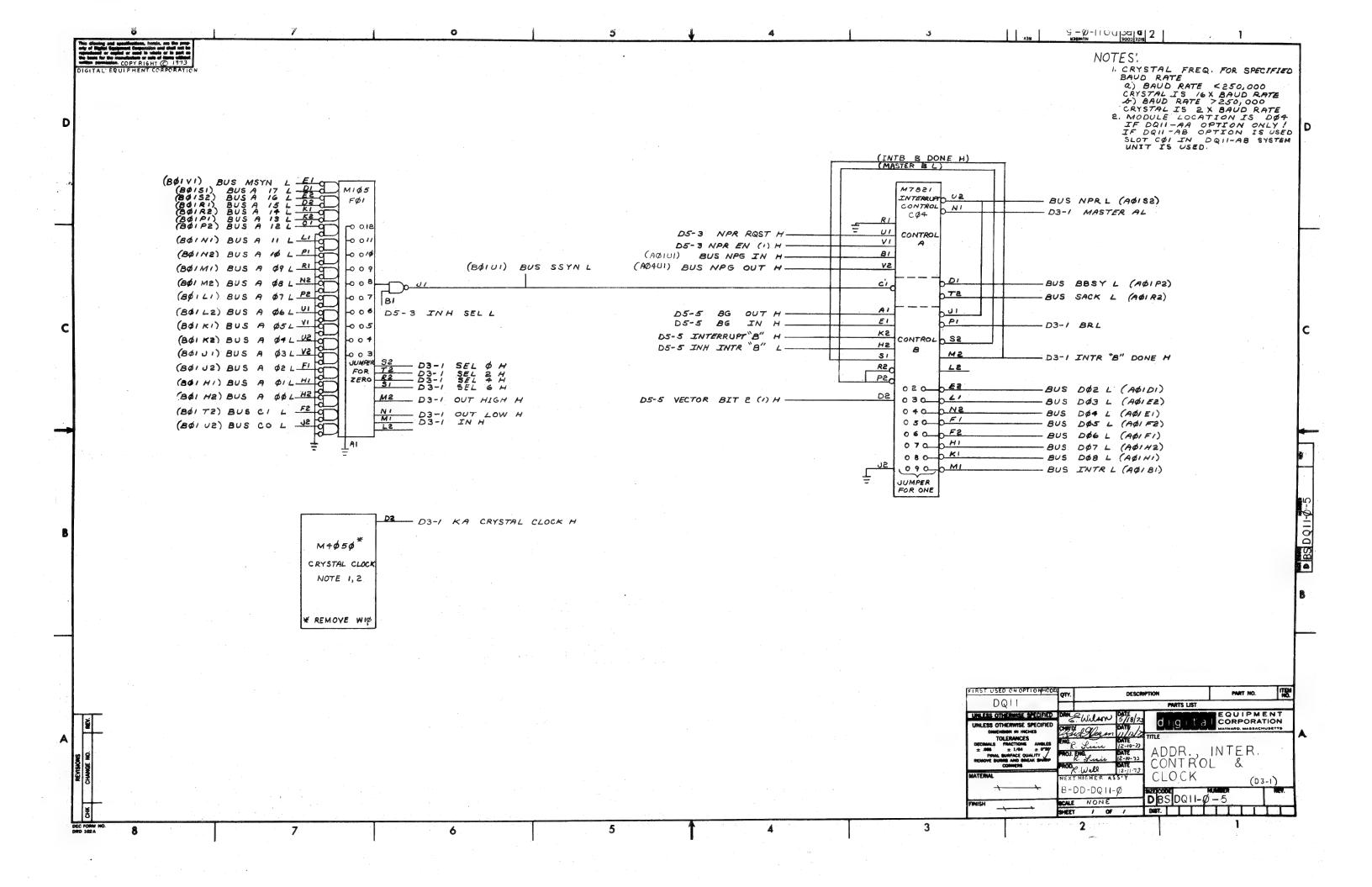


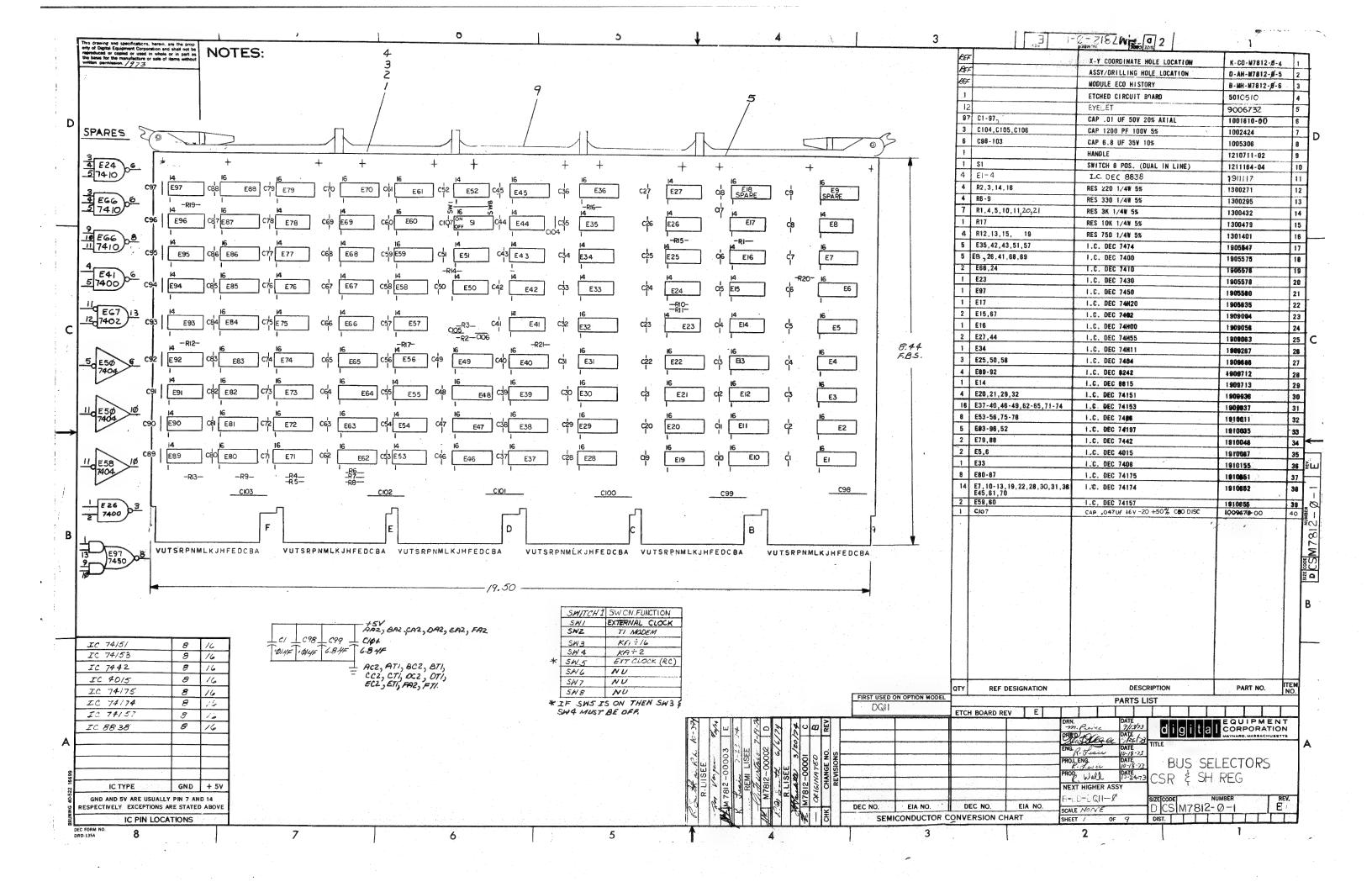




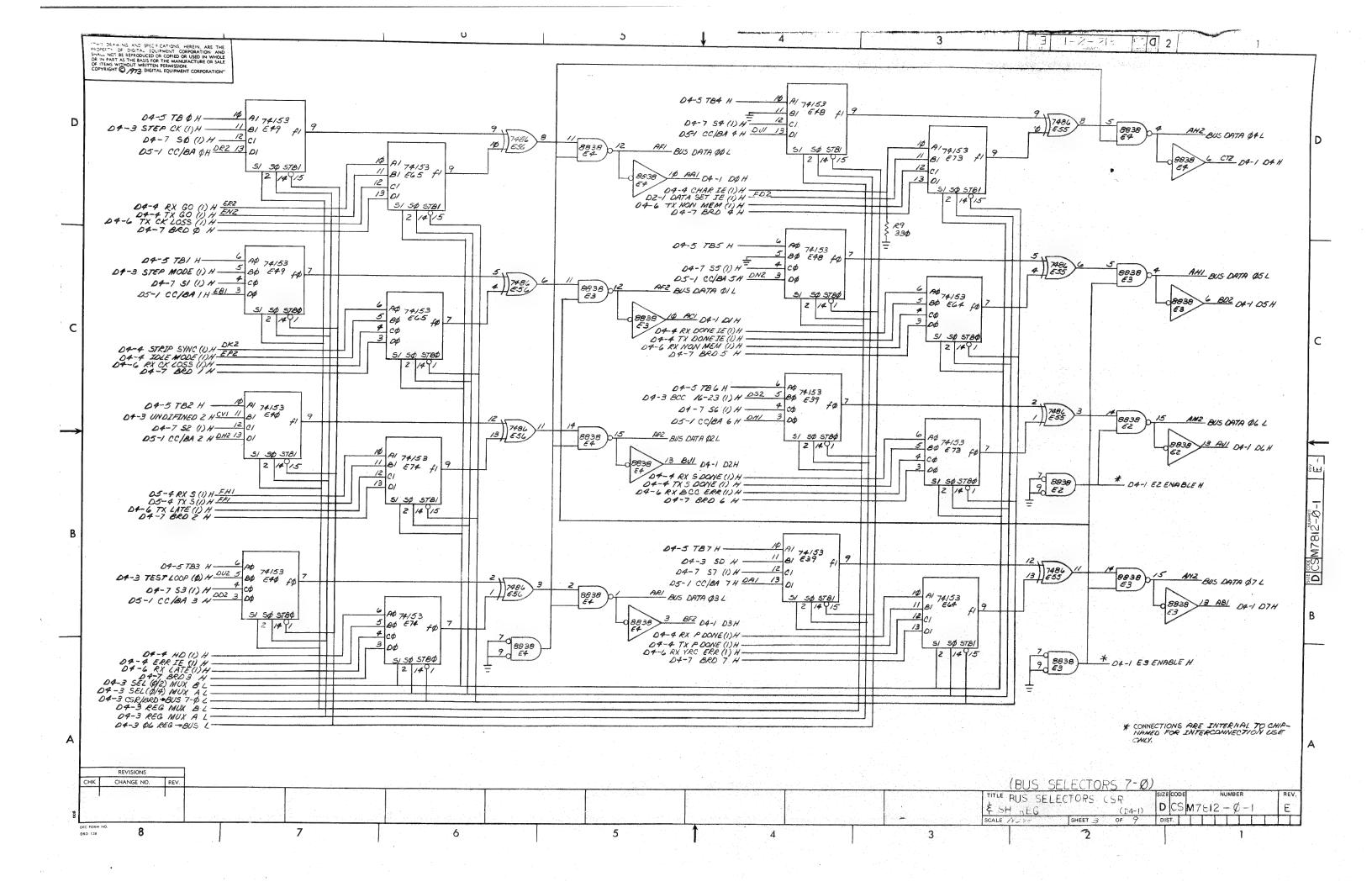


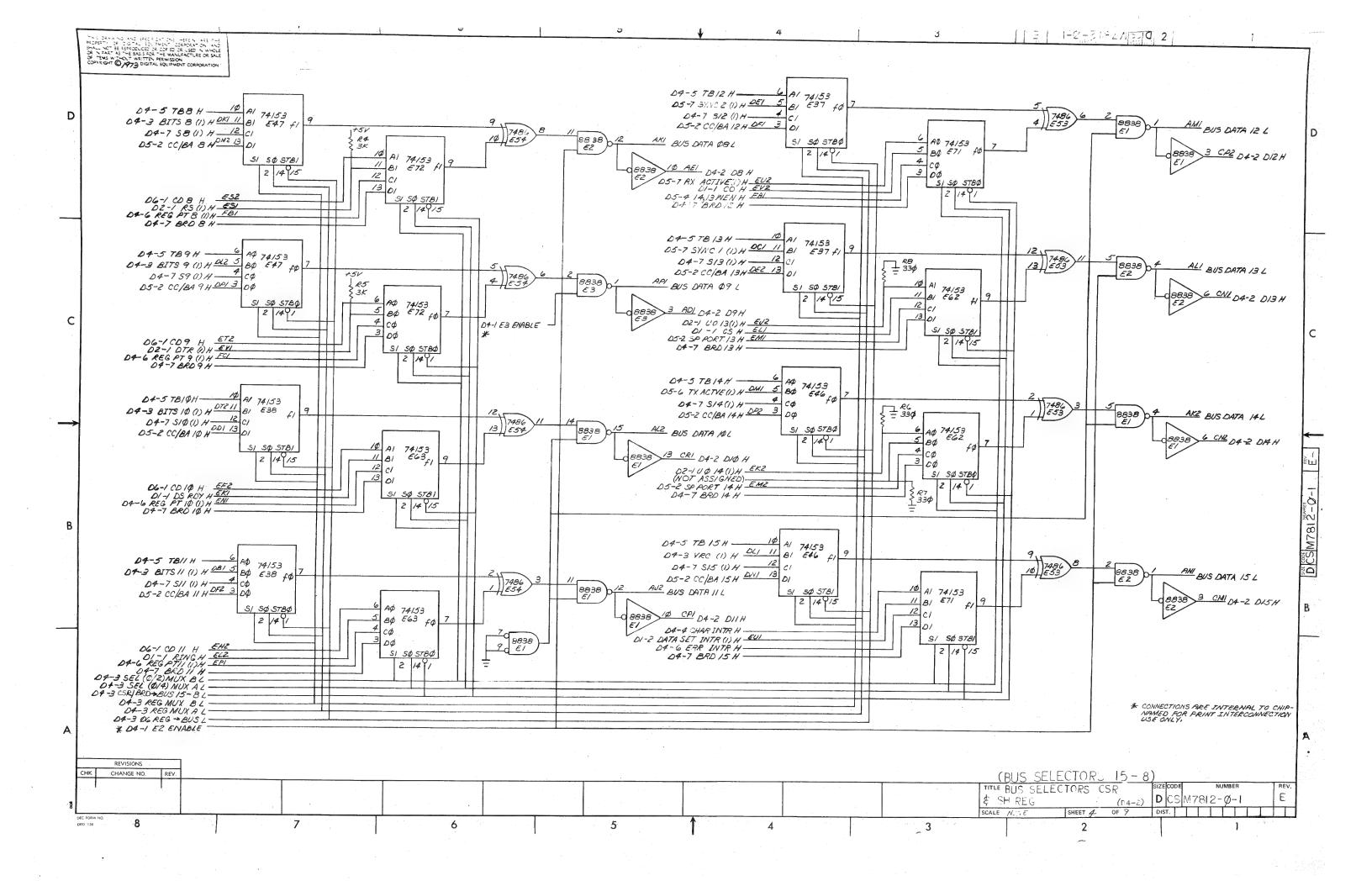


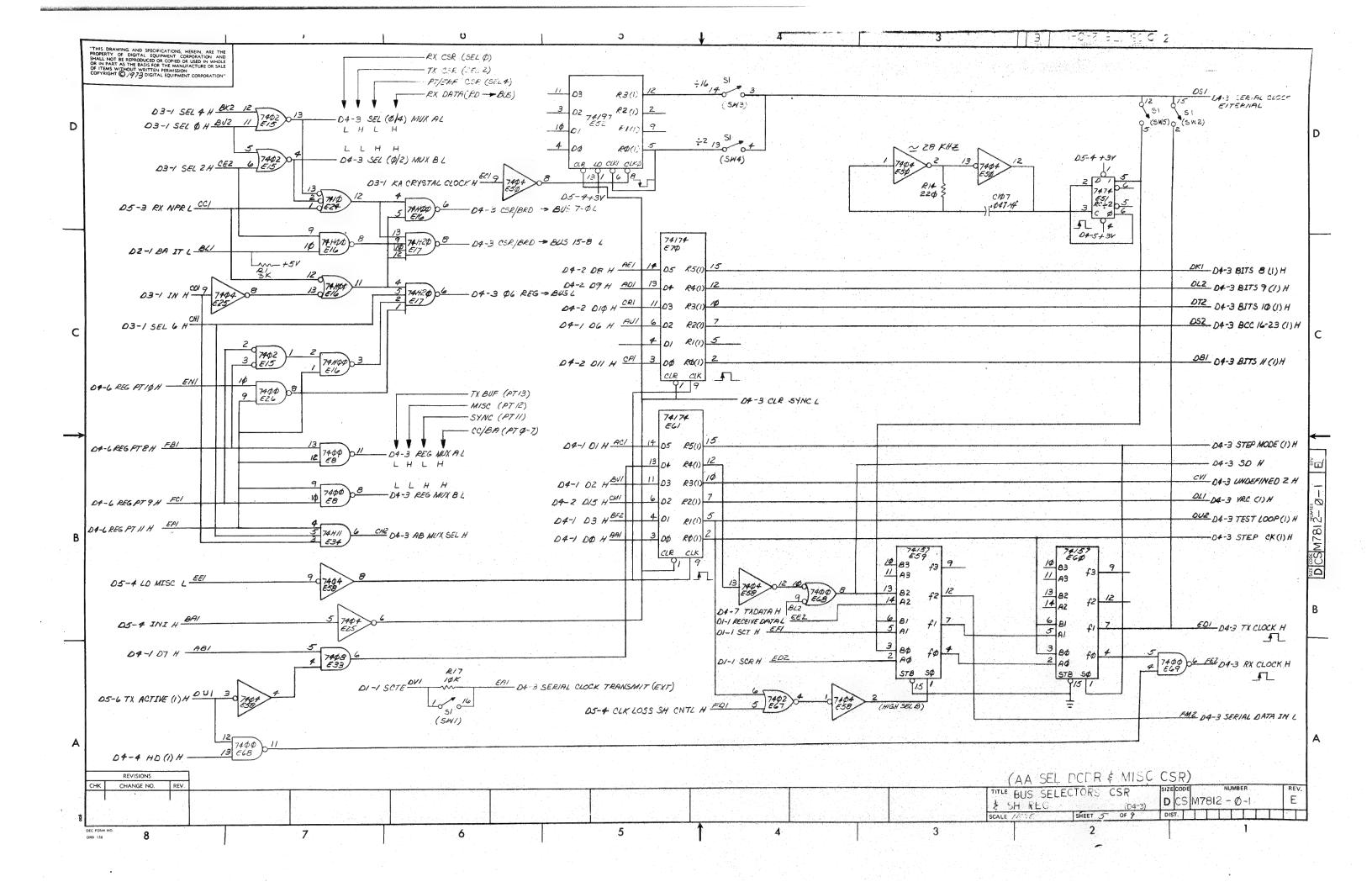


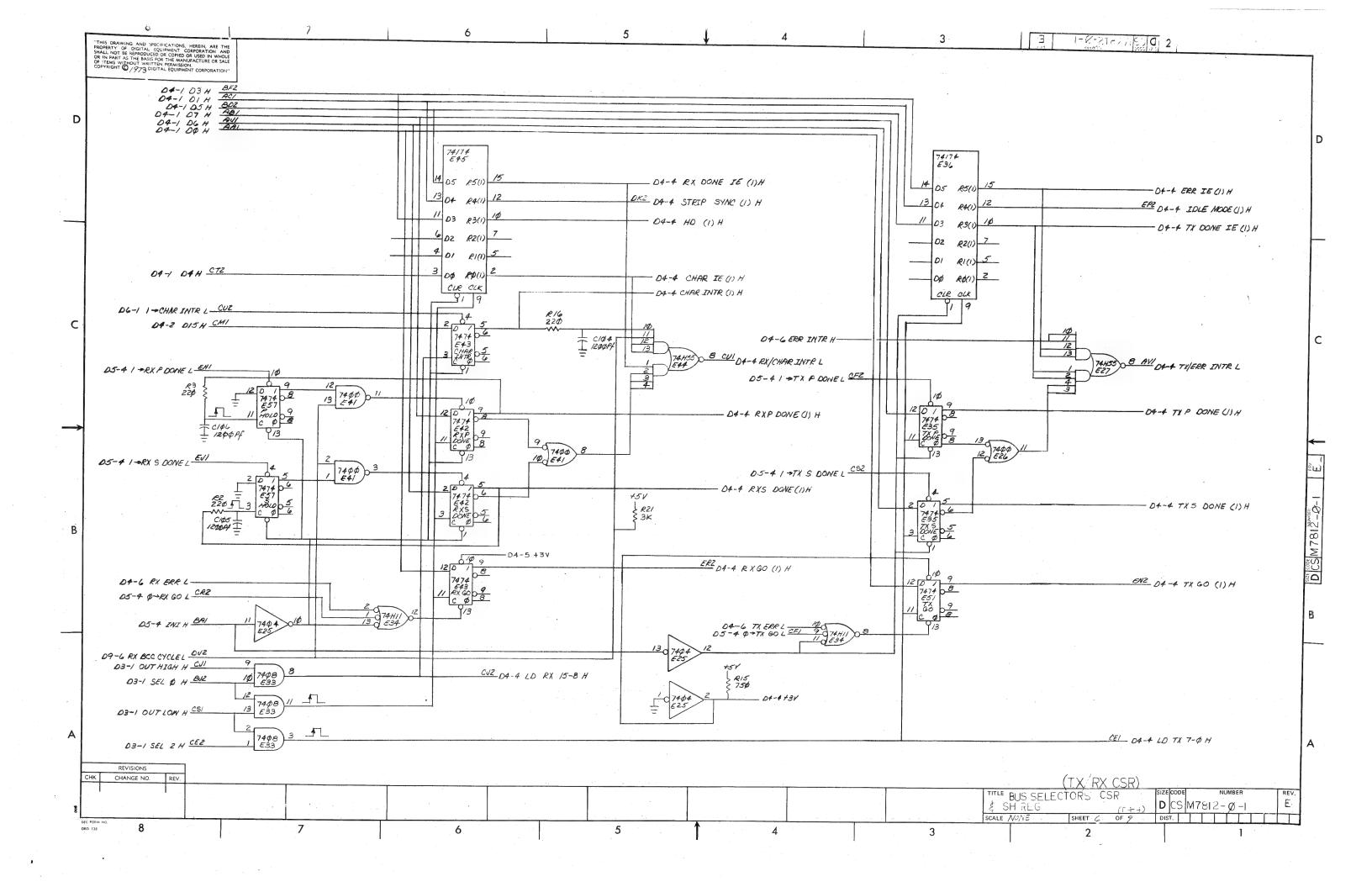


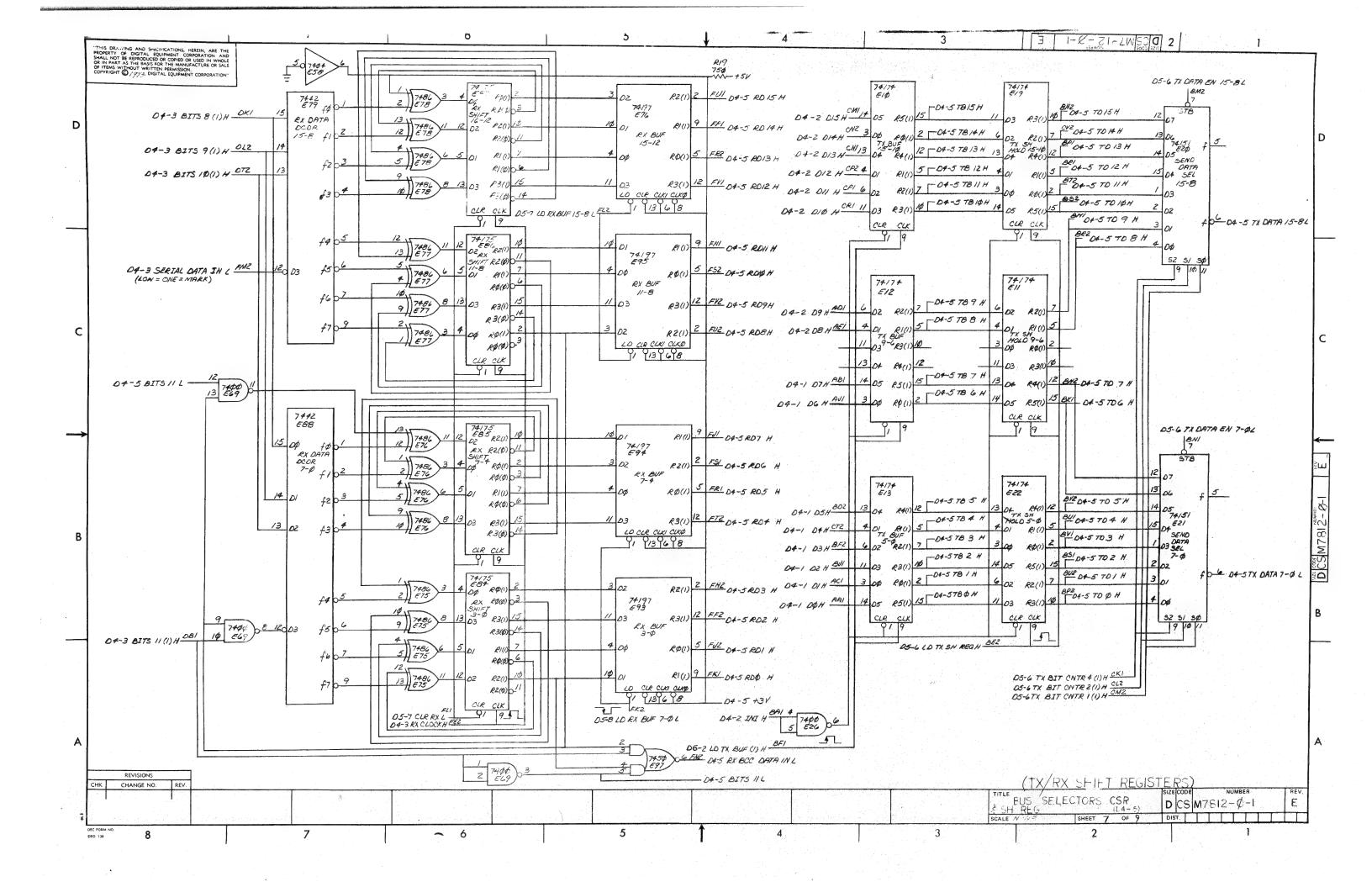
PIN SIGNAL NAME PAGE AF1 BUS DATA 99 L AF2 BUS DATA 91 L AP2 BUS DATA 92 L D AR1 BUS DATA 93 L	X AV1 D. X D.	I-4 TX ERR INTR L I-4 TX P DONE (1) H I-4 TX S DONE (1) H		PAGE - 7 4 6 5 7 8 9		
AH2 BUS DATA BA L AH1 BUS DATA B5 L AM2 BUS DATA B6 L AN2 BUS DATA B6 L AN2 BUS DATA B6 L AN1 BUS DATA B6 L AP1 BUS DATA B L AP1 BUS DATA B L AP2 BUS DATA B L AP3 BUS DATA B L AP4 BUS DATA B L AP4 BUS DATA B L AP4 BUS DATA B L AP5 BUS DATA B L AP6 BUS DATA B L AP7 BUS DATA B L	X	I-4 TX GO (1) Å -5 RO 15 H -5 RO 14 H -5 RO 13 H -5 RO 11 H -5 RO 10 H -5 RO 9 H -5 RO 7 H	X X X X X X X X X X X X X X X X X X X			
AM1 BUS DATA 12 L AL1 BUS DATA 13 L AK2 BUS DATA 14 L AN1 BUS DATA 15 L EL2 D1-1 RING H EV2 D1-1 CO H EL1 D1-1 CS H EU1 D1-1 DS INTR (1) H DV1 D1-1 SCTE EE2 D1-1 RECEIVE DATA L EF1 D1-1 SCT H ED2 D1-1 SCT H ED2 D1-1 SCT H	X	-5 RD 6 H -5 RD 5 H -5 RD 4 H -5 +3 Y -5 RD 3 H -5 RD 2 H -5 RD 1 H -5 RD 0 H -5 RX BCC DATA IN L -5 BITS 11 L -5 BITS 15 H	X X X EBB DH2 D5-1 CC/BA B H EBB D5-1 CC/BA 1 H DH2 D5-1 CC/BA 2 H DD2 D5-1 CC/BA 3 H DD2 D5-1 CC/BA 4 H X DD1 D5-1 CC/BA 5 H DH1 D5-1 CC/BA 5 H DH1 D5-1 CC/BA 6 H	X X X X X X X X X X X X X		
EKI D1-1 DS RDY H FD2 D2-1 DATA SET IE (1) H ES1 D2-1 RS (1) H EVI D2-1 OTR (1) H EJ2 D2-1 U.O. 13 (1) H EK2 D2-1 U.O. 14 (1) H BL1 D2-1 BA IT L CH1 D3-1 SEL 6 H BJ2 D3-1 SEL 6 H CE2 D3-1 SEL 7 H	X X D4 D4 X D4 X X D4 D4 D4 X D4	-0 16 10 H -5 TB 13 H -5 TB 12 H -5 TB 12 H -5 TB 11 H -5 TB 10 H -5 TB 8 H -5 TB 8 H -5 TB 6 H	X X X DM2 D5-2 CC/BA 8 H DP1 D5-2 CC/BA 9 H DP1 D5-2 CC/BA 10 H DF2 D5-2 CC/BA 11 H DF2 D5-2 CC/BA 12 H DF1 D5-2 CC/BA 12 H DF1 D5-2 CC/BA 12 H DF1 D5-2 CC/BA 13 H DF2 D5-2 CC/BA 14 H DF2 D5-2 CC/BA 14 H DF2 D5-2 CC/BA 14 H DF2 D5-2 CC/BA 15 H DF2 D5-2 CC/BA 15 H DF1 D5-2 SP PORT 14 H DN1 D5-2 SP PORT 14 H DN1 D5-2 CC/BA 15 H DF1 D6-2 LD TX BUF (I) L	X X X X X X X X X X X X X X X X X X X		
BK2 D3-1 SEL 4 H CD1 D3-1 IN H CJ1 D3-1 OUT HIGH H CS1 D3-1 OUT LOW H EC1 D3-1 KA CRYSTAL CLOCK H AA1 D4-1 D8 H AC1 D4-1 D1 H BJ1 D4-1 D2 H BF2 D4-1 D3 H CT2 D4-1 D4 H BD2 D4-1 D5 H AJ1 D4-1 D6 H	X X X X X X BN2 D4 X X X X X X BN2 D4 X X X X X X X BP1 D4 X X X X X X X BR1 D4 X X X X X X X BR1 D4	5 TB 4 H 5 TB 2 H 5 TB 2 H 5 TB 1 H 5 TB 1 H 5 TB 1 H 5 TD 15 H 5 TD 14 H 5 TD 12 H 5 TD 12 H 5 TD 10 II	X		NOTES:	
AB1 D4-1 07 H AE1 D4-2 08 H AD1 D4-2 09 H CR1 D4-2 D19 H CP1 D4-2 D11 H CP2 D4-2 D12 H CN1 D4-2 D13 H CN1 D4-2 D13 H CN2 D4-2 D14 H CM1 D4-2 D15 H CM1 D4-2 D15 H CM1 D4-3 D5-1 (G/A) MIX AL	X	5 TD 9 H 5 TD 8 H 5 TD 7 H 5 TD 6 H 5 TD 5 H 5 TD 4 H 5 TD 2 H 5 TD 2 H 5 TD 1 H 5 TD 1 H	X		I. IF A NAME APPEARS TWO TIMES IT IS SHOWN WITH	
B CH2 D4-3 SEL(Ø/2) MUX B L D4-3 CSR/BRD→ BUS 7- Ø L D4-3 CSR/BRD→ BUS 15-8-L D4-3 Ø6 REG → BUS L D4-3 REG MUX A L D4-3 REG MUX B L D4-3 AB MUX SEL H D4-3 SERIAL CLOCK TRANSMIT EXT D4-3 CLR SYNC L D4-3 SERIAL CLOCK EXTERNAL	X X X X D4- X X X X D4- X X X X D4- D4- X X X X D4- D4- X X X X D4- D4- X X X X D4- D4- D4- D4- D4- D4- D4- D4- D4- D4-	5 TX DATA 15-8 L 5 TX DATA 7-0 L 6 TX CK LOSS (1) H 6 TX CK LOSS (1) H 6 TX NON MEM (1) H 6 RX CK LOSS (1) H 6 TX ERR L 6 RX ERR L 6 RX LATE (1) H 6 RX LATE (1) H 6 RX DATE (1) H	X X X X DM1 D5-6 TX ACTIVE (1) H D5-6 TX BIT CNTR 1 (1) H CM2 D5-6 TX BIT CNTR 2 (1) H CM2 D5-6 TX BIT CNTR 4 (1) H D5-6 TX SYNC EN 15-8 L CK2 D5-6 TX SYNC EN 15-8 L CK2 D5-6 TX SYNC EN 15-8 L CK2 D5-6 TX DATA EN 15-8 L D5-6 TX DATA EN 15-8 L			NIMIEE
DS2	X X X X X X X X X X X X X X X X X X X	3 RX BCC ERR (1) H 5 RX VRC ERR (1) H 5 RX VRC ERR (1) H 6 REG PT 8 (1) H 6 REG PT 10 (1) H 6 REG PT 10 (1) H 6 EE 13 (1) H 6 EE 14 (1) H 7 SIE RINTR H 7 SIS (1) H	X			3002 122/5
ED1 D4-3 TX CLOCK H FE2 D4-3 RX CLOCK H D4-3 SERIAL DATA IN L D4-4 +3V D4-4 +HD (1) H DK2 D4-4 STRIP SYNC (1) H D4-4 ERR IE (1) H D4-4 RX DONE IE (1) H D4-4 CHAR INTR (1) H D4-4 CHAR INTR (1) H	X X X X X X X X D4-D4-D4-D4-D4-D4-D4-D4-D4-D4-D4-D4-D4-D	7 S14 (1) H 7 S13 (1) H 7 S12 (1) H 7 S11 (1) H 7 S10 (1) H 7 S8 (1) H 7 S8 (1) H 8 S8 (1) H 8 S8 (1) H 9 S8 (1) H 9 S8 (1) H 9 S8 (1) H	X X X X X X X X X X X X X X X X X X X			
A CE1 D4-4 RX P DONE (1) H D4-4 RX S DONE (1) H D4-4 RX S DONE (1) H D4-4 LD RX 15-8 H D4-4 LD TX 7-0 H EP2 D4-4 IDLE MODE (1) H D4-4 TX DONE IE (1) H REVISIONS	X X X X D4- 04- 04- 04- 04- 04- 04- 04- 04- 04- 0	S3 (1) H S2 (1) H S2 (1) H S8 (1) H S8 (1) H SERIAL DATA OUT L TX DATA H BRD 15 H	X X X X X X X X X X X X			
CHK CHANGE NO. REV. DEC FORM NO. DRD 138.	7	6	5	4	(PRINT FIND) TITLE FUS SELECTORS CSR & H REG SCALE N2.6 SHEET 2 OF 9 DIST. 3	NUMBER REV. M7812-Ø-1 E.

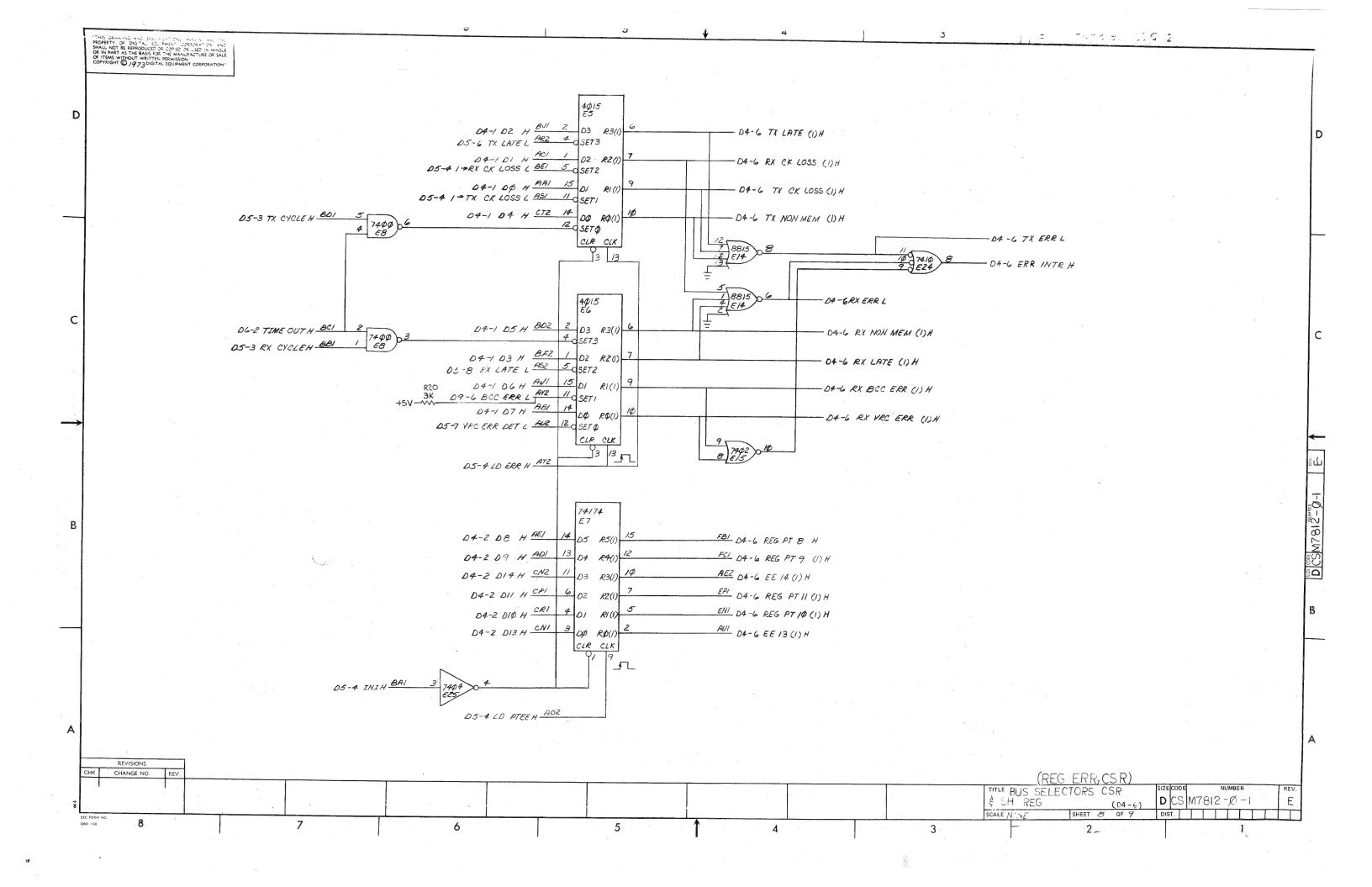


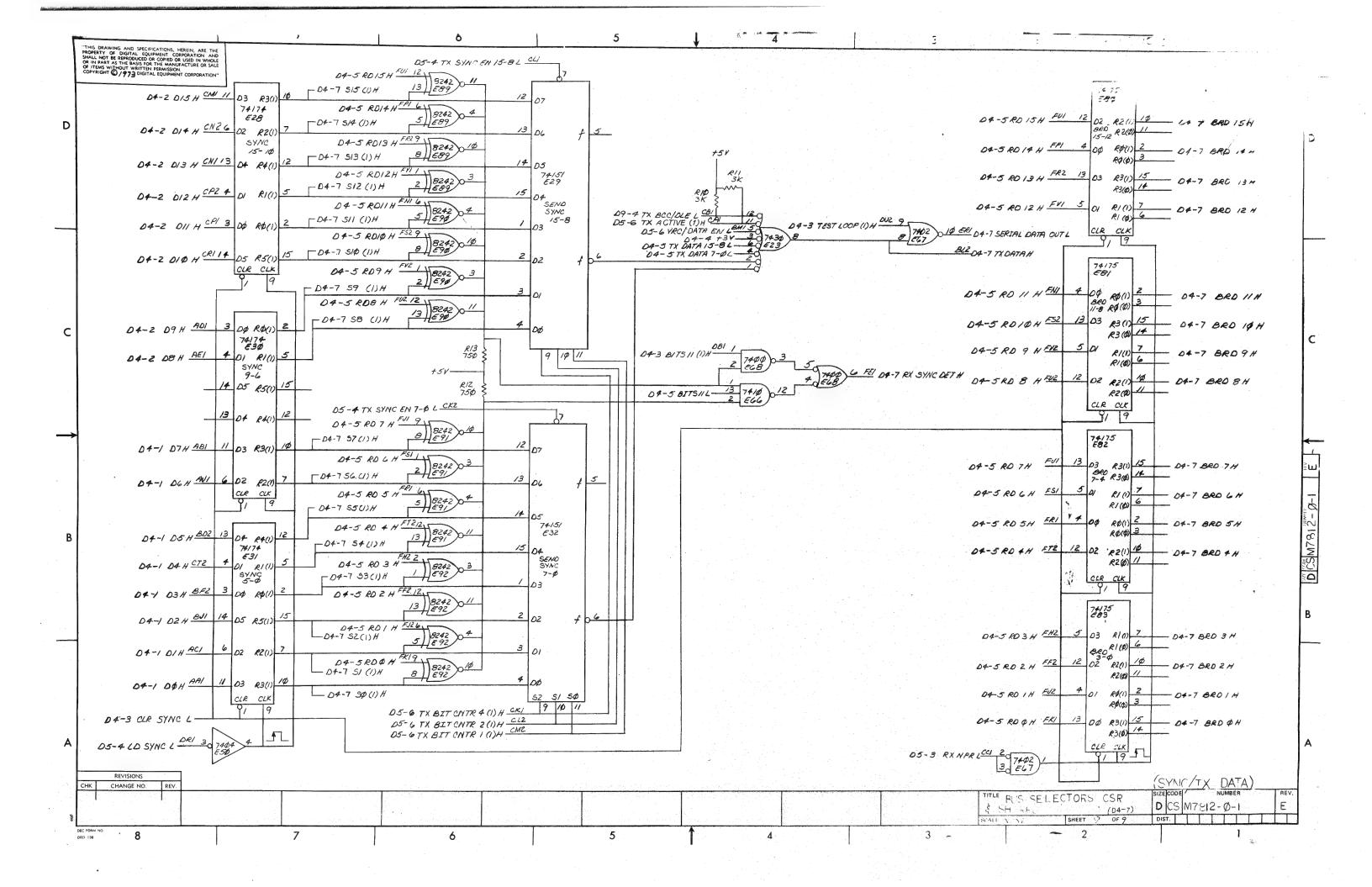


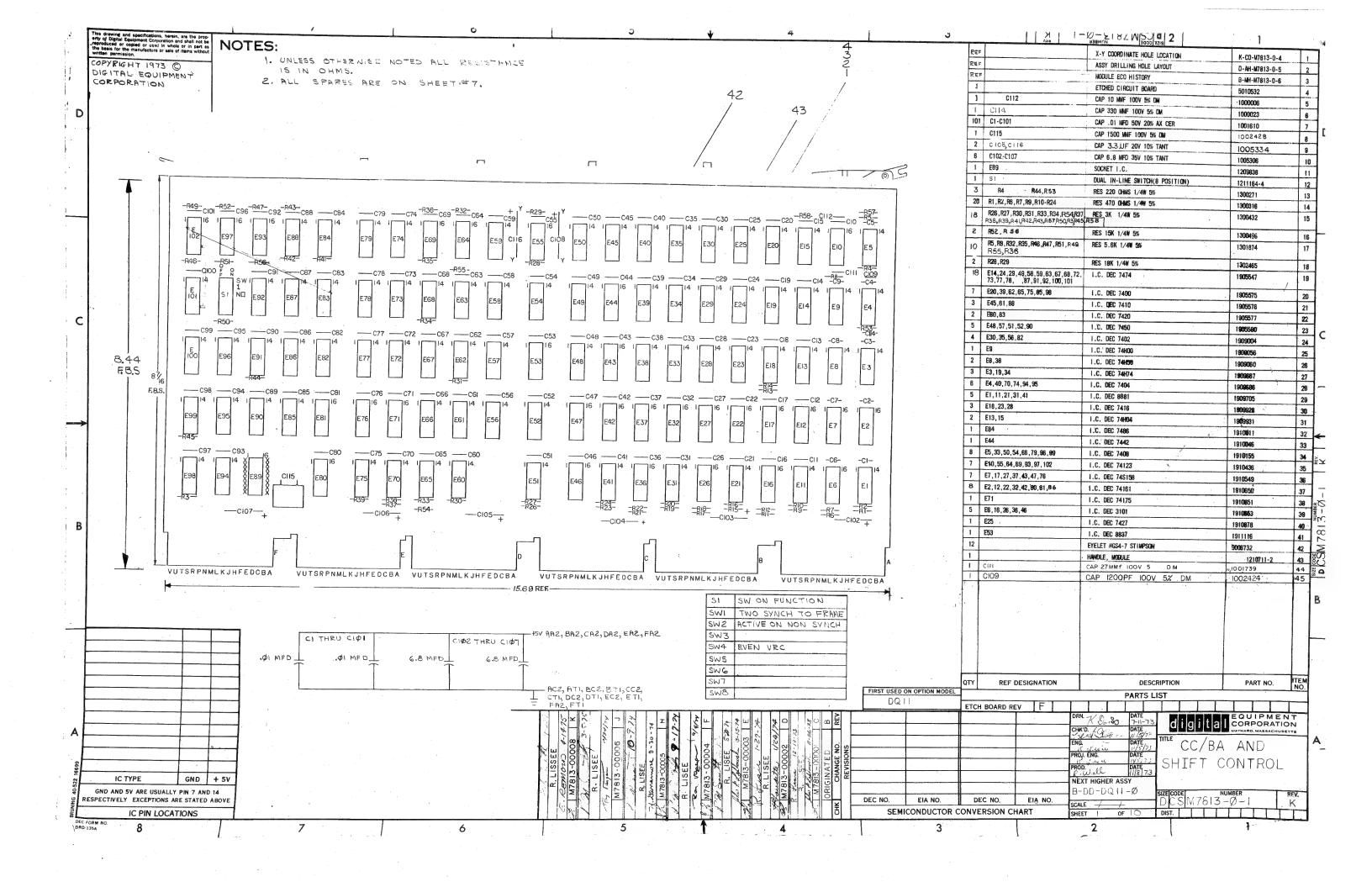




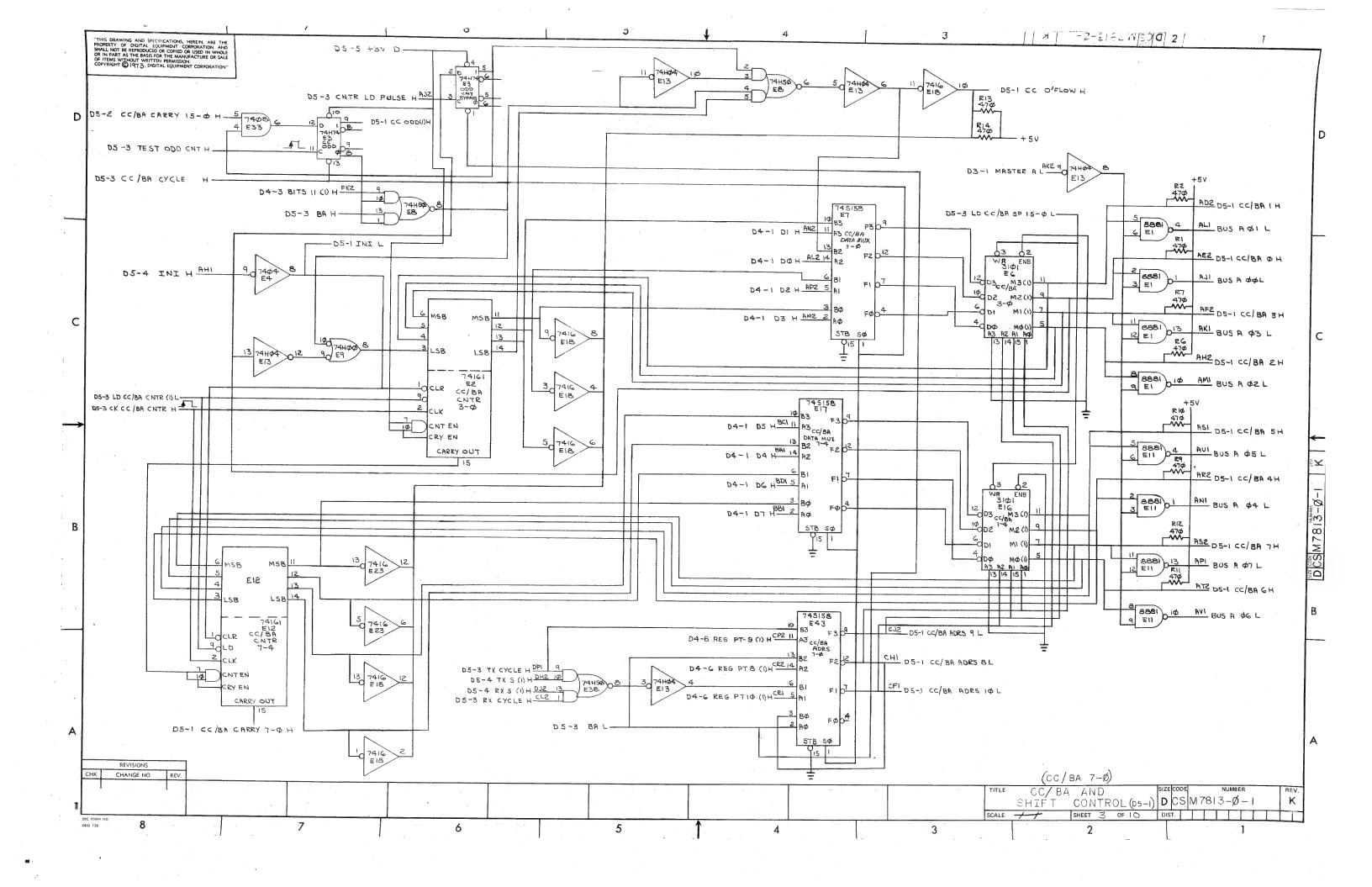


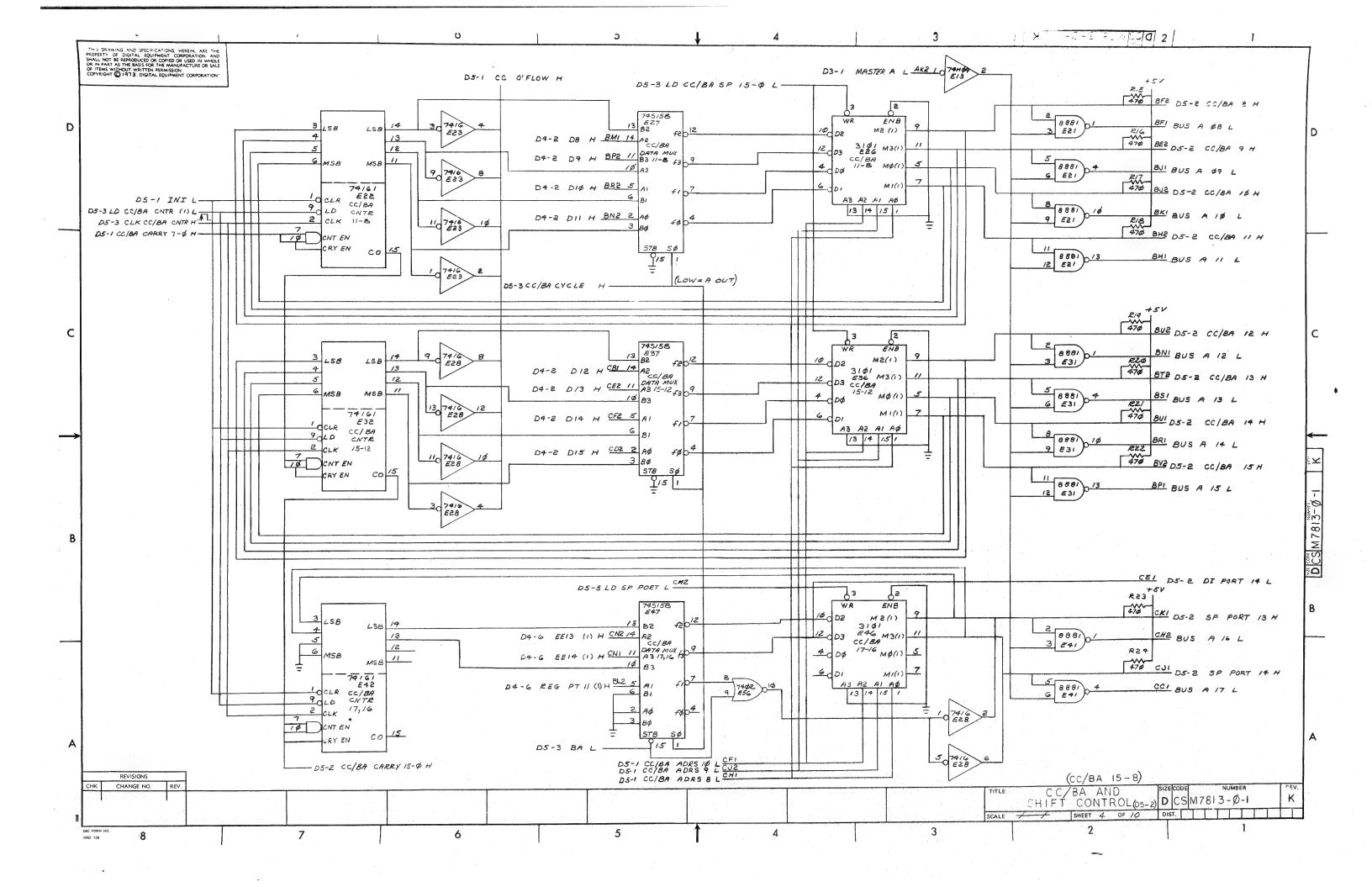


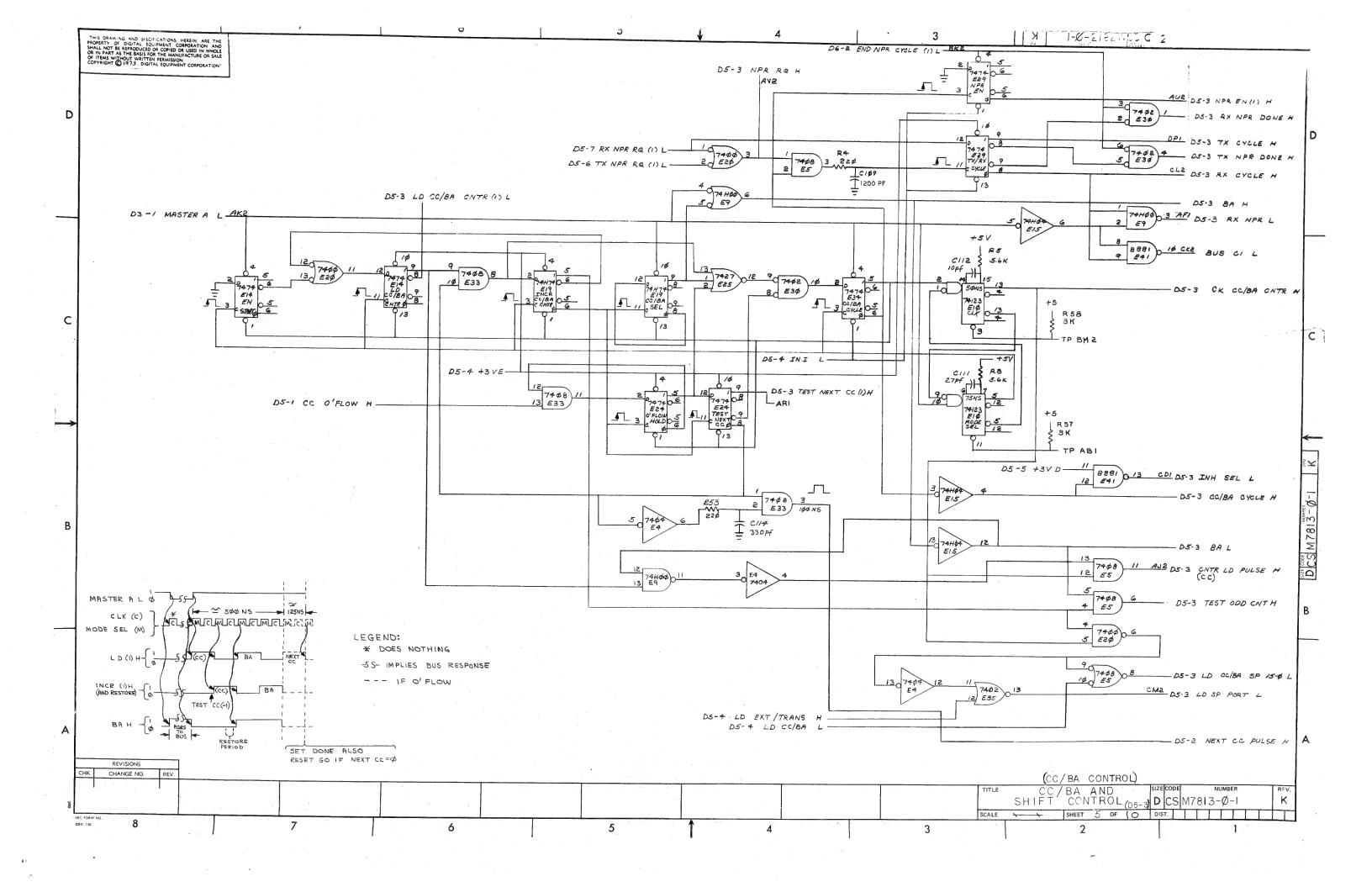


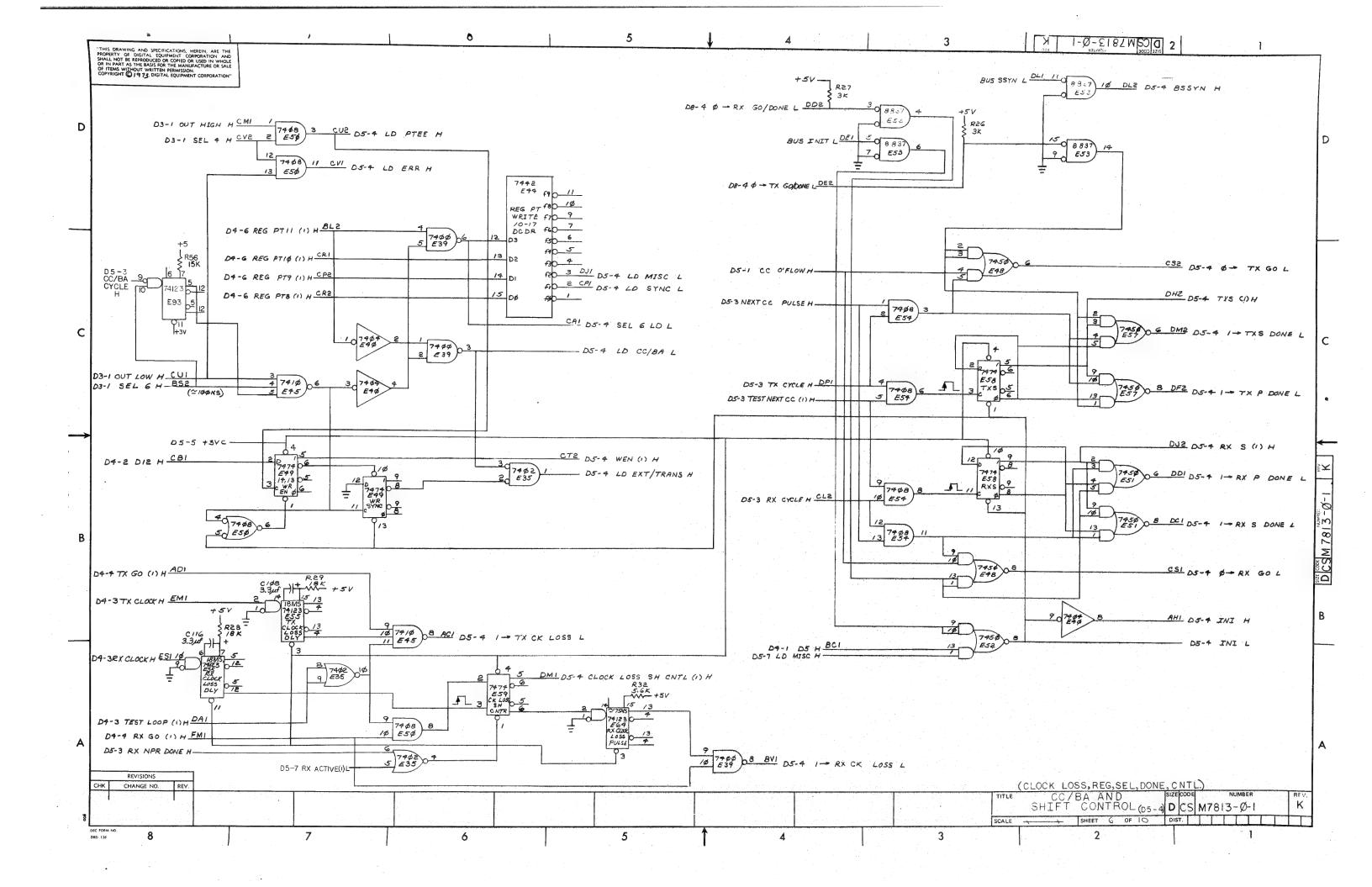


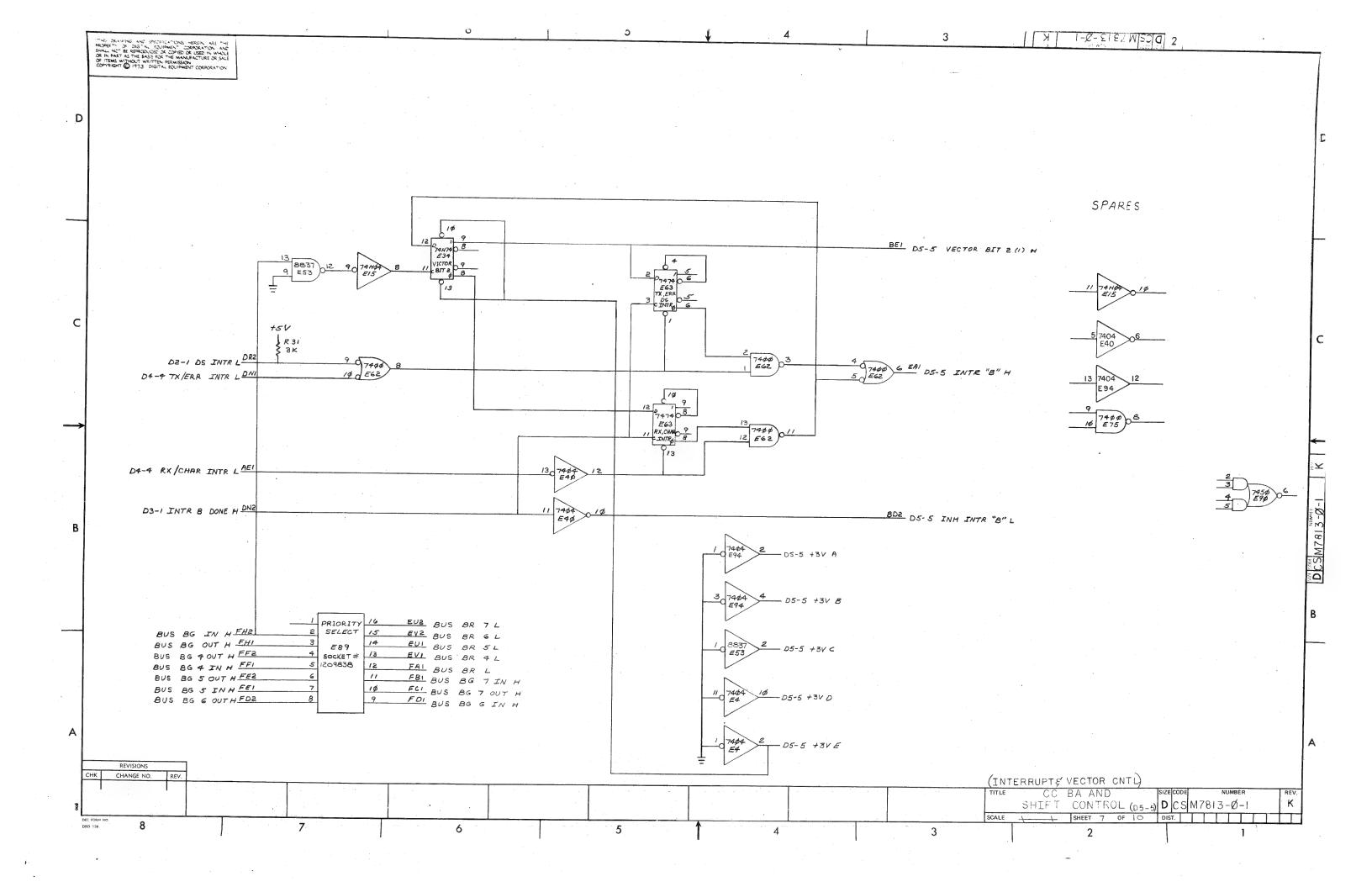
AJ1 BUS AØØ L AL1 BUS AØ1 L	90 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	PIN SIGNAL NAME P. AR2 D5-1 CC/DA 4 H AS1 D5-1 CC/BA 5 H	x FS1 05-	BIGNAL NAME. PAGE - 3 4 5 6 7 8 9 0 VPC ERP DET L	
AK1 BUS AG3 L AN1 BUS AG4 L AU1 BUS AG5 L AV1 BUS AG6 L AP1 BUS AG7 L	X X X X X	A12 D5-1 CC/BA 6 H A12 D5-1 CC/BA 6 H A52 C5-1 CC/BA 7 H BF2 D5-2 CC/BA 9 H B12 D5-2 CC/BA 1 H B12 D5-2 CC/BA 11 H	X	B RX C L EVEN VRC L LC RX 8UF 7-Ø L PX CC 20D L CRA DLY L RX LATE L RX NPR PQ (1) L	
BF1 BUS AR8 L BJ1 BUS AR9 L BK1 BUS AR9 L BH1 CUS A10 L BH1 CUS A11 L BH1 BUS A12 L BS1 BUS A13 L BR1 BUS A13 L BR1 BUS A14 L	X	BU2	X BKZ 06 X FP2 08 X FS2 03 X FT2 06 X DE2 00-2 X DD2 08-4	END NPR CYCLE (1) L RX SEARCH DONE L CRA L STRIP DBL CHAR L 8 TX GO/CDNE L X X X X X X X X X X X X X	
BP1 BUS A15 L CH2 BUS A16 L CC1 BUS A17 L CK2 BUS C1 L DL1 BUS SSYN L	X X X	CJ1 D5-2 SP POPT 14 H D5-2 CC/9A CARRY 15-F H D5-3 LD CC/9A CNTR (1) L D5-3 TEST NEXT CC (1) H AV2 D5-3 MPR RQ H AU2 D5-3 MPR PR (1) H D5-3 RY MPR DONE H	X X X DD2 D8-5 DU2 D8-5 U2 D8-6 U2 D8-6 U2 D8-6 U2 D8-6 U2 D8-6 U2 U2 U3	TX TGANS (1) L PAD PQ L CLE PQ L TX TOTAL TRANS (1) L BCC PQ L RX TRANS (1) L	
DE1 BUS INIT L FH2 BUS BU IN H FH1 BUS BG OUT H FF2 BUS BG 4 OUT H FF1 BUS BG 4 IN H FE2 BUS BG 5 OUT H FF1 BUS BG 5 OUT H FF1 BUS BG 5 OUT H	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	DP1 D5-3 TX CYCLE H D5-3 TX NPP DDNE H D5-3 TX NPP DDNE H D5-3 RX CYCLE H D5-3 RX NPR L D5-3 RX NPR L D5-3 CX CC/PA CNTR H	X X X X X X X X X X X X X X X X X X X	DIS RX TRANSFER PULSE L RX BCC CYCLE L X X	
FD2	XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX	CD1 D5-3 INH SEL L D5-3 CC/DA CYCLE H D5-3 CC/DA CYCLE H D5-3 CNTR LD PULSE H D5-3 TEST ODD CNT H D5-3 LD CC/DA SP 15-Ø L	X X X X X X X X X X X X X X X X X X X		
EU1 BUS BR 5 L EV2 BUS BR 6 L EU2 BUS BR 7 L DR2 D2-1 DS INTR L AK2 D3-1 MASTER A L CM1 D3-1 OUT HIGH H	x x x x x x x x x x x x x x x x x x x	CM2			
CV2 D3-1 SEL 4 H CU1 D3-1 OUT LOW H BS2 D3-1 SEL 6 H DN2 D3-1 INTR B DONE H AL2 D4-1 DØ H AN2 D4-1 D1 H AP2 D4-1 D2 H	X X X X X X	CA1			NOTE
AP2 D4-1 D2 H AM2 D4-1 D3 H BA1 D4-1 D4 H BC1 D4-1 D5 H BD1 D4-1 D5 H BB1 D4-1 D7 H BM1 D4-2 D8 H		CS2 D5-4 0 → TX GD L DM2 D5-4 1 → TXS DONE L DH2 D5-4 1 → TX P DONE L DJ2 D5-4 1 → TX P DONE L DJ2 D5-4 RX S(1) H DD1 D5-4 PX P DONE L	x x x x x x x x x x x x x x x x x x x		SHOWN WITH & 2 SIGNAL NAMES WITH TWO PINS ARE SHOWN AS PIN/PIN
BP2 D4-2 D9 H BR2 D4-2 D18 H BR2 D4-2 D18 H BR2 D4-2 D11 H CB1 D4-2 D12 H CCE2 D4-2 D13 H		D5-4 1 → RX S DONE L CS1 D5-4 ∅ → RX GO L AH1 D5-4 INI H D5-4 INI L DL2 D5-4 BSSYN H BD2 D5-5 INH INTR "s" L	X X X X X		
CD2 D4-2 D15 H EP2 D4-3 BITS 8 (1) H EN2 D4-3 BITS 9 (1) H EM2 D4-3 BITS 10 (1) H EL2/FN2 D4-3 BITS 11 (1) H EM1 D4-3 TX CLOCK H	x	BE1	$\begin{bmatrix} x & & & & & & \\ & x & & & & & \\ x & & & &$		
ES1 D4-3 RX CLOCK H DA1 D4-3 TEST LOOP (1) H DF1 D4-3 VRC (1) H EH2 D4-3 SEPIAL DATA IN L FM2 D4-4 LD RX 15-8 H AD1 D4-4 TX GD (1) H FM1 D4-4 RX GD (1) H		DV2 D5-6 B TXC L EB1 D5-6 TX FAKE END (1) H EB1 D5-6 TX ACTIVE (1) H D5-6 INI L ED2 D5-6 DLE EN (1) L EC1 D5-6 BCC EN (1) L			
DNI D4-4 TX/ERR INTP L AEI D4-4 TX/CHAP INTP L DS2 D4-4 IDLE MODE (1) H FNI D4-4 STRIP SYNC (1) H EPI D4-6 RX SYNC DET H CR2 D4-6 RS SYNC DET H		ED1 05-6 TX SYNC EN 15-8 L EE2 D5-6 TX SYNC EN 7-8 L EF1 D5-6 DATA EN 15-8 L EF2 D5-6 DATA EN 7-8 L DV1 D5-6 SYNC EN L DV1 D5-6 SYNC EN L	X X X X X X X X X X X X X X X X X X X		
CP2: D4-6 REG PT 9 (1) H CR1 D4-6 REG PT 10 (1) H BL2 D4-6 REG PT 11 (1) H CN2 D4-6 REG PT 11 (1) H CN1 D4-6 EE 13 (1) H CN1 D4-6 EE 14 (1) H FR2 D4-7 TX DATA H	X X X X X X X	EK1 05-6 TX BIT CNTR 8 (1) H EJ2 D5-6 TX BIT CNTR 4 (1) H ES1 05-6 TX BIT CNTR 2 (1) H ER2 D5-6 TX BIT CNTR 1 (1) H FJ1 D5-6 TX BIT CNTR 1 (1) H ER1 D5-6 TX LATE L D5-6 TX LATE L D5-6 TX NPR RQ (1) L			
D5-1 CC 00D (1) H D5-1 CC BA CARRY 7-1 H D5-1 CC B CARRY 7-1 H D5-1 CC BC FLOW H CH1 D5-1 CC/BA ADRS 8 L CJ2 D5-1 CC/BA ADRS 9 L CF1 D5-1 CC/BA ADRS 1 L	X X X X X X X X X X X X X X X X X X X	DS1			
AE2 D5-1 CC/BA Ø H AD2 D5-1 CC/BA 1 H AH2 D5-1 CC/BA 2 H AF2 D5-1 CC/BA 3 H	X X X X X X X X X X X X X X X X X X X	DH1 D5-7 RX ACTIVE (1) H EL1 D5-7 LD PX BIT CNTR L D5-7 LD MISC H FU1 D5-7 CLR PX CNTL L D5-7 CLR PX CNTL L D5-7 LD BUF L D5-6 SYNC/ DATA EN L			선생님 보고 있다. 사용하여 환경 보고 되었다는 것이 되는 것이 되었다. 되는 사용 보안에 가장된 것이 되는 것이 되는 것이 되는 것이 되는 것이다. 사용 경치를 통해 결과하였다는 것이 보고 있다고 있다. 그 없는 것이다.
REVISIONS CHK CHANGE NO. REV.		D5-6 SŸNG/DATA EN L D5-7 RX ACTIVE(I)L			(PRINT FIND) TITLE CC/BA AND SIZE CODE NUMBER RE SHIFT CONTROL DCS M7813-0-1

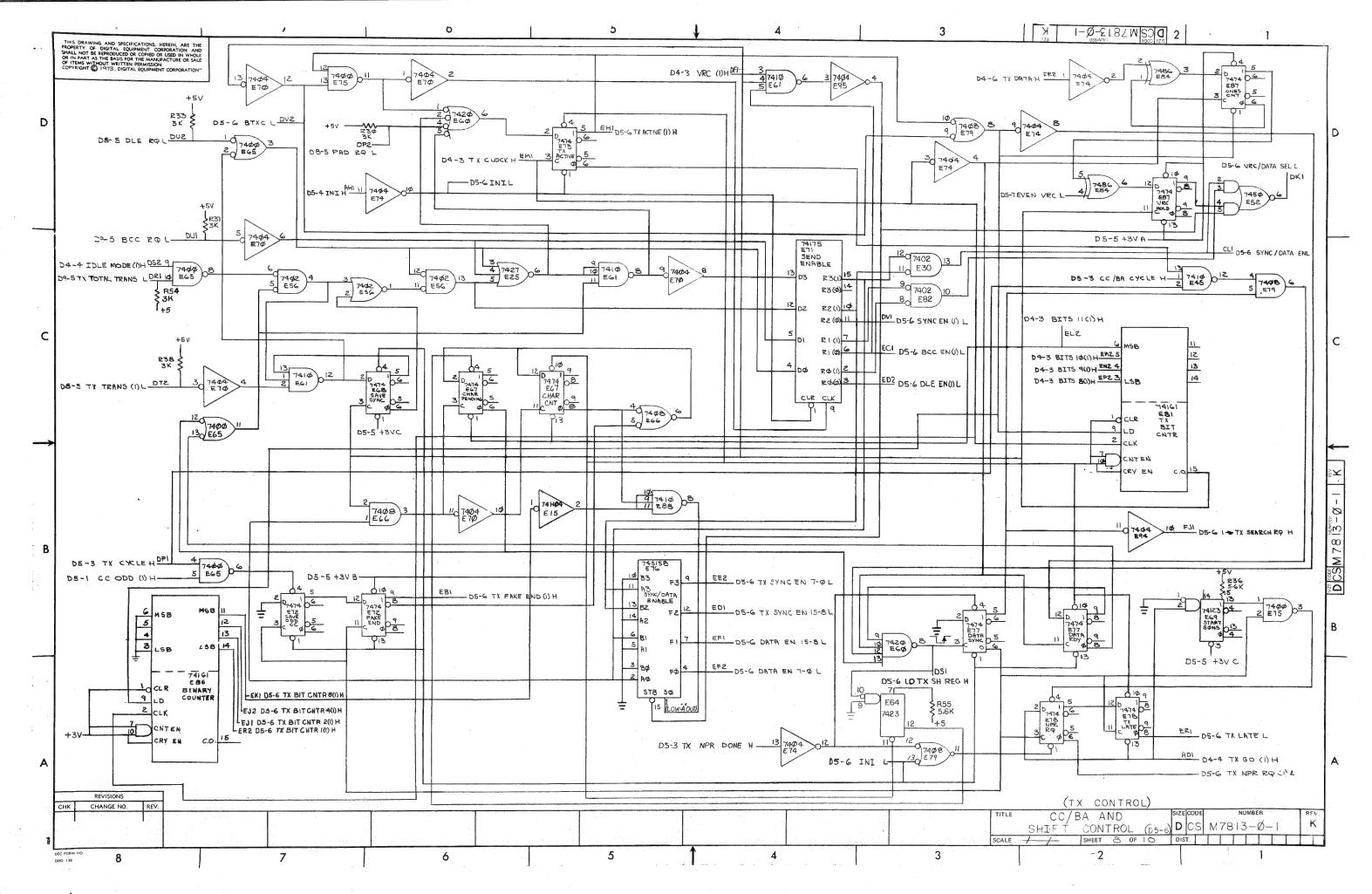




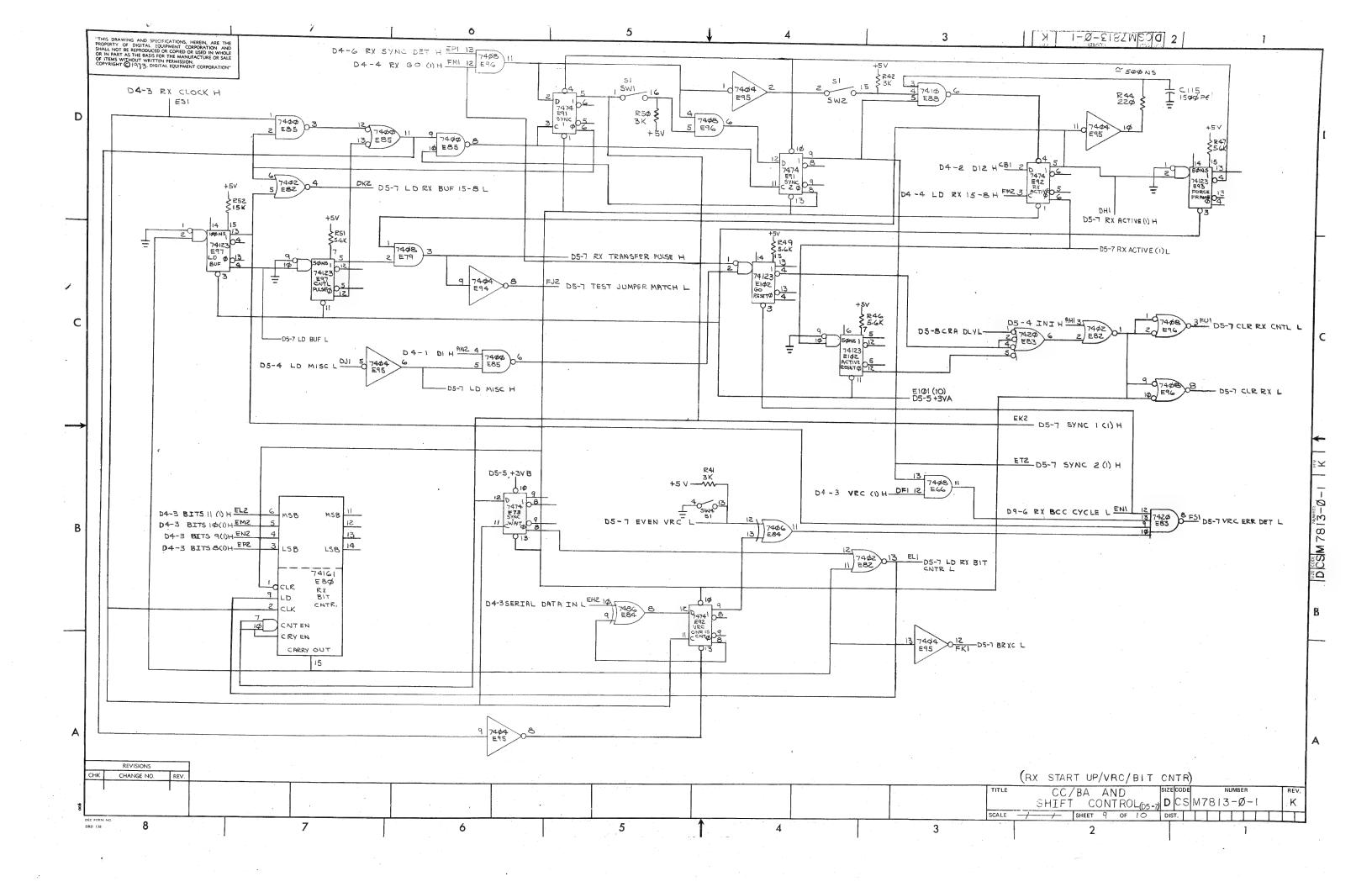


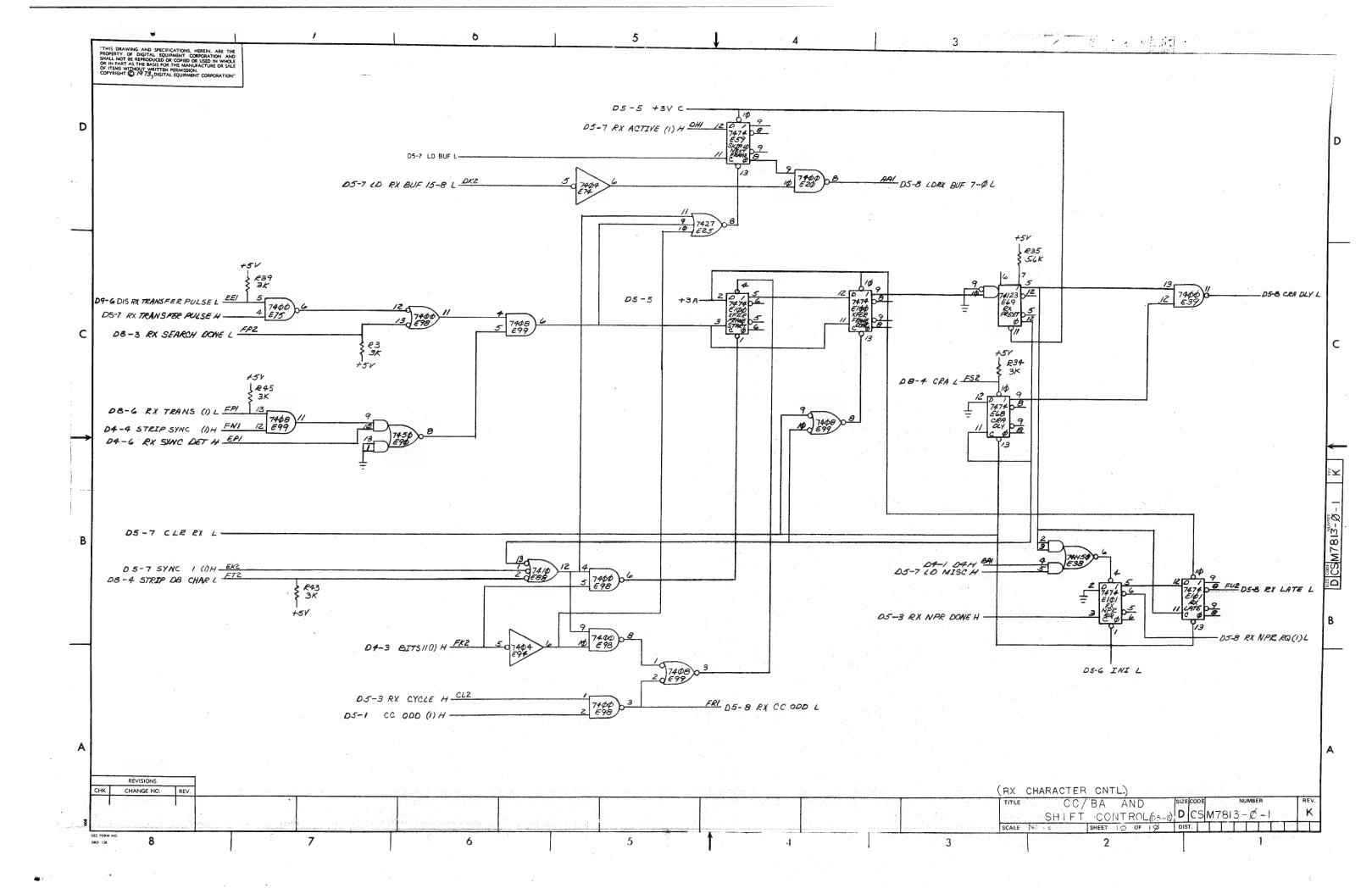


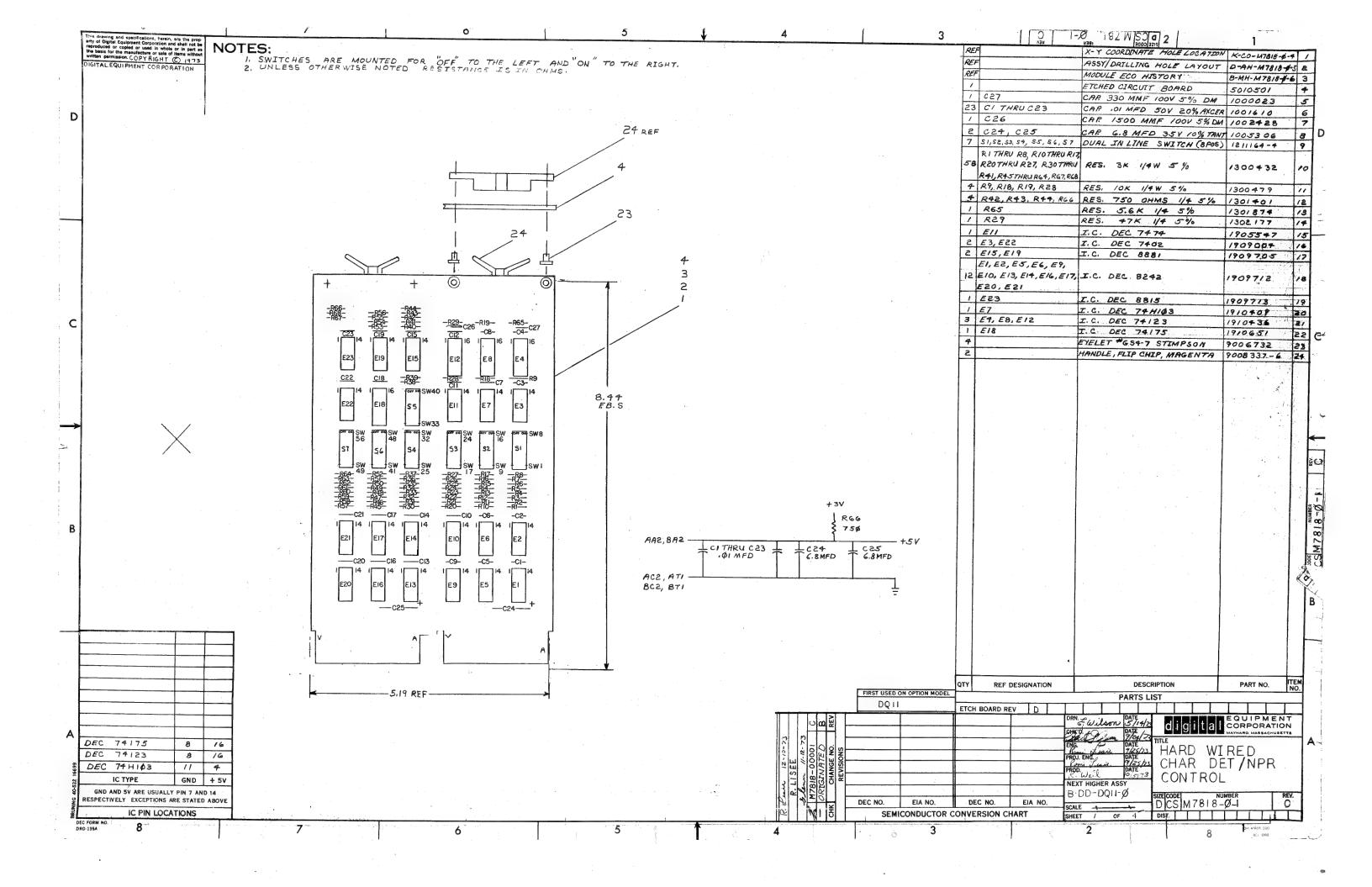


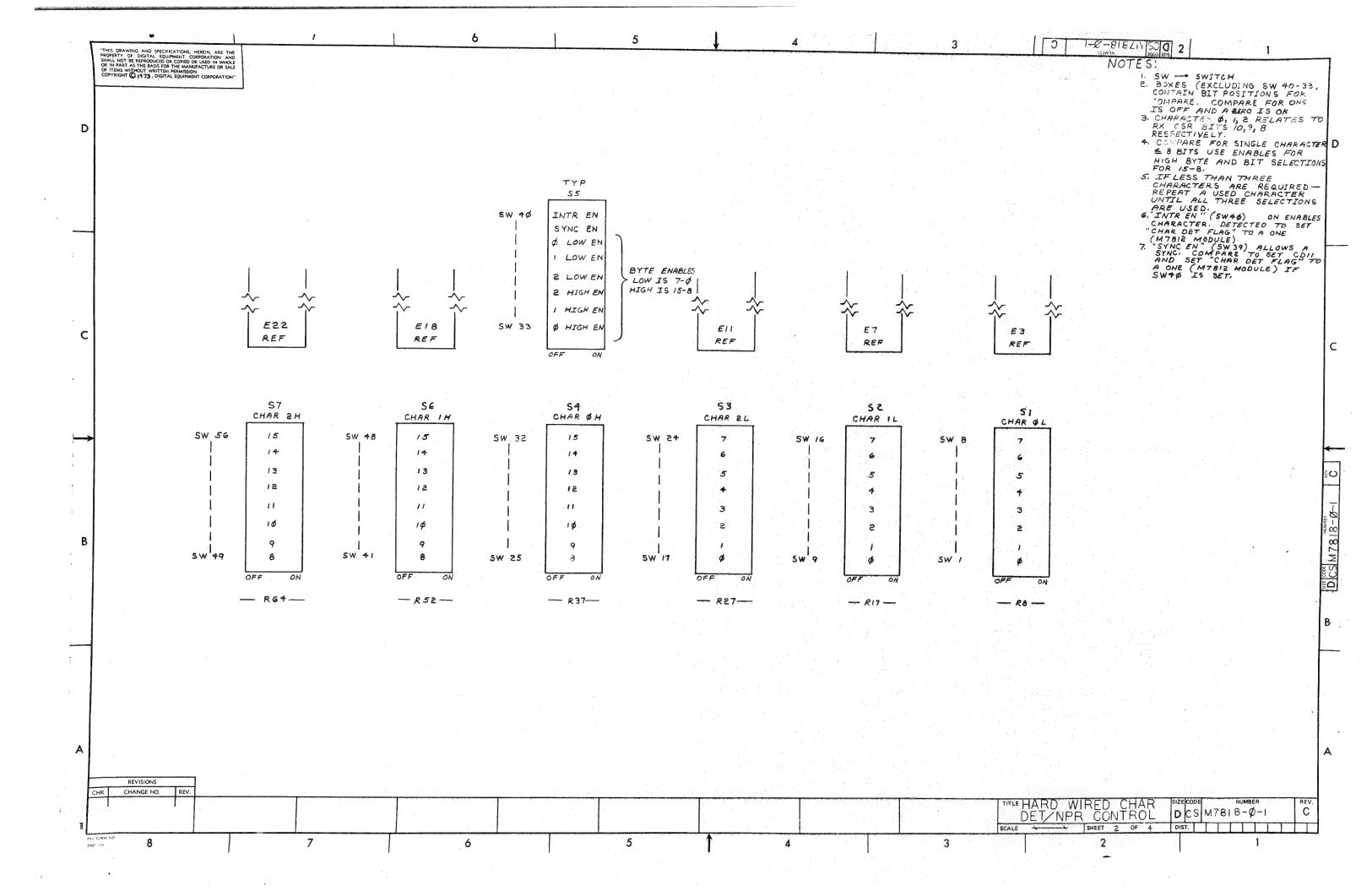


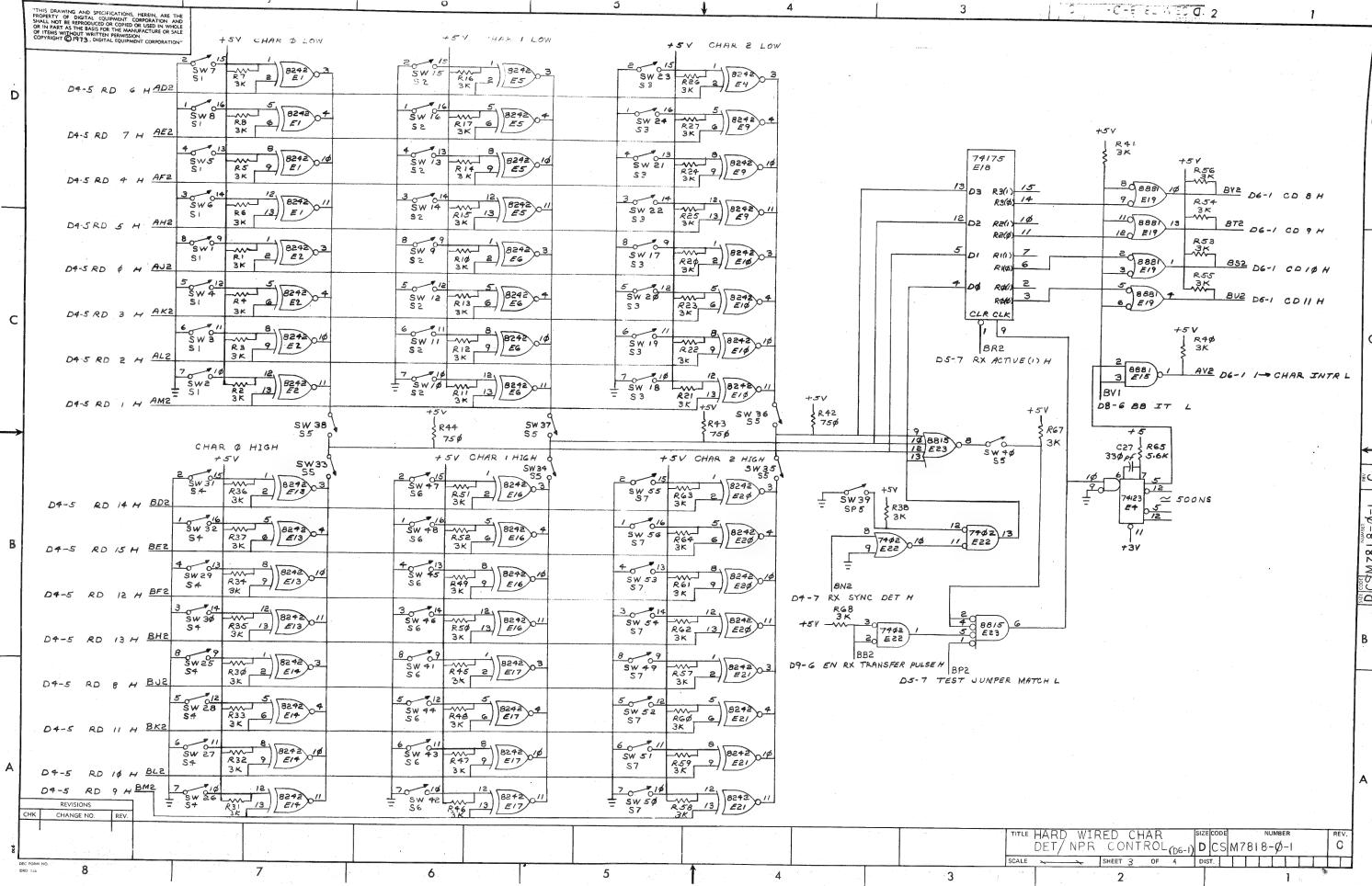
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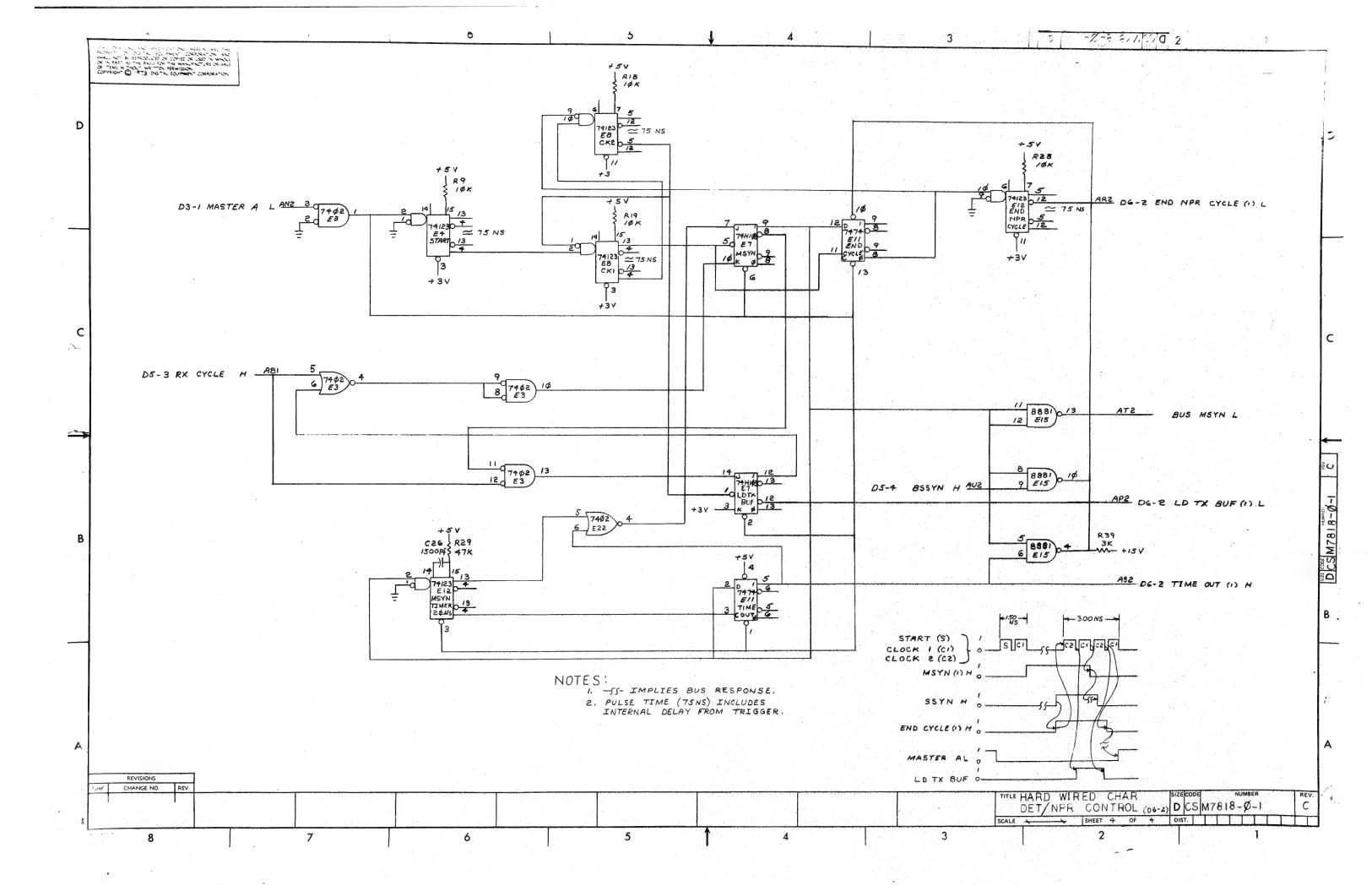


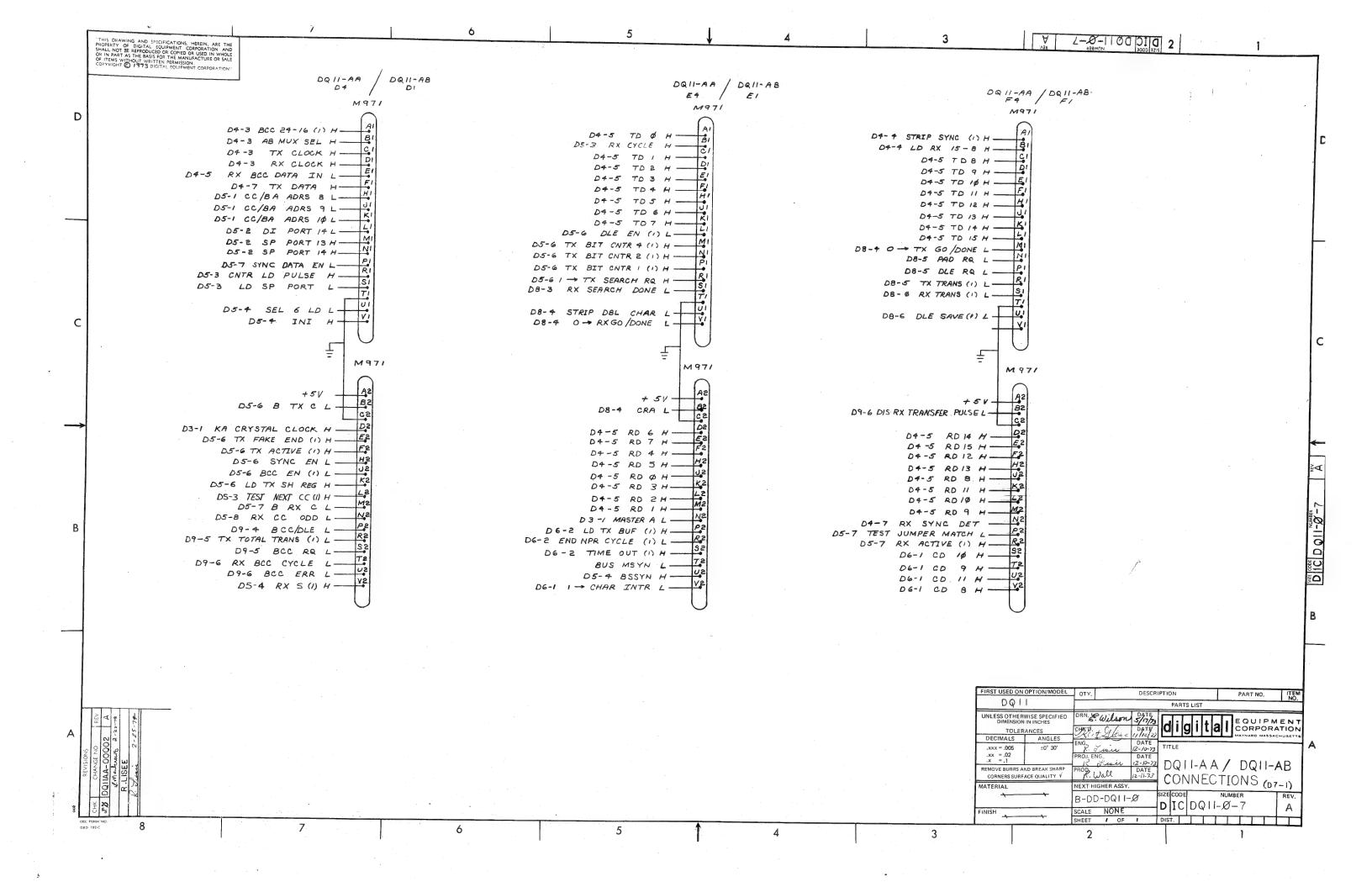


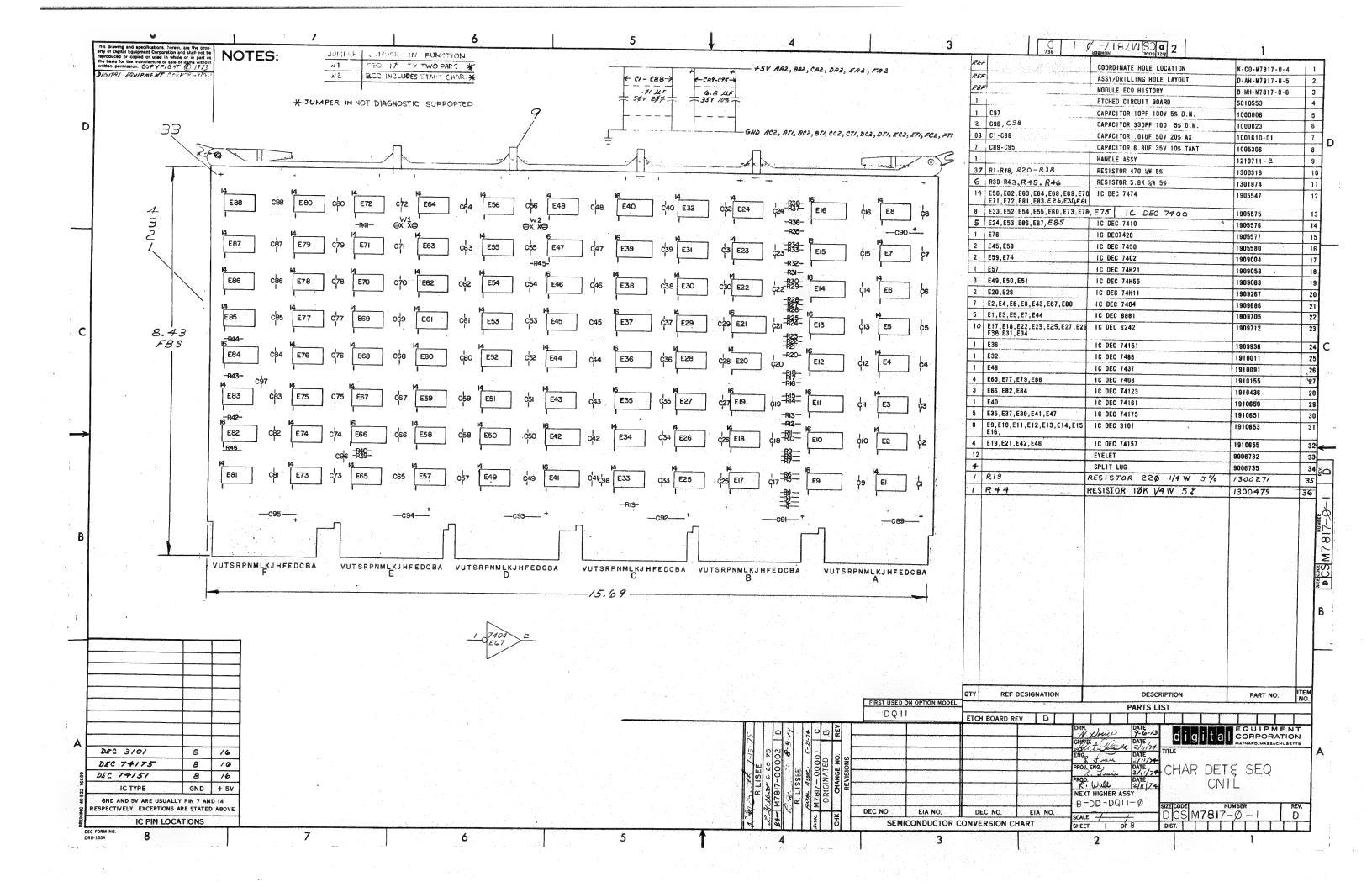




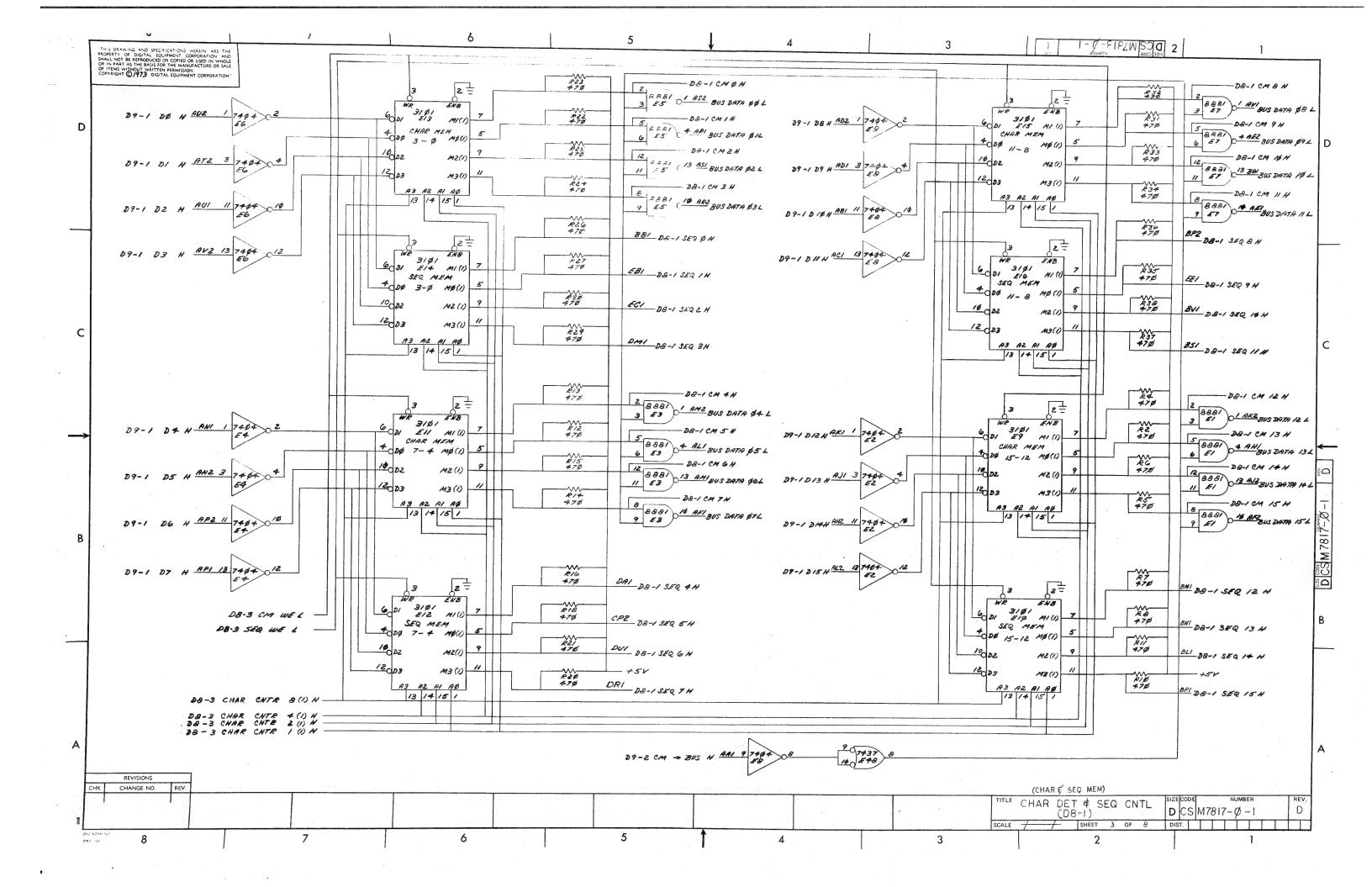
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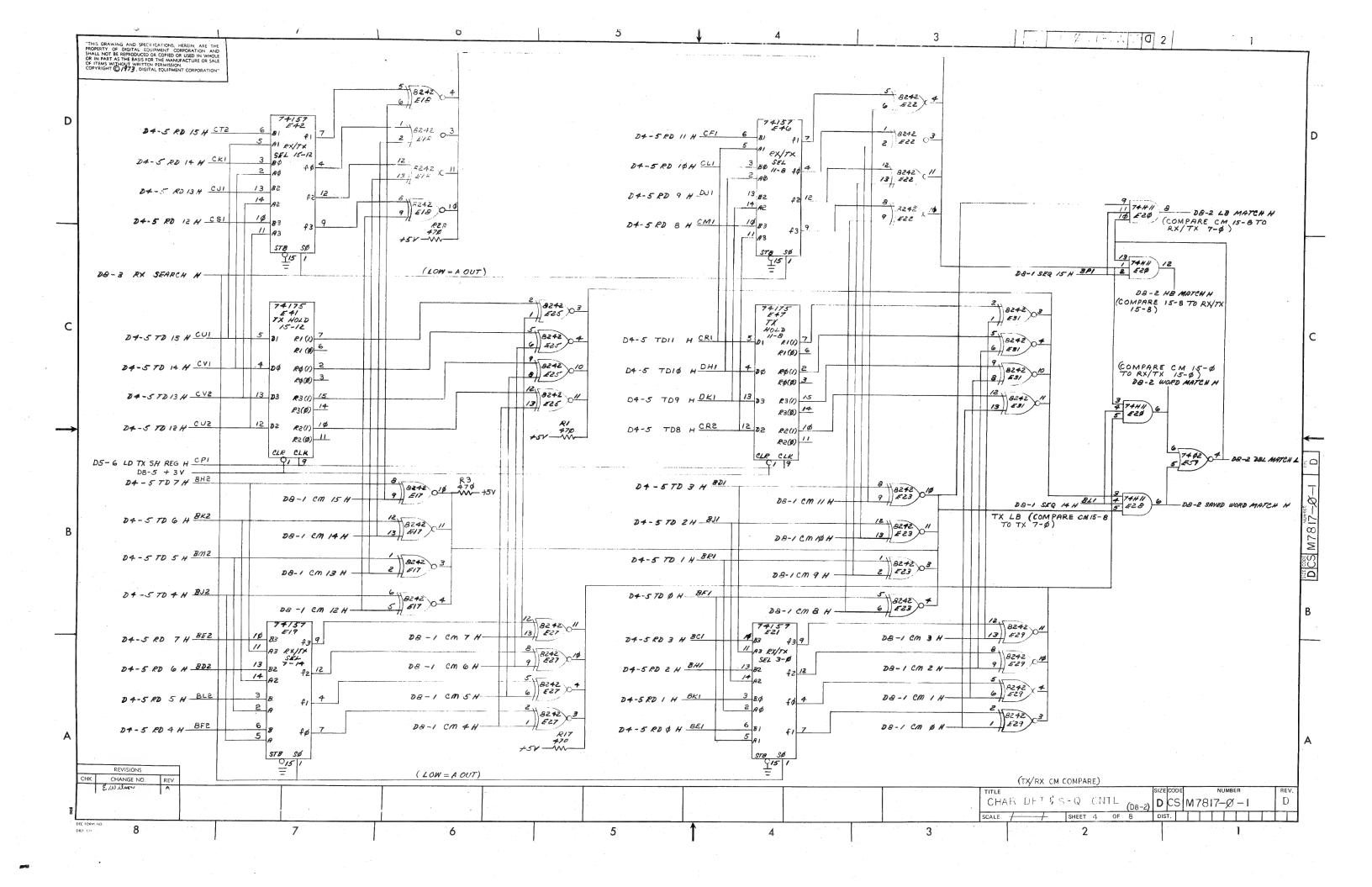


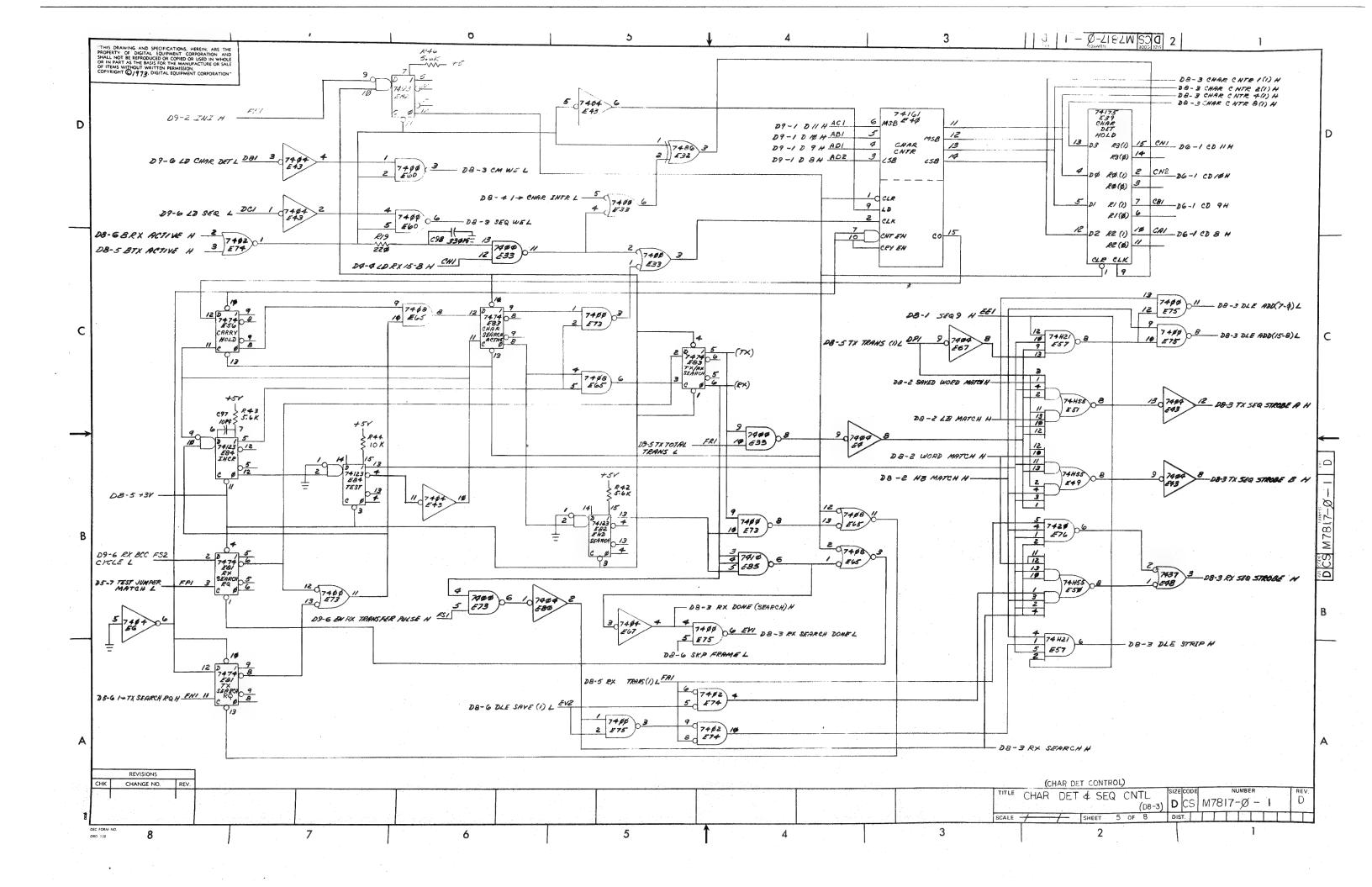


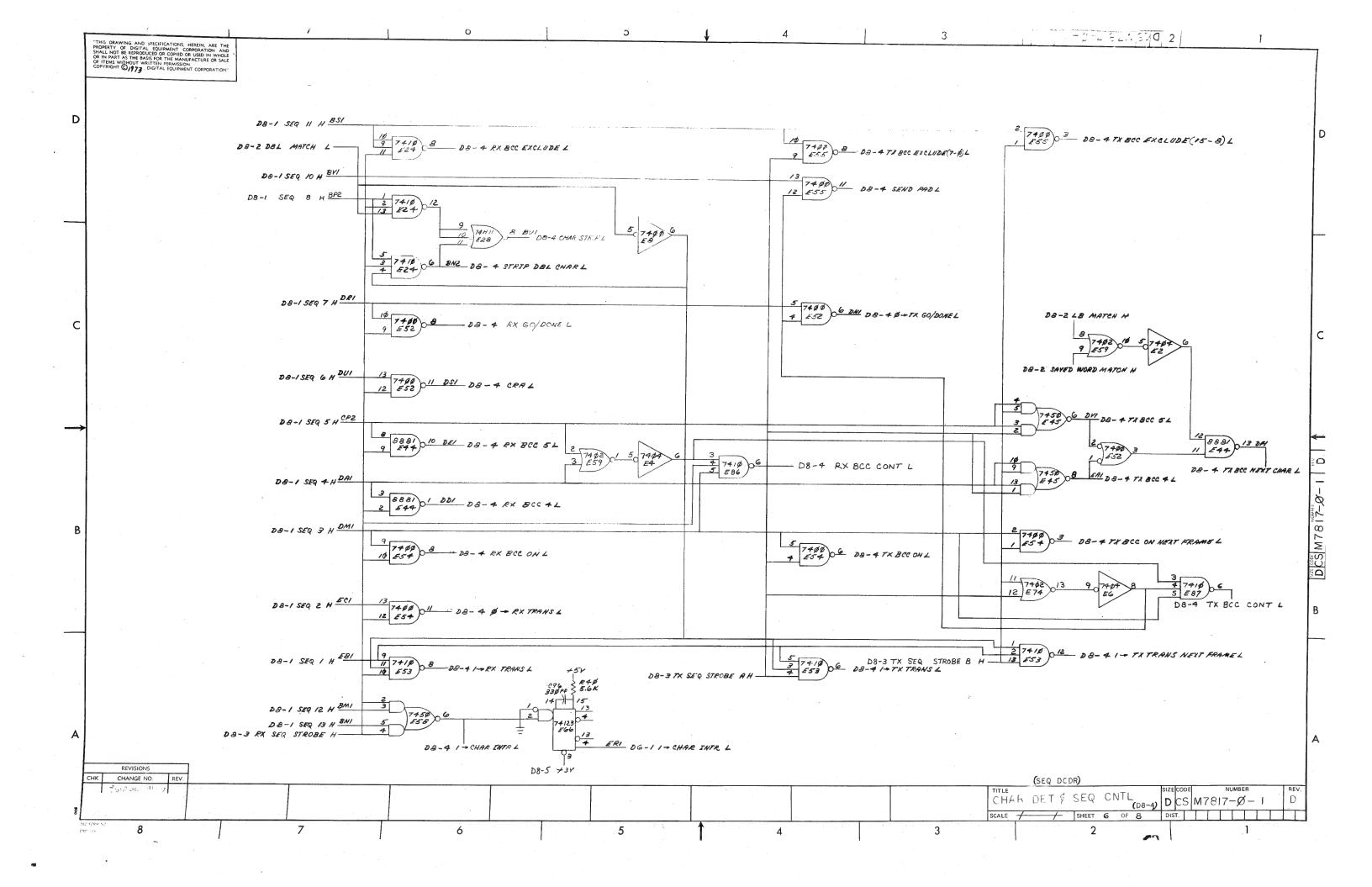


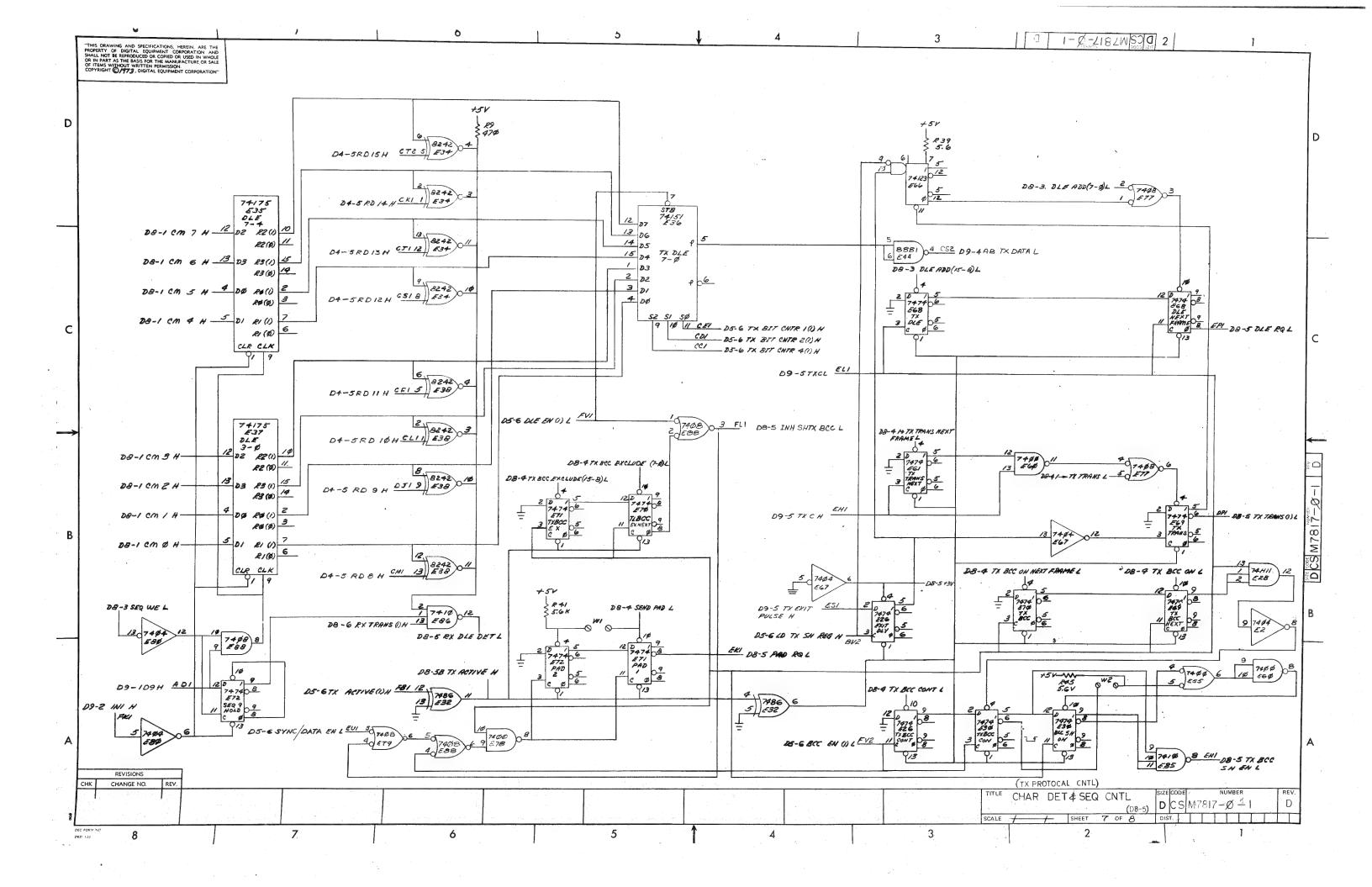
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PIN NAME		PIN NAME	PAGE 3 4 5 6 7 8	PIN NAME	PAGE 3 4 5 6 7 8			
AR1 BUS DATA 01 L AS1 BUS DATA 02 L AR2 BUS DATA 03 L AM2 BUS DATA 04 L	X	DUI	X X X X X	DC1	X X X			
AL1 BUS DATA 85 L AM1 BUS DATA 86 L AF1 BUS DATA 87 L AV1 BUS DATA 88 L		BVI D8-1 SEQ 10 H BS1 D8-1 SEQ 11 H BMI D8-1 SEQ 12 H BNI D8-1 SEQ 13 H BLI D8-1 SEQ 14 H		EF1	X X		¥	
AE2 BUS DATA N9 L BA1 BUS DATA 1		BP1						
AH1 BUS DATA 13 L A12 BUS DATA 14 L BUS DATA 15 L F11 D4-3 RX CLOCK H CH1 D4-4 LD RX 15-8 H	X	D8-2 DBL MATCH L D8-2 SAVED WORD MATCH H D8-3 CM WE L D8-3 SEQ WE L	X X X X X X X X X X X X X X X X X X X					
FF1 D4-4 STRIP SYNC (1) H BE1 D4-5 RD Ø H BK1 D4-5 RD 1 H BK1 D4-5 RD 2 H		D8-3 RX DONE (SEARCH)H EVI D8-3 RX SEARCH DONE L D8-3 DLE ADD (7-0) I D8-3 DLE ADD (15-8) L D8-3 TX SEO STORME A	X X X X X X	·		NOTES;	1. IF A NAME APPEARS TWO TIMES IT IS	*
BC1	X X X X X X X X X X X X X X X X X X X	D8-3 TX SEQ STROBE A H D8-3 TX SEQ STROBE B H D8-3 RX SEQ STROBE H D8-3 DLE STRIP H D8-3 RX SEARCH H					SHOWN WITH X. 2. SIGNAL NAMES WITH TWO PINS ARE STORNAL OF THE STREET O	
BEZ D4-5 RD 7 H CM1 D4-5 RD 8 H DJ1 D4-5 RD 9 H CL1 D4-5 RD 1B H CF1 D4-5 RD 1B H	X X X X X X X X X X X X X X X X X X X	D8-3 CHAR CNTR 1 (1) H D8-3 CHAR CNTR 2 (1) H D8-3 CHAR CNTR 4 (1) H D8-3 CHAR CNTR 8 (1) H	X				SHOWN IN BRACKETS.	
CS1	X X	DB-4 RX BCC EXCLUDE L BUI						
BF1] [X]] [] []	DE1						
BH2 D4-5 TD 4 H BK2 D4-5 TD 5 H BK2 D4-5 TD 6 H BH2 D4-5 TD 7 H CR2 D4-5 TD 8 H		D8-4 Ø → RX TRANS L D8-4 1 → RX TRANS L D8-4 1 → CHAR INTR L D8-4 TX BCC EXCLUDE (7-Ø) L						
ORT D4-5 TD 9 H DH1 D4-5 TD 19 H CR1 D4-5 TD 11 H CU2 D4-5 TD 12 H	Î X X X X X X X X X X X X X X X X X X X	D8-4 SEND PAD L D8-4 8						
CV2		DF1		-				
FRZ		D8-4 1X BCC CONT L D8-4 1 TX TRANS NEXT FRAME D8-5 RX DLE DET L FL1 D8-5 INH SH TX BCC L EK1 D8-5 PAD RQ L						
CD1		DB-5 B TX ACTIVE H DB-5 B TX ACTIVE H DB-5 B TX ACTIVE H DB-5 DLE RQ L DB-5 TX TRANS (1) L	X X X X X X X X X X X X X X X X X X X					GEWIN.
FB1	x X X	ENI D8-5 TX BCC SH EN L M1 D8-6 NOT RESTART L EDI D8-6 BCC DN SYNC (1) L D8-6 SKP FRAME L						300
CAI	X	V2 DB-6 DLE SAVE (†) L DB-6 B RX ACTIVE H FU1 DB-6 IT BB L M1 DB-6 DES RX BCC SH L DB-6 RX TRANS (1) H	X X X X X X X X X X X X X X X X X X X					201 37.5
EU2 D6-2 END NPR CYCLE (1) H D8-1 CM Ø H D8-1 CM 1 H D8-1 CM 2 H	X X X X	FAI	$\begin{bmatrix} x \\ x \end{bmatrix}$				•	•
D8-1 CM 3 H D8-1 CM 4 H D8-1 CM 5 H D8-1 CM 6 H		NU1 D9-1 D2 H NV2 D9-1 D3 H NN1 D9-1 D4 H NN2 D9-1 D5 H	X X X					
DB-1 CM 7 H DB-1 CM 8 H DB-1 CM 9 H DB-1 CM 10 H		RP2	X X X X X					
D8-1 CM 11 H D8-1 CM 12 H D8-1 CM 13 H D8-1 CM 14 H D8-1 CM 15 H		NB1						
BB1 DB-1 SEQ Ø H EB1 DB-1 SEQ 1 H EC1 DB-1 SEQ 2 H DB-1 SEQ 3 H	X X X A	NL2 D9-1 D15 H NA1 D9-2 CM → BUS H K1 D9-2 INI H D9-4 AB TX DATA L		•				
DA1 D8-1 SEQ 4 H CP2 D8-1 SEQ 5 H REVISIONS	X X E	R1	X X X X X					
CHK CHANGE NO. REV.		S1 D9-6 EN RX TRANSFER PULSE H					TITLE CHAR DET SEQ CNTL	SIZE CODE NUMBER REV.
DEC FORM NO. 8	7			<u> </u>			CALE / SHEET 2 OF 8	D CS M 7817-Φ-1 D
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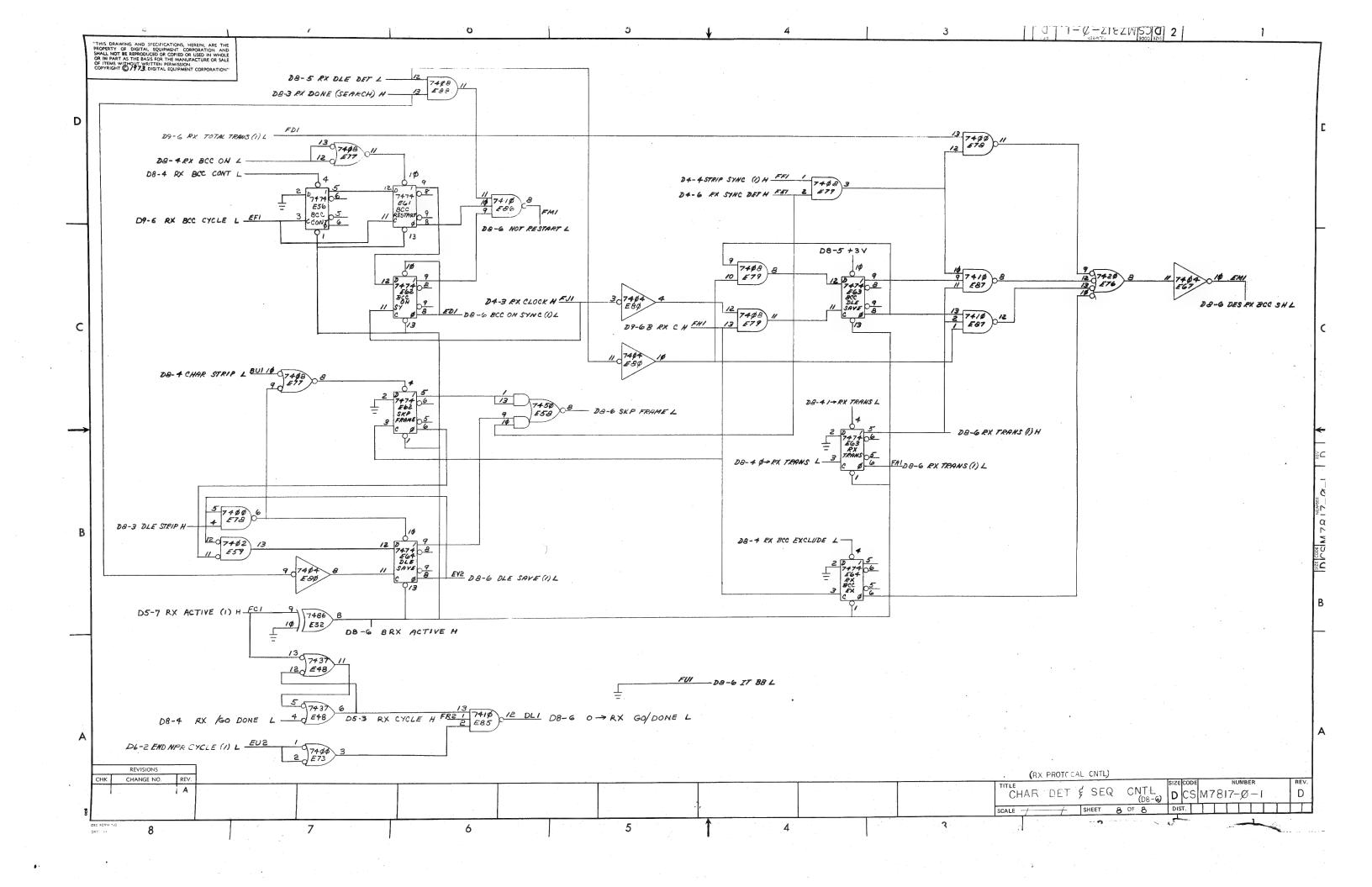


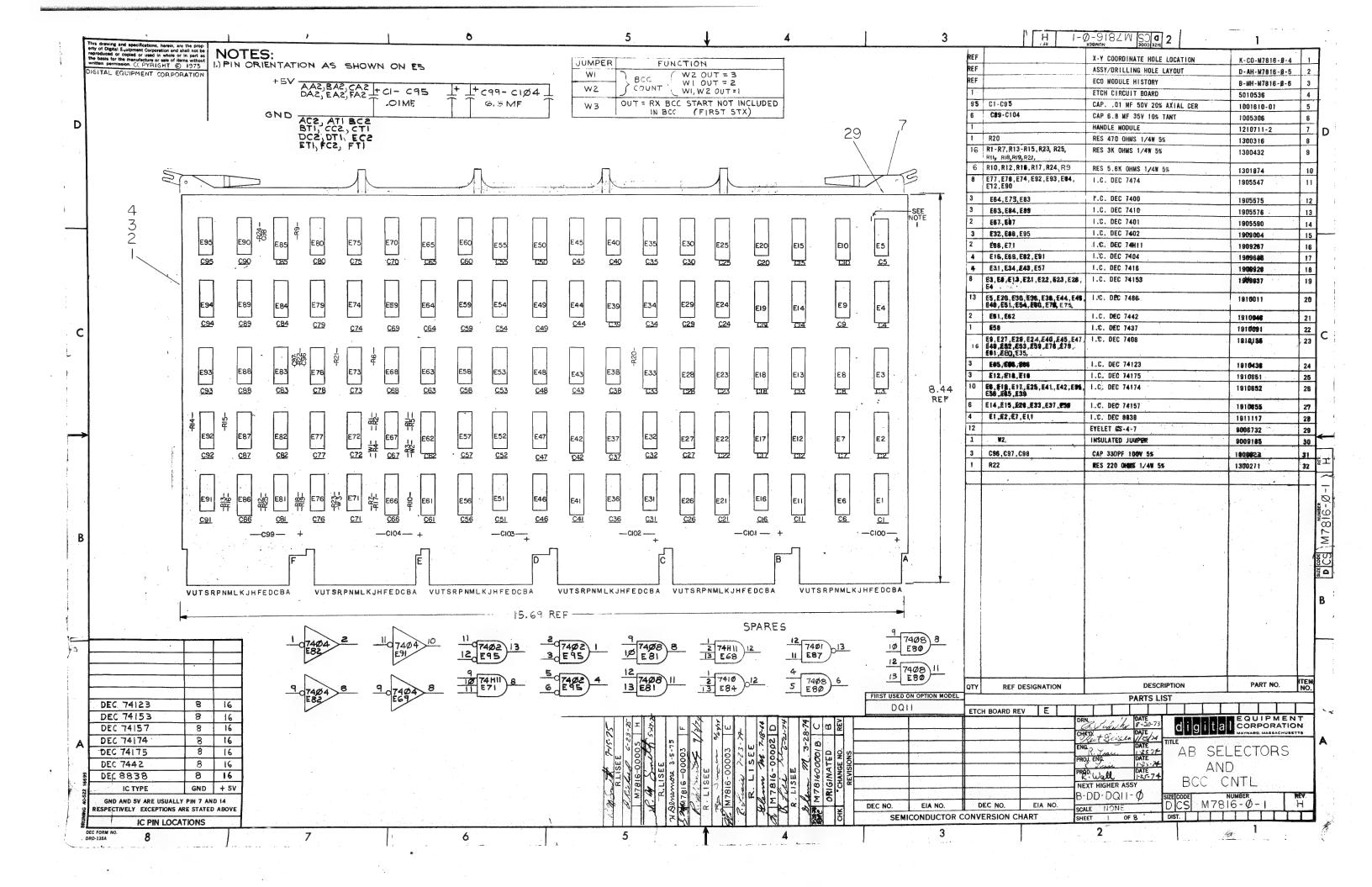




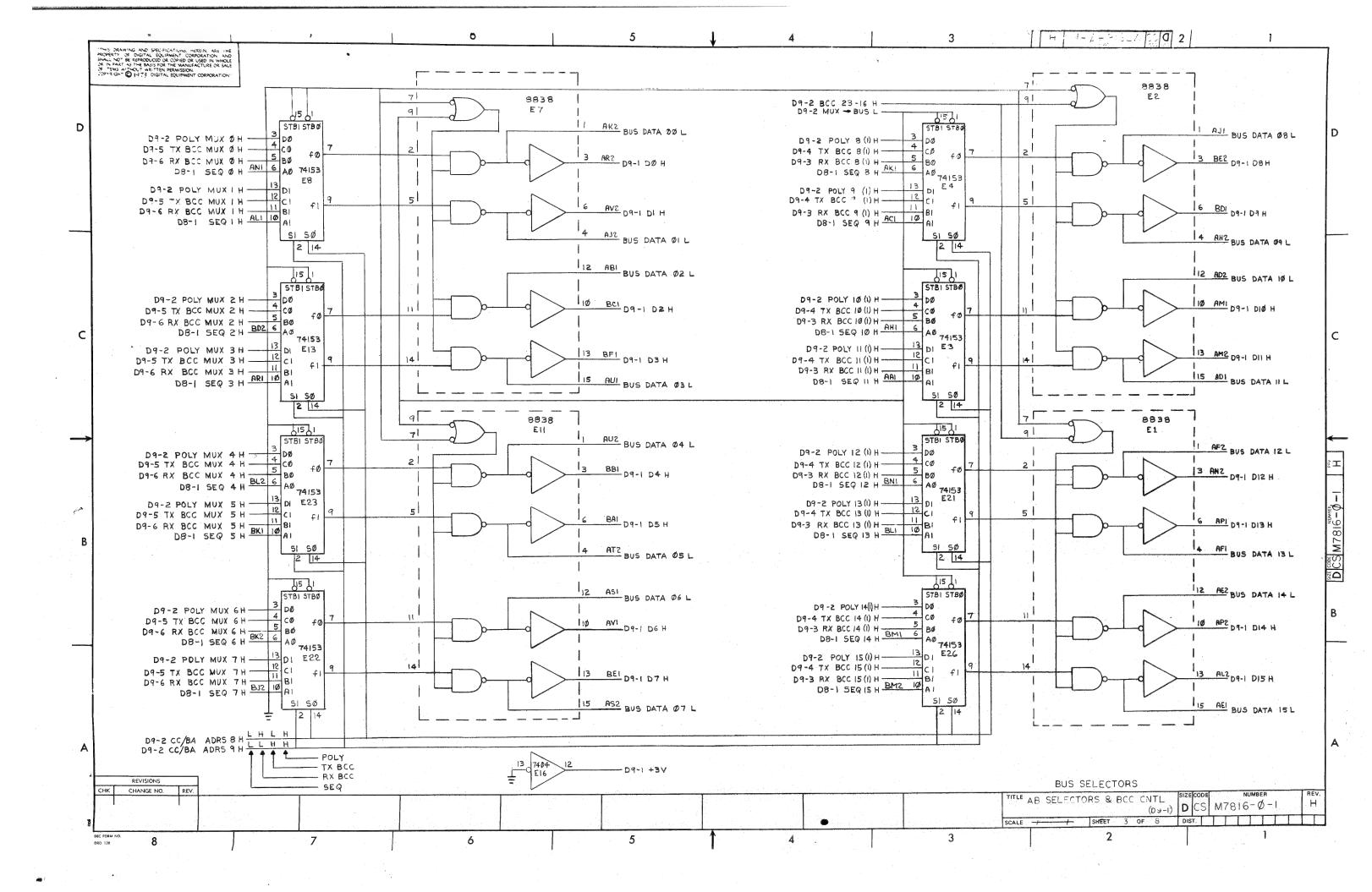


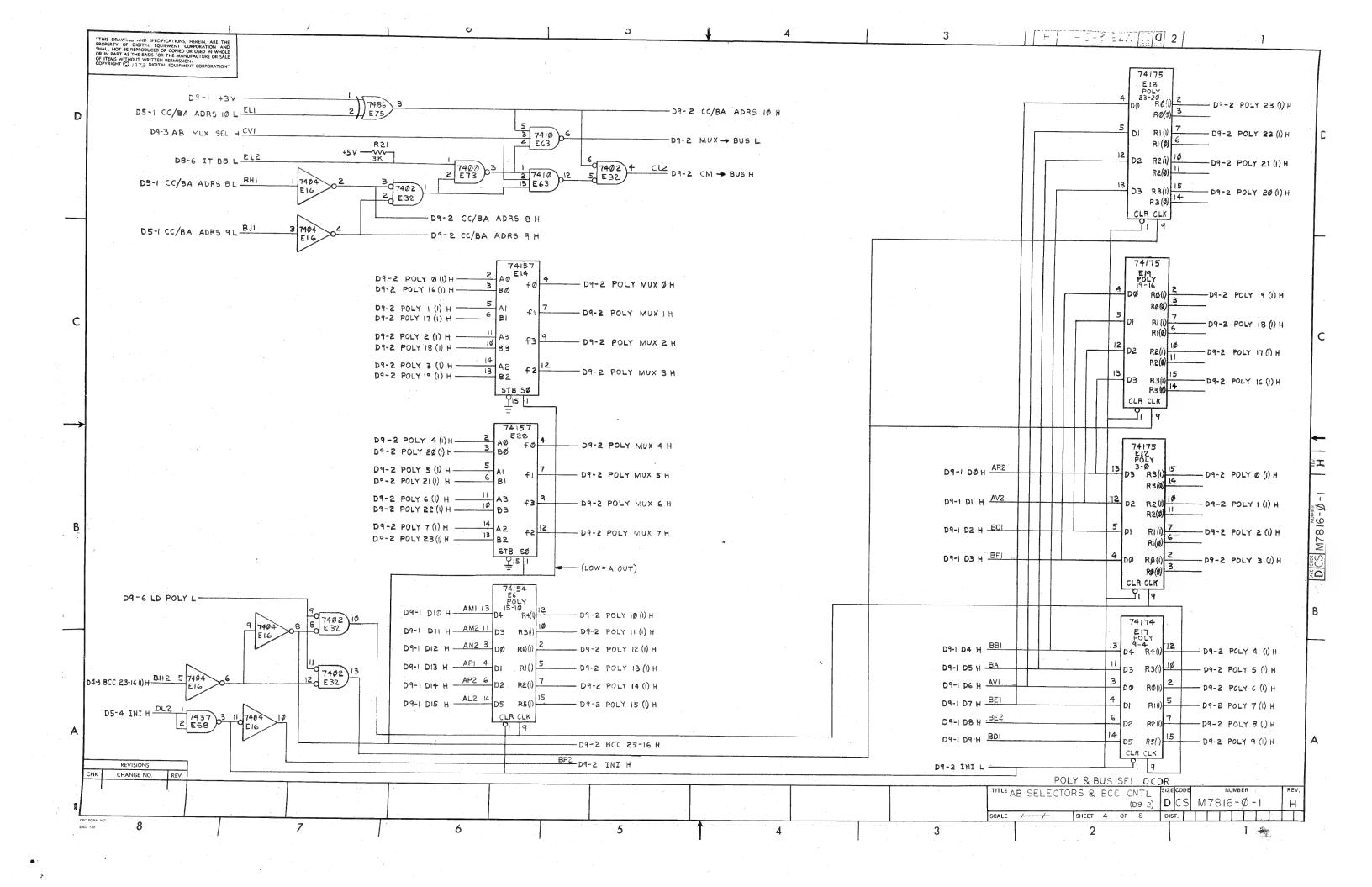


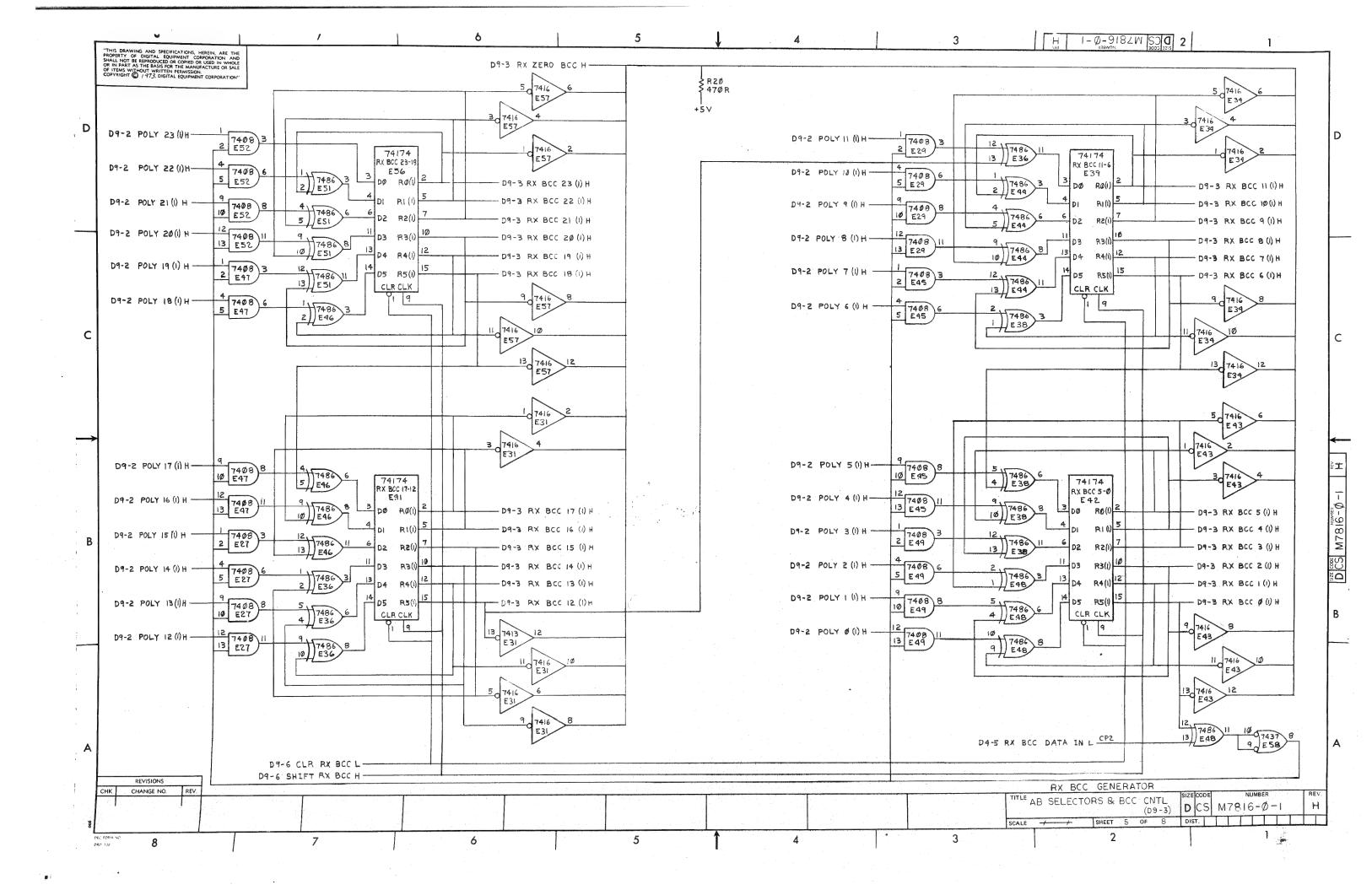


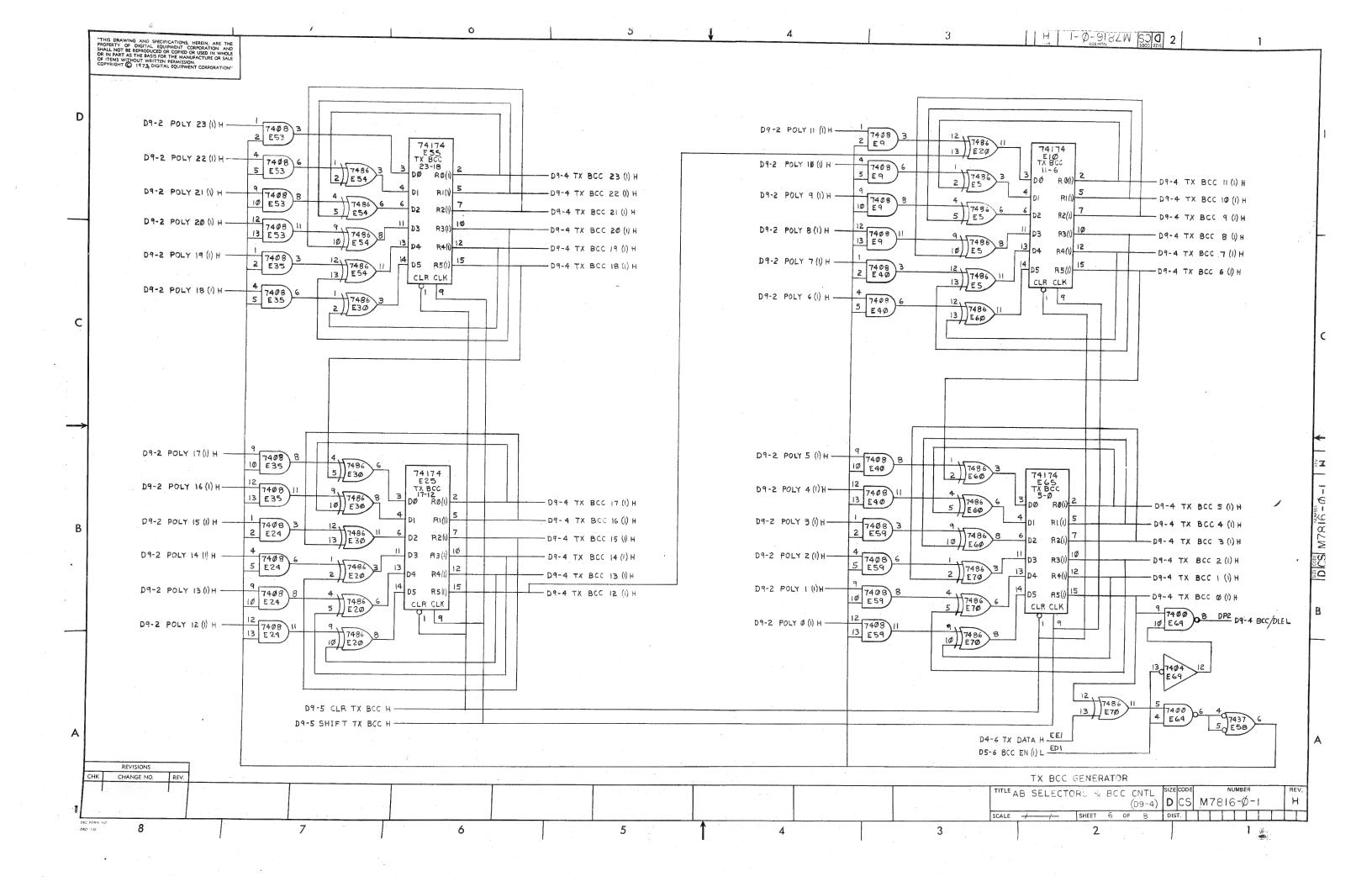


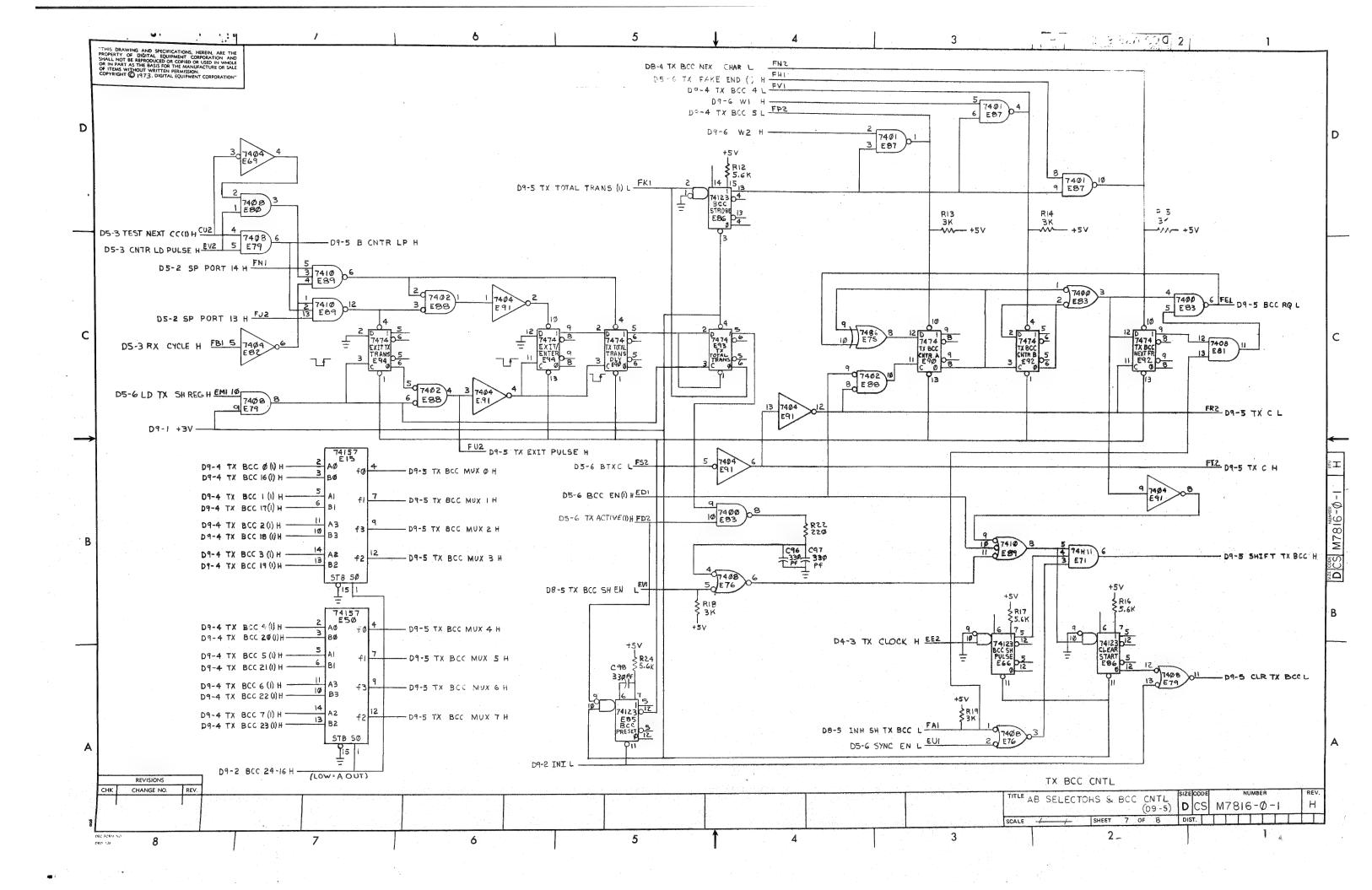
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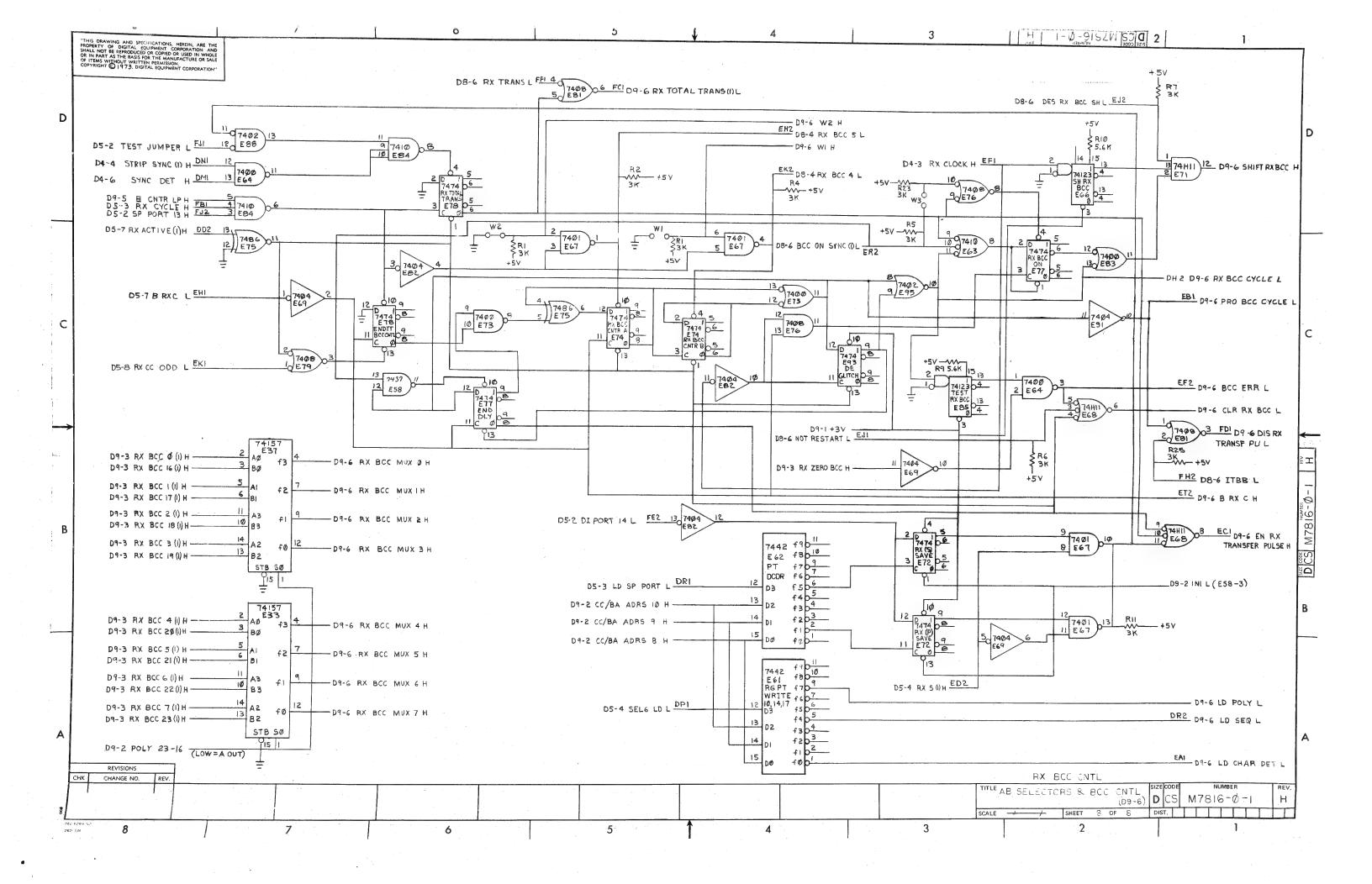


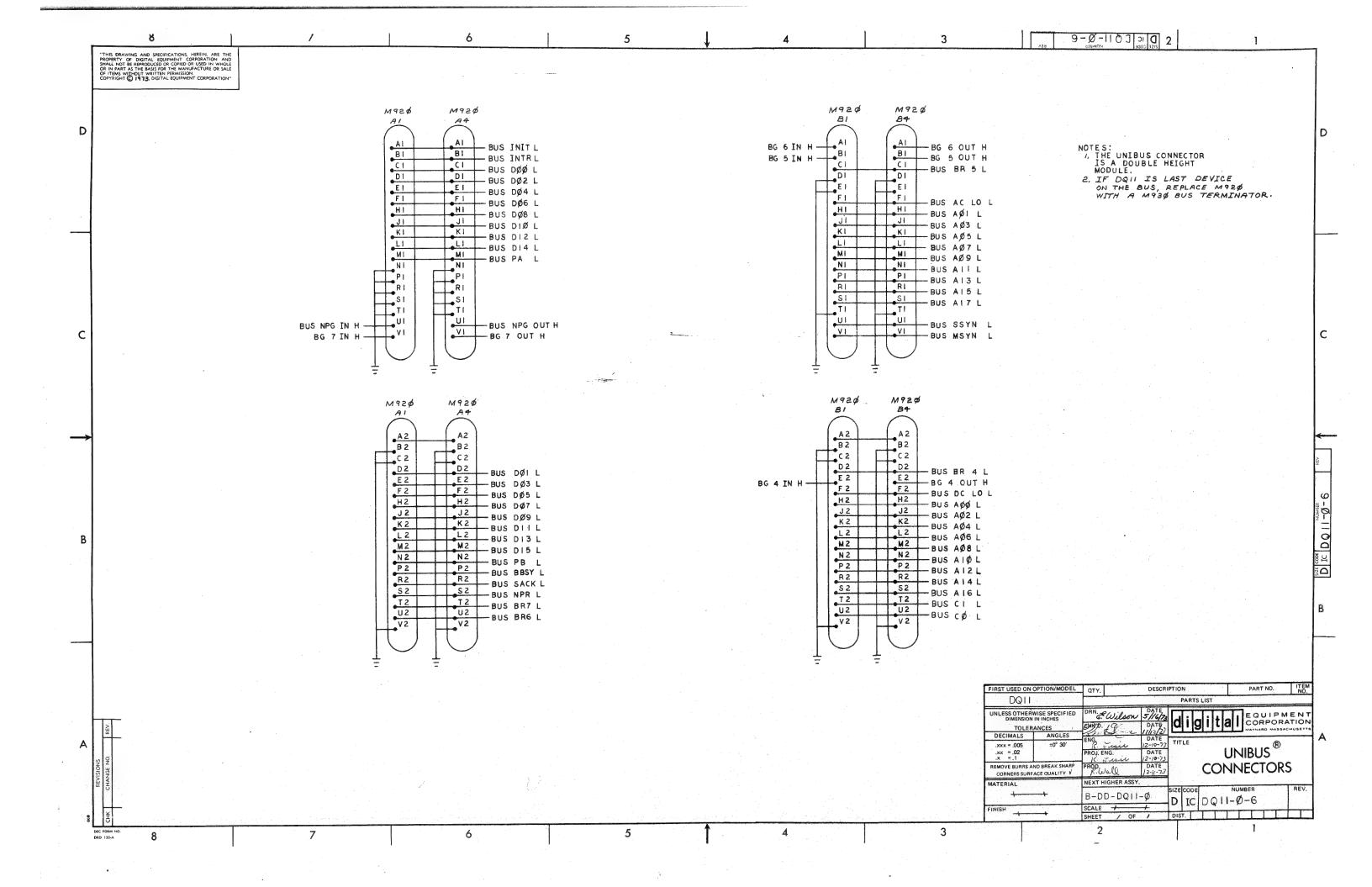


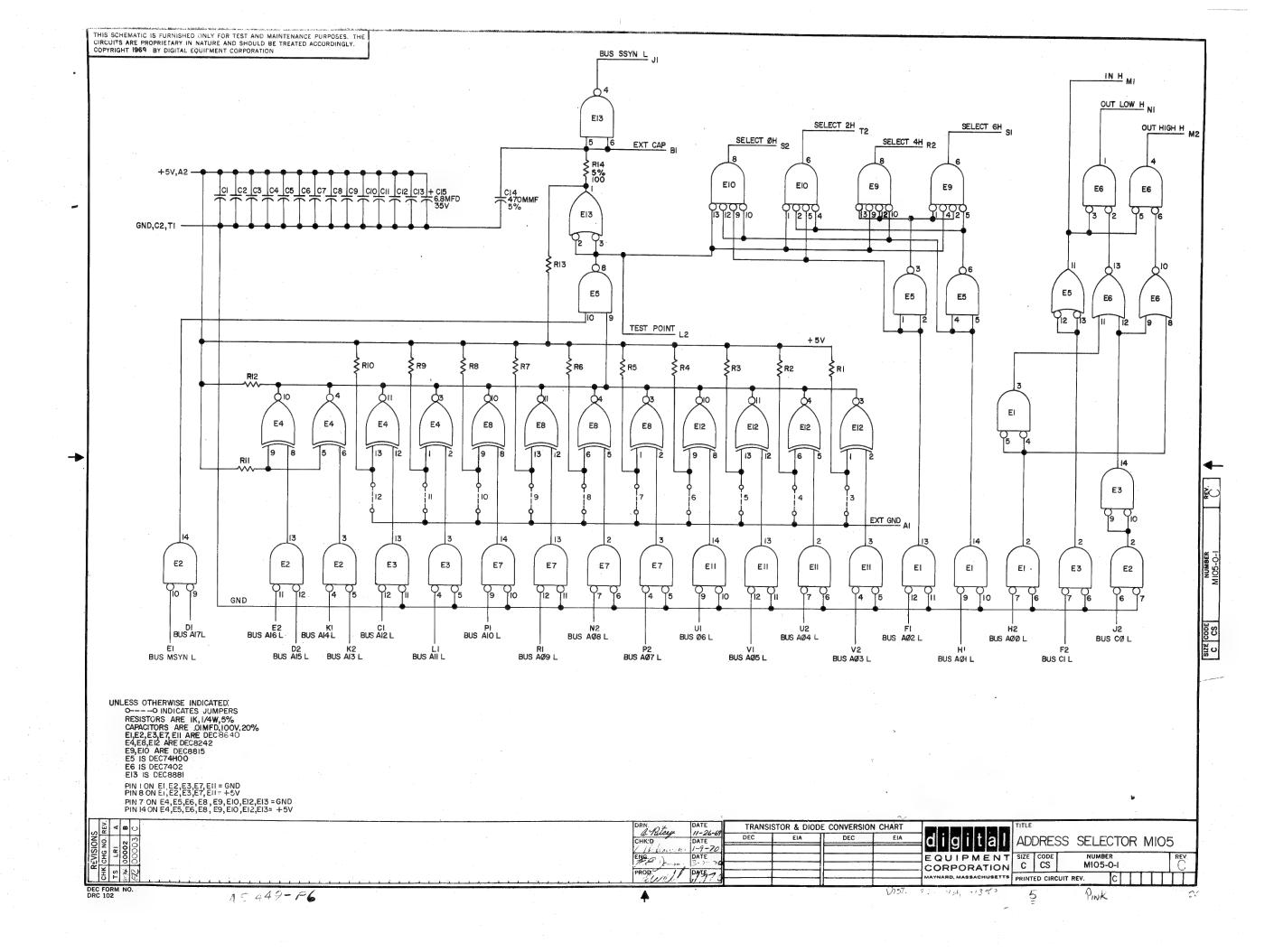


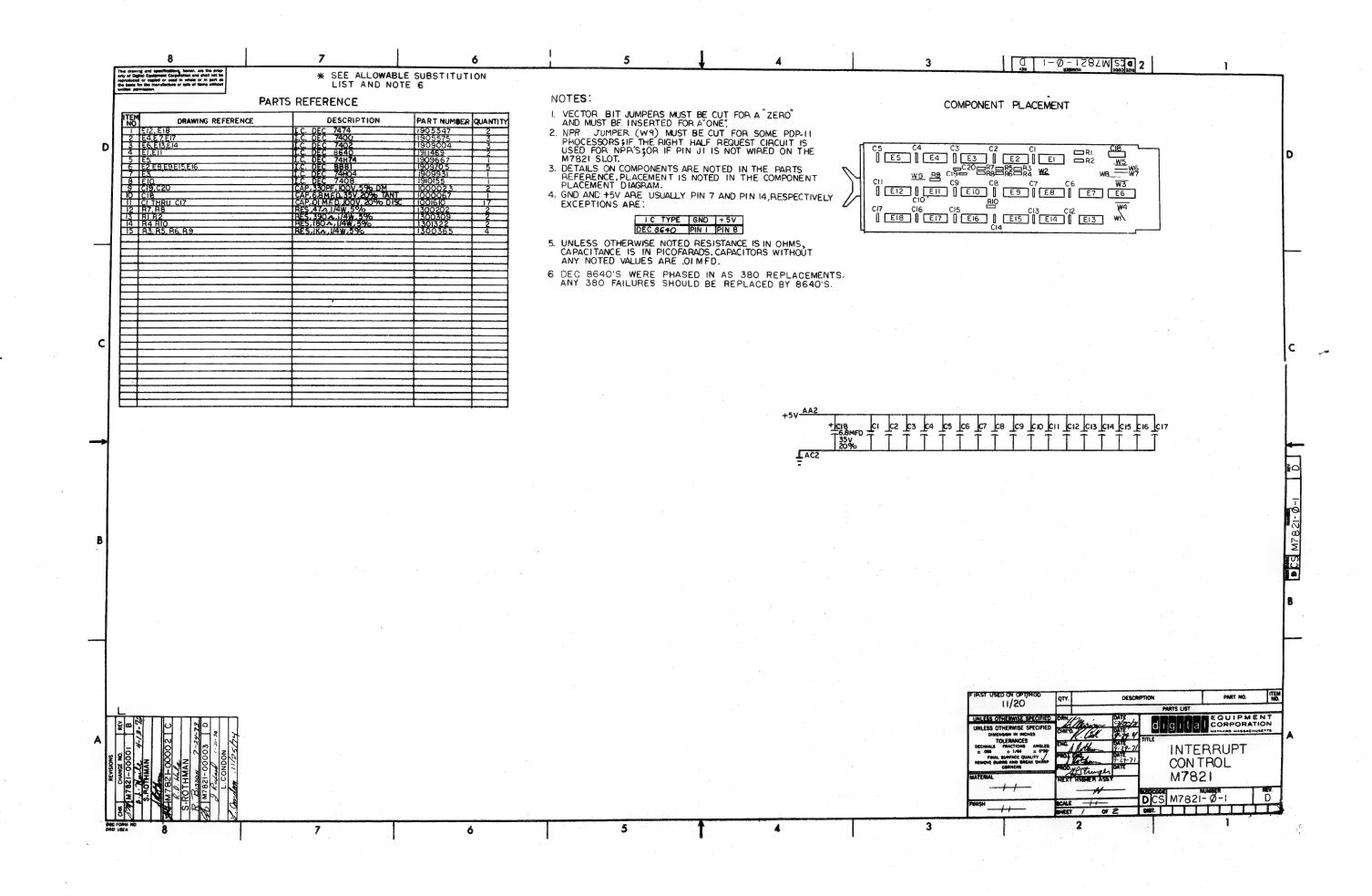


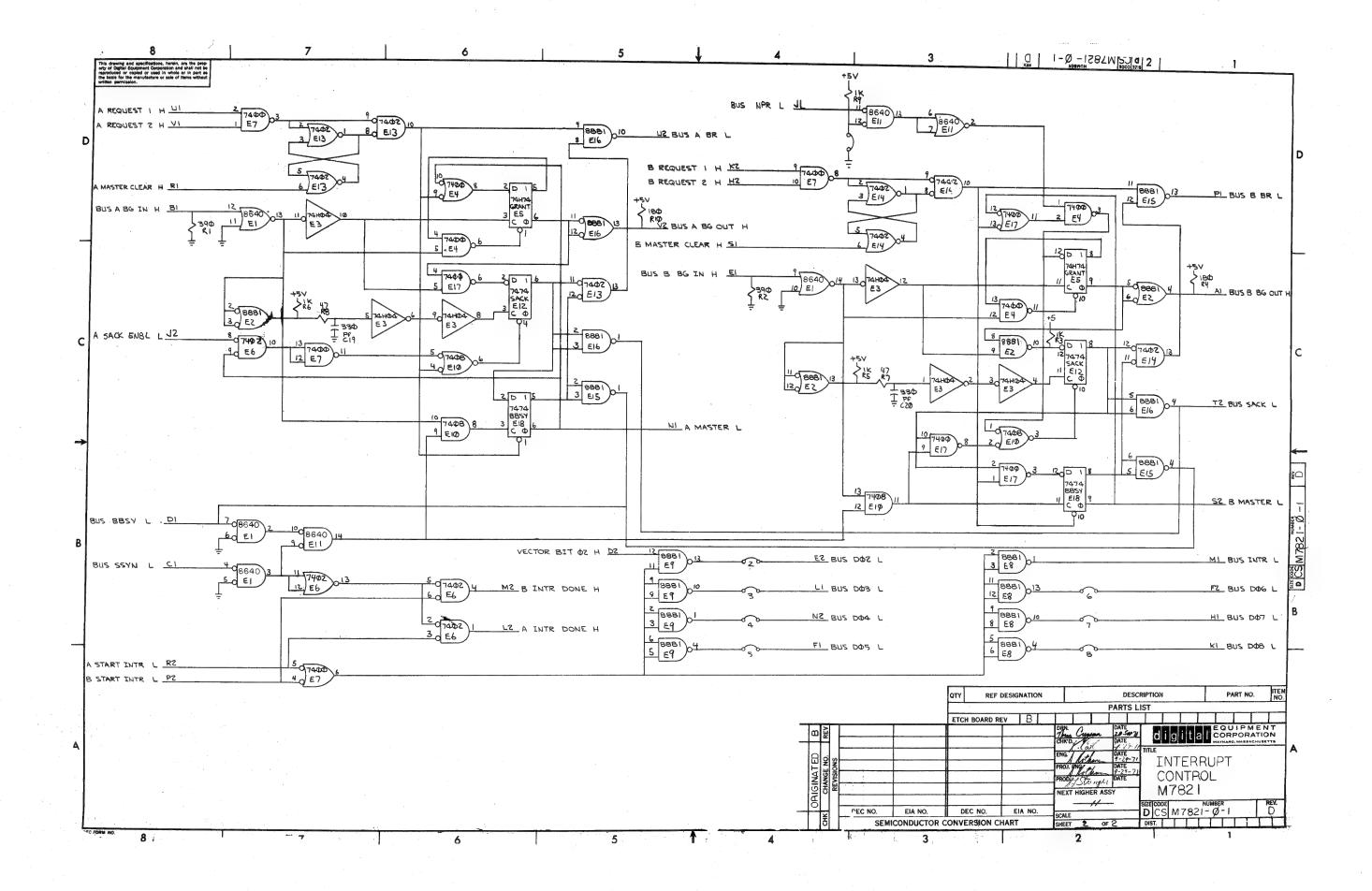


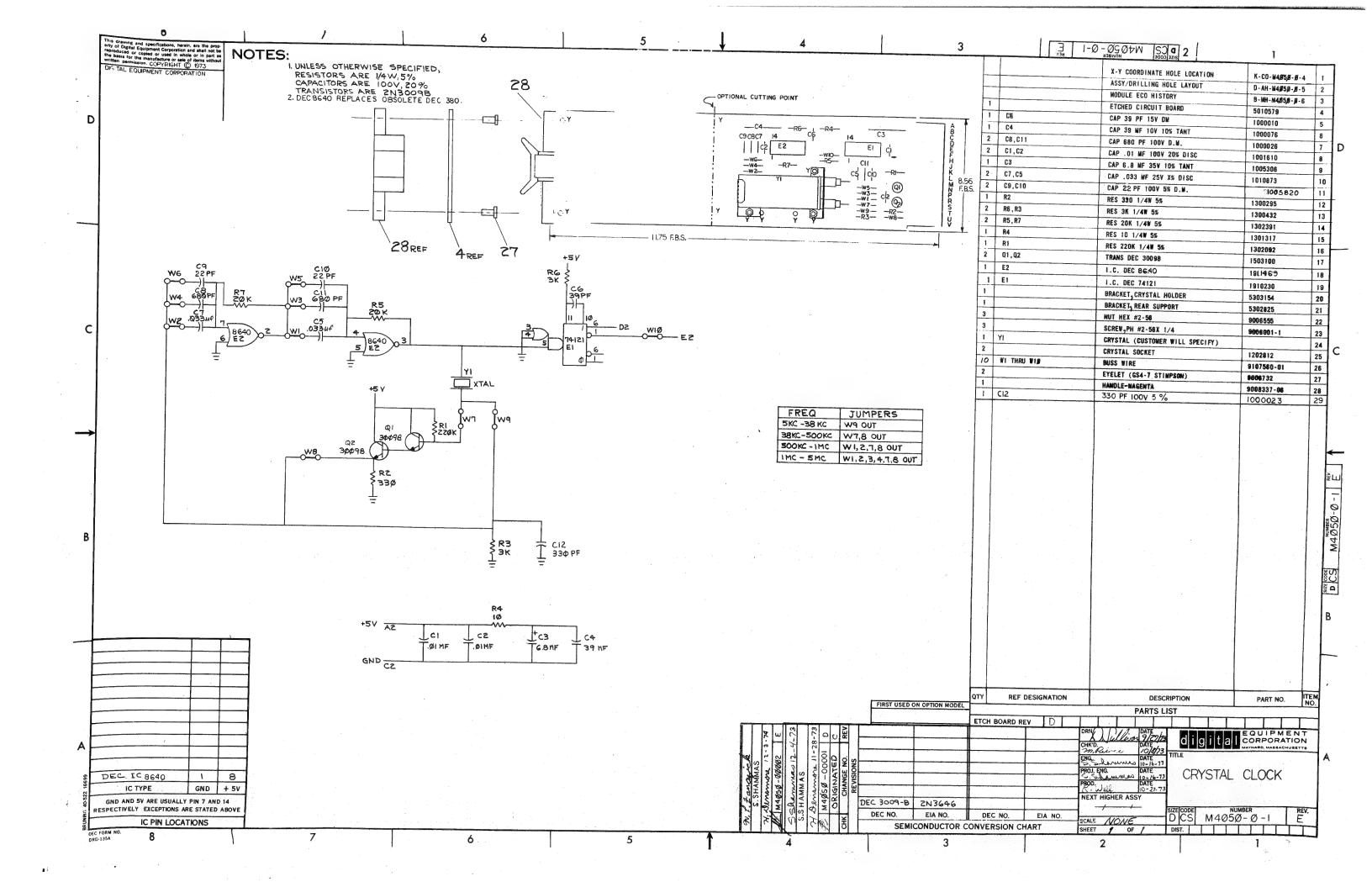


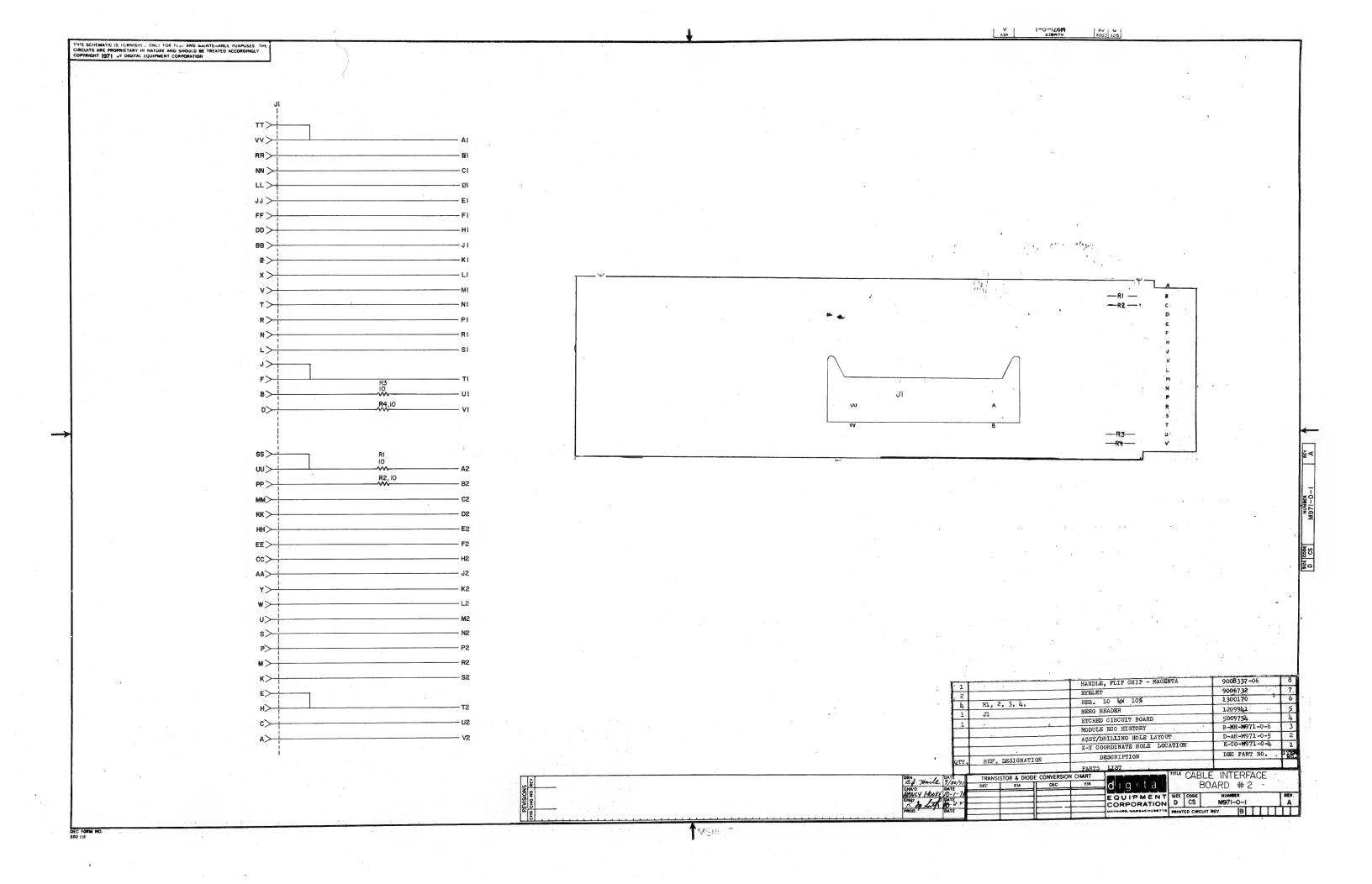


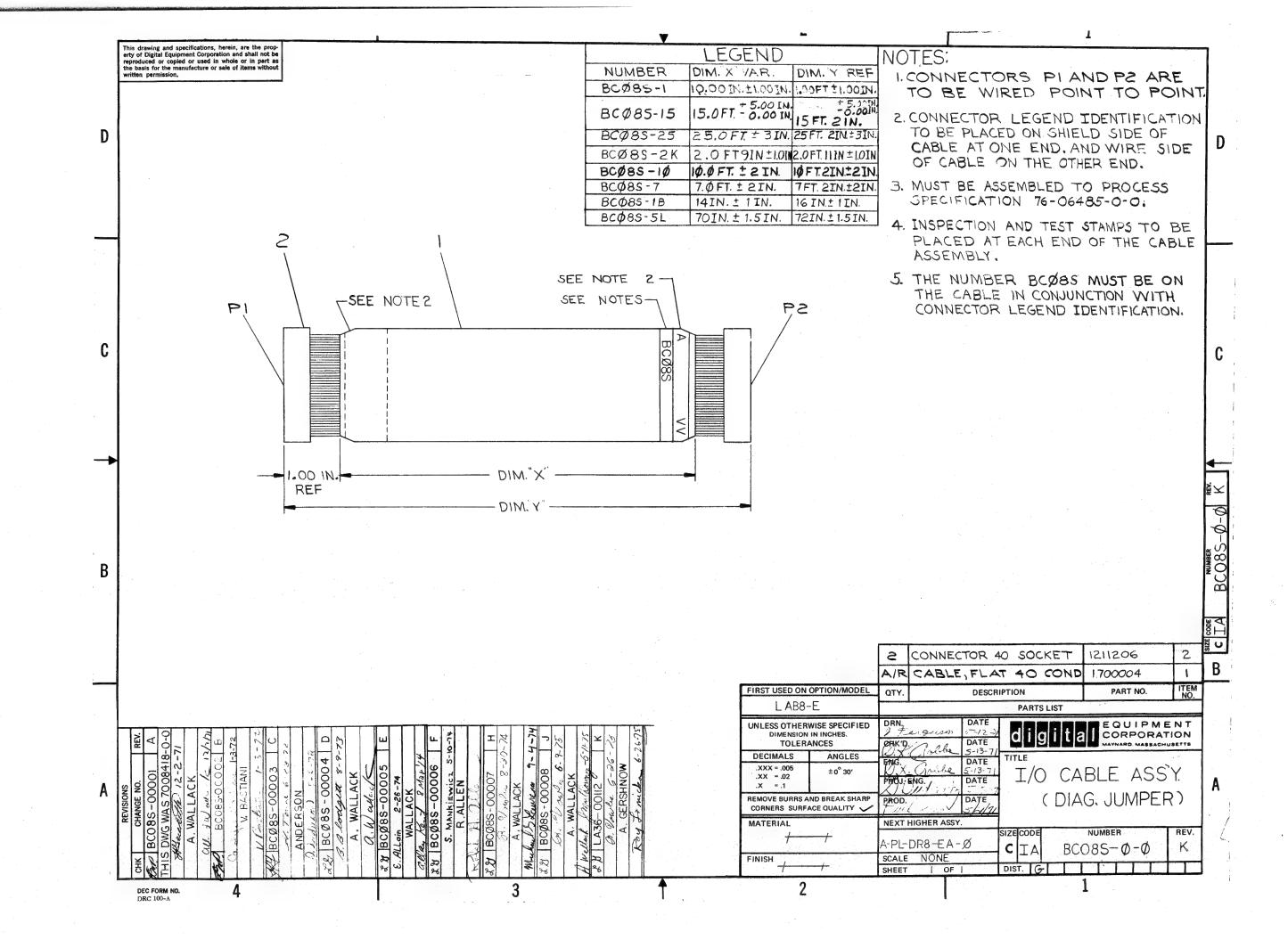


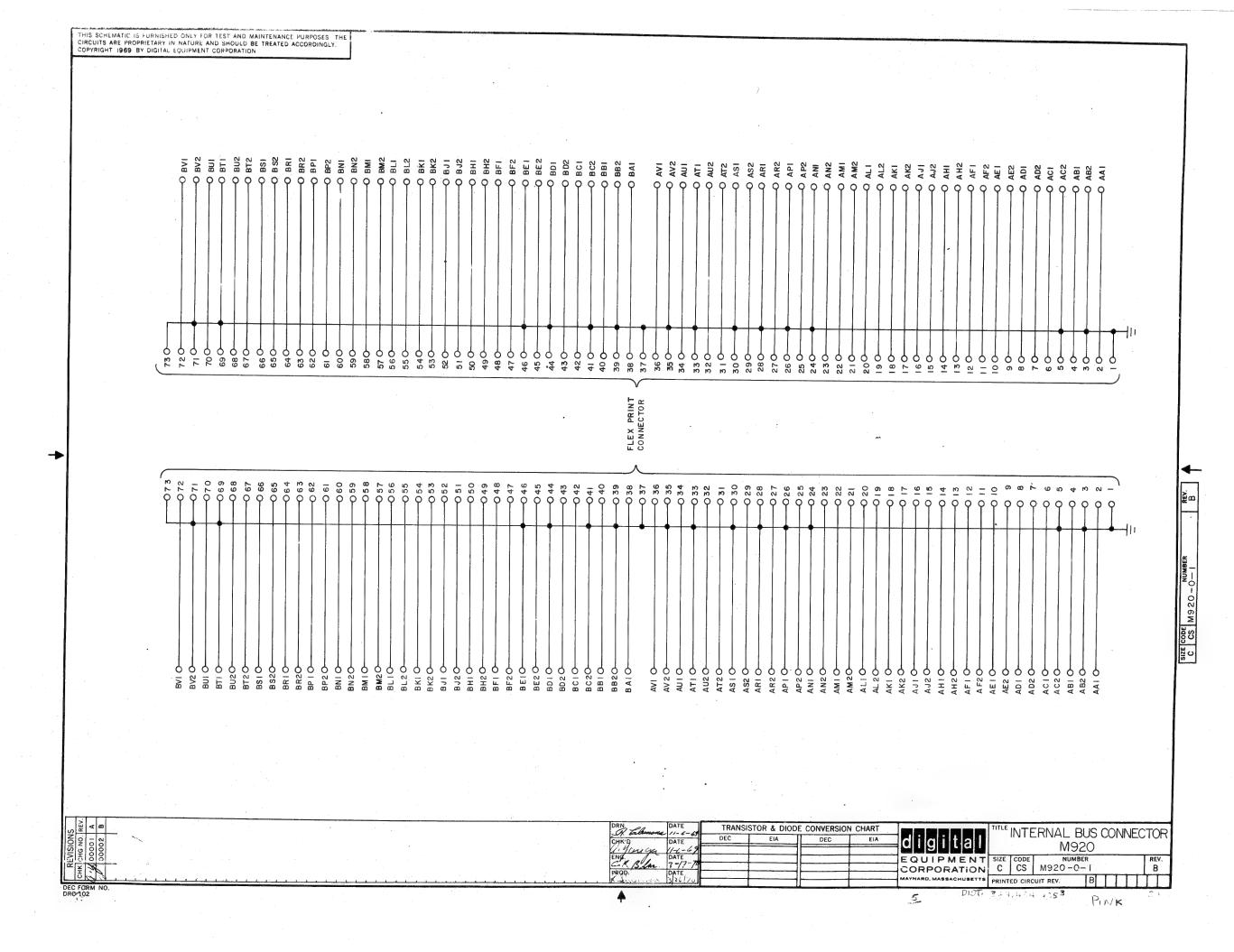


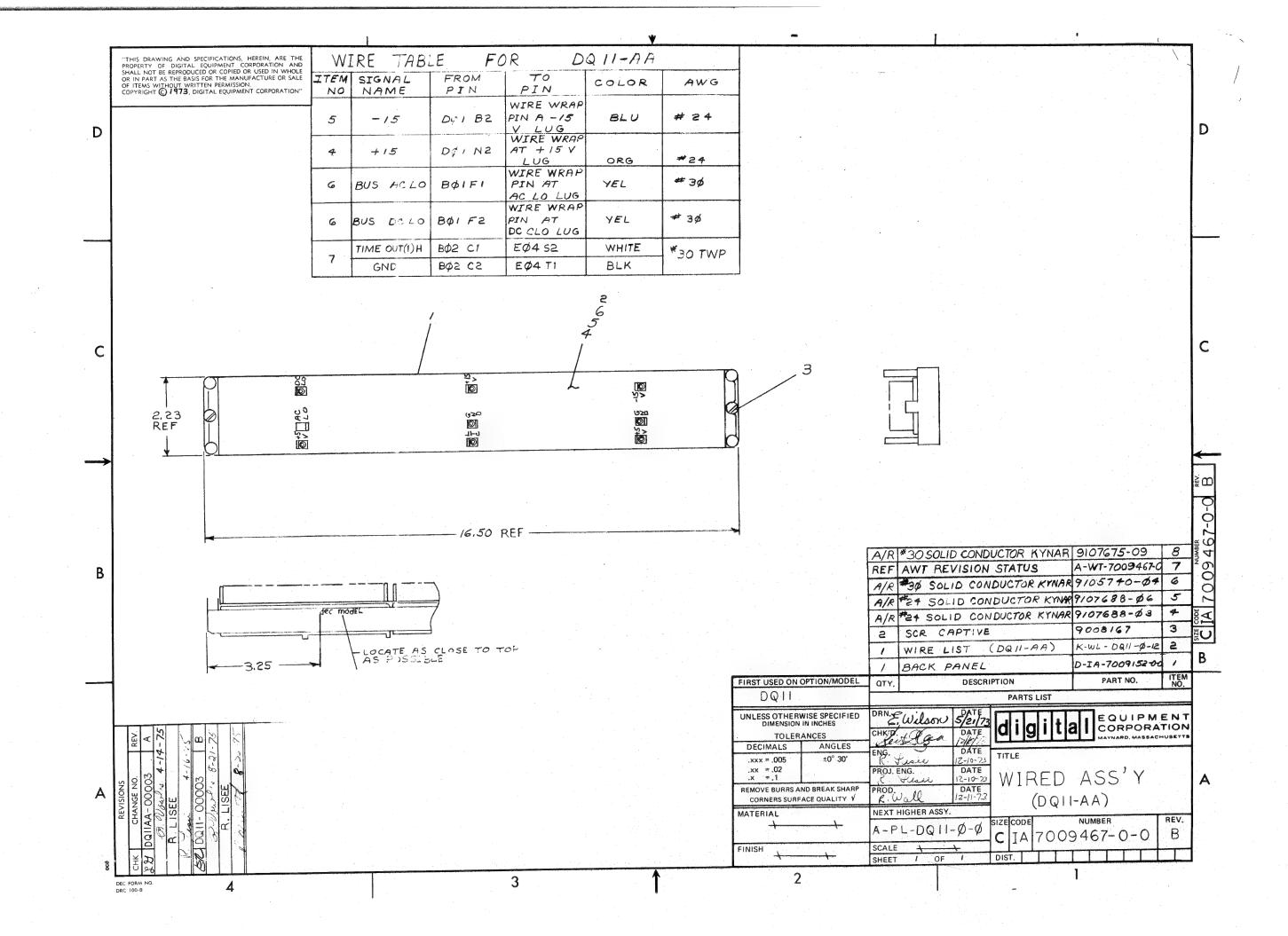




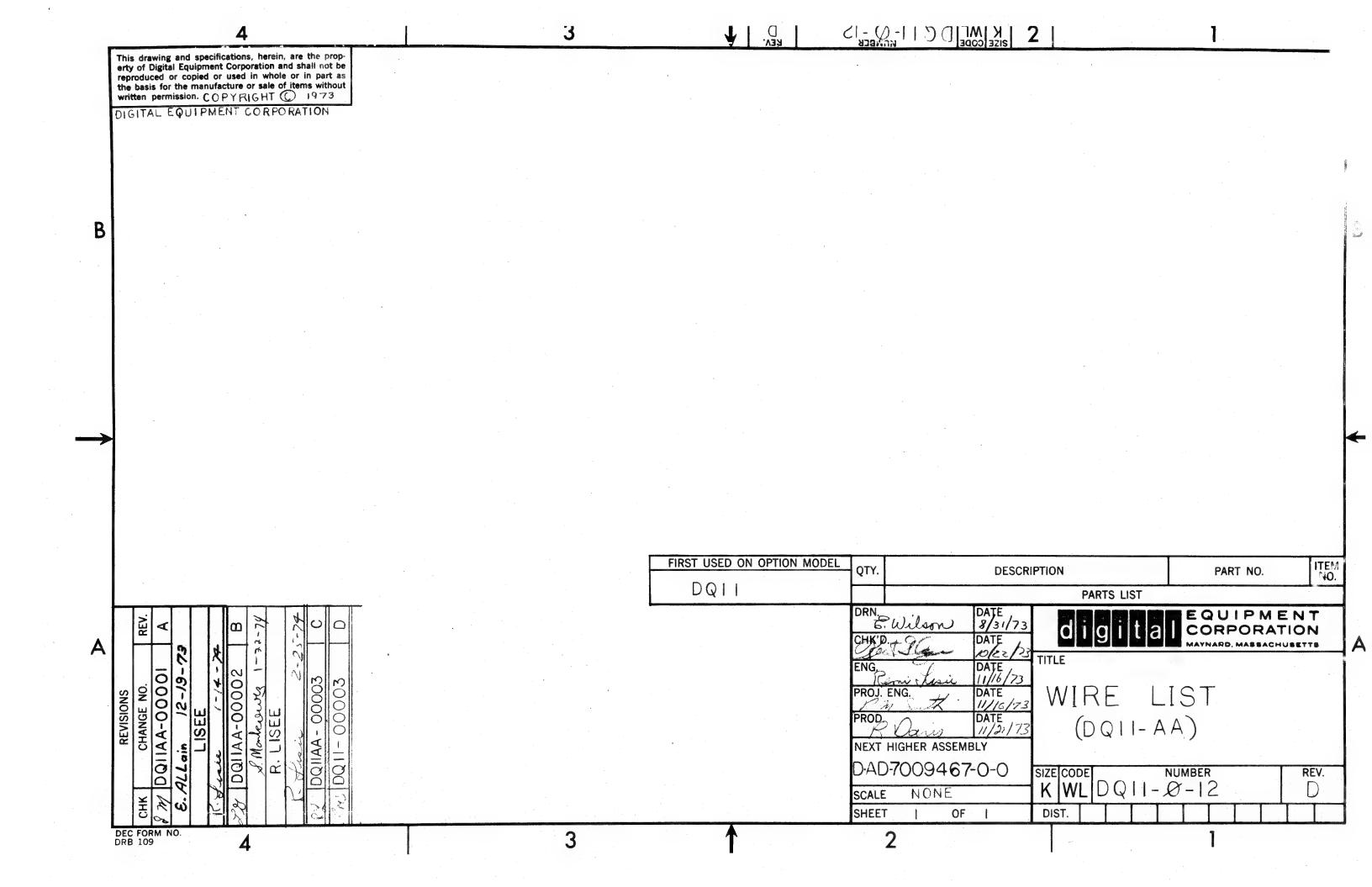








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30-AUG-75 9:4 PAGE 1 LENGTH EXCEPTIONS RUN NUMBER 1-PIN RUN 21=6/8
2=3
6=2 3*6/8 2=6/8 21-4/8 23=5/8 0000 0001 0011 0000 0000 4 11 11 10 0000 0000 1000 1000 1000 1000 1100 1100 561 561 ** ** HND288 V23(23) Ø5/24/74 A/P PIN ORDER BAY --NAME PIN ORDER A01A2 A04A2 D91N2 DØ1A1 DØ1B2 801A2

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914 EXCEPTIONS								
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914 EXCEPTIONS														4																			
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914 EXCEPTIONS									
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9:4 EXCEPTIONS										
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RV PG Y										
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13) 05/24/74 10ER BAY -	* * *	1 = 01 + 1	* * *	***	* * * *	1	1 6 6 1 1 6 6 2 1 4 4	1 1 1 1 0000 1 1 1 1 1 00000	1 - 6 - 1	14.02
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HND288.V2. PIN NAME	A01M2 A02N1 A04M2	B01F2 B04F2	A01A1 A04A1 D03E1	A0181 C0481	7017 7047 7047 7017	A01U1	A04U1	A 0 1 5 2 5 2 5 2 5 5 5 5 5 5 5 5 5 5 5 5 5	A01M1	A01N2 A04N2
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914 EXCEPTIONS									ž.	
30"AUG-75 Length	11 8 4 / 88	20-6/8	4 8 12 2 8 18 8	8/0=5	8/0=6	8/9=9	9-4/8	8	5=4/8	
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23) PIN										
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PAGE 9 RUN NUMBER	444	12 TO	996	rrr rr	00 00 00	000	00 00 00 00 00 00	00 00 00 00 00 00	CV CV CV	60 60 60 FE FE FE	00 00 00 4a 4a 4a
9:4 Exceptions											
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g											
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3(23) 05/24/74 ORDER BAY PIN ORDER									•		
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PAGE 10 RUN NUMBER	10 10 00 00 00 00	00 00 00 00 00 00	C C C C C C C C C C C C C C C C C C C		တ် စာ ဇာ ဝ. ဝ. ဝ.	8 8 8 8 8 8	6시 6	0 0 0 0 0 0	ଲ ୀଲ ଲେ ଲ ପ୍ରଧାର ବାଦ	् के के के के के का का का का
914 PEXCEPTIONS										
30-AUG-75 Length	3-6/8	3=6/8	3-2/8	3 = 6/8	8/0=6	11-4/8	7 8 / 8	5=2/8	16-0/8	16-0/8
REMARKS										
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05/24/74 R BAY •	* *	1011	**	1 = 01 + + + + + + + + + + + + + + + + + +	**	1 = 61 + + + + + + + + + + + + + + + + + +	11.00.1	111	* * *	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
(23) PRDE		•								
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	TO SEND TO SEND		555	222			D D D O O O O O O O O O O O O O O O O O	PYSTAL CLOCK PRYSTAL CLOCK PRYSTAL CLOCK	~ ~ ~ ~	pic die die die die die
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DO11AA.E	777	* * * * * * * * * * * *	000	777		300	#### ####	3 - 1 - X	EEEE	****
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PAGE 11 RUN NUMBER	or ou ou ou no no no no no no no no	9 6 6	0000 LLL L	0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,	\$ \$ \$ \$ \$	1000	1001	102 102 102	1001	4444
914 EXCEPTIONS										
30-AUG-75 Length	13=2/8	14=0/8	14=6/8	16=4/8	15=6/8	3 = 6 / 8	918	2 = 2 5 = 1 / 8	4 = 6 / 8	14-0/8
REMARKS							•			
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ø										
05/24/74 R BAY = ORDER	* * *	111111111111111111111111111111111111111	11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	* * *	**	1-81	1 - 0 - 1 - 0 - 1 - 0 - 1 - 0 - 1 - 1 -	1 - 60 - 1	
23(23) ØS ORDER PIN										
HND288.V2 PIN NAME	CO3U1 CO2S1 FOIN1	B0232 F0152	T02E2 T01C1 T01T1	802X2 C03V2 F01R2	60332 C02H1 F01S1	A02A1 A03L2	A02C1	A03P2 B02J1	A03M2 B02F2	80311 C0212 E0111
A/A	m m m	no no	50 50 50	pe pe pe	DC DC DC	tott tott	cer tec		per bet	ne ne nè
	2222 2222 2222	5 6 6	2222	4444	96 96 96					
AA Me	TOO TOO TOO TOO	SEL SEL SEL SEL	S S S S S S S S S S S S S S S S S S S		8 8 8 8 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	0000	001 001	002	000	0 0 0 0 4 4 4 4
POST1A RUN N	03-1 03-1 03-1	03*1 03*1 03*1	033	003-11	03.1 03.1 03.1	444 1111	04*1 04*1	04*1	1-40	0000

PAGE 12 RUN NUMBER	1 1 9 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	196 196 106	107	11 11 11 12 12 12 12 12 12 12 12 12 12 1	1100	1110		222	लाला लाला जाजा जाजा जाजा जाजा जाजा जाजा	च चच च स्टूबा स्टूबा स्टूबा
914 EXCEPTIONS										
30-AUG-75 Length	2-4/8	4 = 5 / 8	5=2/8	15 E 6 / 8	16-6/8	5+2/8	5 * 2 / 8	4=0/8	1 t s	11=2/8
REMARKS										
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05/24/74 BAY - ORDER	1-01	**	1 = 61	* * *	***	1 0 0 1 1 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1	1 6 0 1 1 6 0 2 1 4	1 = 02 + +	***	* * *
(23) ORDER										
HND288,V23	B02D2 B03C1	A0231 B0301	A0281 B0381	802E1 803M1 E01E1	A0201 B03P2 E01M2	8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	803N2	C02P2 C03B1	C0382 C02N1 E01P2	000 000 000 180 180
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DOILAA.D RUN NAME	1-40	04-1 04-1	1-40	04422	00 440 5440 5440	400	440	04-2	4440	0444

NAME												
NAME	PAGE 13 RUN NUMBER	and and and and	#### ####	Pro Pro Pro and and and and and and		****	120 120 120	2000	1222	123 123 123	124	125
NAME												
1111A.D NAME NAME NAME PIN NAME NAME PIN NAME PIN NAME NAME PIN NAM	30-Aug-75 Length	11-6/8		4 4 / 8	3 s s s s s s s s s s s s s s s s s s s	S = 2 / 8	4. 8.	11-2/8	10-4/8	2=6/8	80 60 60 60	9
1111A.D NAME NAME NAME NAME PIN NAME PIN NAME NAME PIN NAM	REMARKS											
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111AA.D NAME NAME NAME NAME NAME PIN N	×											
1111AA D NAME A/P PIN NAME P	74 PG	* 0.00	80 A4	008	en en	602 m26	en 45.	69 69	47 47 (S)	80 80 80 80 80 80	- CD	0 0 0 0 0 0 0
### ### ##############################	G											
### ### ### ### ### ### ### ### ### ##	/24/74 BAY - ORDER	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1-01	6 6	* *	**	110011	* * *	* * *	* *	**	* *
4-2 D15 4-2 D15 4-2 D15 4-2 D15 4-3 D15 4-3 D15 4-3 DB MUX SEL 4-3 DB MUX SEL 4-3 DB MUX SEL 4-3 DB MUX SEL 4-3 DB CC 24-16 (1) 10 D0 AB 10 D0 AB 10 D0 AB 10 D0 AB 11 D0 A	C23) DRDE											
4-2 D15 4-2 D15 4-2 D15 4-2 D15 4-3 D15 4-3 D15 4-3 D15 4-3 D17 4-3 D1	e 61	H X C	20 24 II	028 04A	X 0	20 20 20 20	927 837	000 000 0000 0000	2 2 2 2 2 2 2 2 2 2 2 2	20 20 20 20 20 20	0 0 X	7 X
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PAGE 14 RUN NUMBER	1112 126 126 126	721 721 721 721 721	128 129 129	130 130 00 130		322	ლოლ ლოლ ლოლ 		ഹനെ ഹനെ നേനാ നന നെന്നു	9 9 9 9
914 Exceptions			1-PIN RUN							
30-AUG-75 Length	8/0=8	11-2/8	3/0=6	4-6/8	16=6/8	8-2/8	4-6/8	4=4 9=5/8	11=0/8	14=0/8
REMARKS										
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(23) 05/24/74 ORDER BAY - Q PIN ORDER	1 = 01 + D14 + D14 + D13	* * * * * * * * * * * * * * * * * * *	* * % %	* *	* * * *	* *	* *	* 601	### ### ##############################	* *
38,V23(23) 05/24/74 V ORDER BAY * Q	0341 1=01 # D14. 02U2 1=02 # D08 01E2 1=03 # D13	* * * * * * * * * * * * * * * * * * *	* * % %	* *	* * * *	* *	* *	* 601	### ### ##############################	* *
88,V23(23) 05/24/74 N ORDER BAY - G ME PIN ORDER	0341 1=01 # D14. 02U2 1=02 # D08 01E2 1=03 # D13	11 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	02V1 02L1 1=01 * 03F1 1	0352 1=01 * 0252 1=02 *	11.1.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.2.	02E1 1=01 # 01K1 1=02 #	02R2 1=01 + 03M1 1=02 +	03E1 1-01 * 02U1 1-02 *	H F04A1 1=01 * D0 H F04A1 1=02 * D0 H F03N1 1=03 * D1	03D1 1-01 *
HND288,V23(23) 05/24/74 A/P PIN ORDER BAY * Q NAME PIN ORDER	D03A1 1=01 * D14. D02U2 1=02 * D08 E01E2 1=03 * D13	DO4C1 1=01 * DO4 E02D1 1=02 * D08 E01D2 1=03 * D13 E03M1 1=04 * D14	UNDEFINED 02 H C02V1 VRC (1) H D02L1 1=01 * VRC (1) H D03F1 1=02 * VRC (1) 1	IDLE MODE (1) H D0352 1=01 * IDLE MODE (1) H E02P2 1=02 * IDLE MODE (1)	LD RX 15=8 H C02J2 1=01 * LD RX 15=8 H F04B1 1=02 * LD RX 15=8 H F04B1 1=03 * LD RX 15=8 H F03M2 1=04 *	LD TX 7/0 H COZE1 1=01 * LD TX 7/0 H E01K1 1=02 * LD TX 7/0	RX GO (1) H E02R2 1-01 * RX GO (1) H F03M1 1-02 * RX GO (1)	RX/CHAR INTR L GOZUI 1-01 * RX/CHAR INTR L GOZUI 1-02 *	STRIP SYNC (1) H D02K2 1=01 * D0 STRIP SYNC (1) H F04A1 1=02 * D0 STRIP SYNC (1) H F03N1 1=03 * D1	TX GO (1) H A03D1 1=01 * TX GO (1) H E02N2 1=02 * TX GO (1)
HND288,V23(23) 05/24/74 A/P PIN ORDER BAY * Q NAME PIN ORDER	EST LOOP (1) H D03A1 1=01 + D14. EST LOOP (1) H D02U2 1=02 + D08 EST LOOP (1) H E01E2 1=03 + D13 EST LOOP (1)	CLOCK H E02D1 1=01 * D04 CLOCK H E02D1 1=02 * D08 CLOCK H E01D2 1=03 * D13 CLOCK H E03M1 1=04 * D14 CLOCK	CFINED 02 H C02V1 1=01 * (1) H D02L1 1=02 * (1) (1) H D03F1 1=02 * (1)	DLE MODE (1) H D0382 1=01 * DLE MODE (1) H E02P2 1=02 * 1 = 02 *	4-4 LD RX 15-8 H C0202 1=01 * 4-4 LD RX 15-8 H E01M1 1=02 * 4-4 LD RX 15-8 H F03M2 1=03 * 4-4 LD RX 15-8 H F03M2 1=04 *	4-4 LD TX 7/0 H C02E1 1=01 # 4-4 LD TX 7/0 H E01K1 1=02 # 4-4 LD TX 7/0	X GO (1) H E02R2 1-01 * X GO (1) H F03M1 1-02 * X GO (1)	HAR INTR L COZUI 1=01 # HAR INTR L COZUI 1=02 #	TRIP SYNC (1) H D02K2 1=01 * D0 TRIP SYNC (1) H F04A1 1=02 * D0 TRIP SYNC (1) H F03N1 1=03 * D1 TRIP SYNC (1)	GO (1) H A03D1 1=01 * GO (1) H E02N2 1=02 *

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PAGE 15 RUN NUMBER	137	138	139	444	444	142	143	1444	444	444	141	444
914 EXCEPTIONS									,			
30-AUG-75 Length	8/9#6	2=6	8/0-8	8/0=8	8 2 8	8/9*9	3=1	7=0/8	2-7	8-0/8	3-4/8	3*6/8
REMARKS												
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05/24/74 R BAY -	1-01	1-01	1-01	111	10-11	1 - 61	1111	**	1 - 0 1	1.01	1-01	100
(23) ORDE PIN												
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A/P	ដដ	20 20	亚 亚	ææ	ææ	min	tati tati .	m m	pc pc	min	* *	x x
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DO11AA RUN NAN	4-40 4-40	04-5 04-5	440 8 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	000 444 888	000 440 111	240 240 8 8 8 8	0 44 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	240 8 4 4 0	440 844	000 440 111	000 444 111	24 + 40 8 + 40 8 + 40

PAGE 16 RUN NUMBER	444 999	158 158 158	151	152 152 152	153 153	20 20 20 20 20 20 20 20 20 20 20 20 20 2	មាស់ មាស់ ស្រីសាស សាសាសា	25 60 60 60	10.00	60 60 60 60 80 10 ont ont ont	159 159 159	160
914 EXCEPTIONS												
30-AUG-75 Length	3-6/8	4*4/8	3=4/8	4-2/8	6/4=4	8/9=8	8/0=6	8 / 9 # 8	9=2/8	4"2 9=1/8	9*2/8	8/0=6
REMARKS												
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RV PG Y												
DRAW I	010	010	518 584	518	D18	0 0 4 4 5 1 5 6 4 5 6 5 6 5 6 5 6 5 6 5 6 6 6 6 6 6	010	0 1 0 0 4 4	010	016	010	D18
ø												
05/24/74 BAY = ORDER	1 = 01 + 1	1 = 01 = 1	1-01 * 1-02 * 1	1 = 01 + 1 = 02 + +	1-01 +	1 8 8 2 1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	1 = 01 +	1=01 +	1=01 * 1=02 * 1	1=01	1 = 01 1 = 01 1 + +	1 1 1 2 2 1 4 *
3.V23(23) 05 ORDER PIN												
HND288.V2 PIN NAME	F02N1 F04K2	F02V1	FØ2R2 FØ4H2	F02P1	F02U1 F04E2	D04E1 F02N2	802P2 E04A1	B02U2 E04C1	B0251 E0401	802V1 E04E1	BØ2U1 EØ4F1	802V2 E04H1
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PAGE 17 RUN NUMBER		222		0 0 0 4 4 4	ស ស ស ស ស ស	166 166 166	167 167 167	1 6 8 1 6 8 1 6 8	444 69 69 69	170	<u>सुन</u> न	000
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9:4 EXCEPTIONS												
914 EXCEP								•				
-75 Length	10-4/8	10-6/8	11-6/8	8/8	8/0	00	2/8	8/8	9-4/8	8/8	7=2/8	80
30-AUG-75 Len	0	50		13-0/8	12-9/8	12-0/8	12-2/8	12-6/8	. 6	13-0/	-	8-6/8
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PAGE 18 RUN NUMBER	173 173 173	174	175 175 175	176 176 176	771 771 771 771	178	1111 179 179 179	© © © © © © ∞ or or	ණේ දාහ හැකි ර ්ධ ද්ධි ද්ධි ණේ ගෙම ගැනි	44 44 40 00 00 00 00 00 00 00 00 00 00 0	24 24 24 24 24 24 24 24 24 24 24 24 24 2	
914 Exceptions						,						
30-Aug-75 Length	9*2/8	4=5 9=7/8	3=5	5=1	2=0	4=4/8	16-2/8	11+6/8	13=0/8	10=4/8	10-2/8	
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3(23) ORDER PIN												
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PAGE 19 RUN NUMBER	184 184 194	24 44 44 80 90 90 80 80 80	186 186 186	187 187 187	11 44 41 30 90 90 90 90 90	20 00 00 00 00 00 00 00 00 00 00 00 00 0	198 198 198	191	192 192 192	193 193 193	1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	0 0 0 0 0 0 0 0 0
9:4 EXCEPTIONS												
30-AUG-75 LENGTH	10=0/8	10=0/8	9=4/8	4=3	2=5 5=7/8	2 * 4 5 * 5 / 8	2=6	8 4 8	8/0=6	N	1=6/8	6=4/8
REMARKS												
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S(23) ORDER PIN												
HND288.V2	A03R2 D02J1	A0351	A03T2 D02H1	A0352 D02A1	C03H1 D04H1	C0332 D0431	C03F1 D04K1	B03F2 D02M2	803E2 D02P1	B03J2	B03H2 D02F2	B03U2 D02F1
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DO11AA.D RUN NAME		H A/A	HND288,V2	3(23) ORDER PIN	05/24/74 BAY = ORDER	O DRAW	IW RV PG Y	×	REMARKS	30-AUG-75 Length	9:4 Exceptions	PAGE 20 RUN NUMBER	
DS-2 CC/BA 13 DS-2 CC/BA 13 DS-2 CC/BA 13		ææ	B03T2 002E2		1-01	014 986	7.0	~4		6 4 / 8		196 196 196	
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05-2 CC/BA 15 05-2 CC/BA 15 05-2 CC/BA 15		= =	803V2 D02N1		* * * * * * * * * * * * * * * * * * * *	014	514.2 506	ਦ 1 .		7-2/8		1 1 9 8 1 9 8 1 9 8	
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055.22 055.22 055.22 057.23 057.23 057.23 057.23	444 444 444 445 (44) (44) (44) (45)	EEE	00000 00000 00000 00000		1 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	000 4400	0 8	~		10=6/8		00000	
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DS-3 NPR ROST DS-3 NPR ROST		EE	A03V2 C04U1		1 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	4.60	en ** en			3 = 6 8 = 1 / 8		208 208 208	

PAGE 21 RUN	2000 3 2000 3 2000 3 2000 3	2000 000 000 000 000 000 000 000 000 00	0 0 0 0 0 0 0 0 0	222	2222	212	222 213 213	2222	2223	215 216 216	2117 2117
914 EXCEPTIONS											
30-AUG-75 Length	13=0/8	3=5	10=2/8	8/0=6	2=4/8	3*6/8	4-2/8	8.4.8	80 99 80	4 8 / 8	S = 2/8
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3(23) 05 ORDER 51%											
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PAGE 22 RUN NUMBER	0.00 00 0.00 00	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	222	22222	222	333 553 553	4 4 4 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	6	\$ \$ \$ \$ \$ CH CH CH CH CH CH CH CH	2222	222 222 222 222 222 222 222 222 222 22
914 Exceptions	,										
30-AUG-15 Length	8/9-4	8/0=9	1-0/8	2=1	8/0°B	8/9-7	9 * 4 / 8	2+5	3*2	7-2/8	3 # 6 8 # 1 / 8
REMARKS											
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BAY - ORDER	1-01	1 = 01 + 1	1-01 +	* * * * 	11-601-	1-01	1=01 *	1 - 0 2 + +	***	**	111111111111111111111111111111111111111
3(23) 05/ ORDER PIN	9 2 2	* *	11 1 1 8 8 1 1 1 1 1 1 1 1 1 1 1 1 1 1	ज न न न न	* *	ਜ ਜ ਜ	ज ज ज	च क्यं ज्यं	ज़र्न को को को	ज ल ज	
3.V23(23) 05/ ORDER E PIN	9 2 2	1 - 61 + 1 - 62 + 1 - 62 + 1 - 62 + 1	D03M1 1-01 + F02D1 + 1		A0212 1-01 *	D03J1 1=01 * 1=02 * 1		C03P1 1=01 * D02R1 1=02 *	D03J2 D04V2 1=02 # F02H1 1=03 #	C03A1 1=01 # D04U1 1=02 #	D03H2 1-01 * 1-02 * 1-02 * 1
3.V23(23) 05/ ORDER PIN	02S2 1-01 03M2 1-02	03L2 1 04U2 1	SH CNT H DØ3M1 SH CNT H FØ2D1 SH CNT	H A03H1 1 H B02A1 1 H D04V1 1 H E01V2 1	02T2 03V1 1	03J1 1	0202 . 1 03U2 . 1		03J2 04V2 1	04U1 1	D03H2 F02F1
HND288,V23(23) Ø5/ P PIN ORDER NAME PIN	1 -02 1 1 -02 1 1 -02 1 1 -02 1 1 1 -02 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	D03L2 1	CNT H DØ3M1 1 CNT H FØ2D1 1 CNT	H A03H1 1 H B02A1 1 H D04V1 1 H E01V2 1	H A02T2 H C03V1	D0341 E02E1	A0252 . 1 C03U2 1	C03P1 1	D03J2 D04V2 1 F02H1	COSP1 DO4U1 1	D03H2 F02F1

PAGE 23 RUN NUMBER	3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5 5	231 231 231	232	222	666 866 444	2233	2336 2336 2336 2336	237 782 783	238 238 238	23 9 23 9 23 9
914 EXCEPTIONS											
30-AUG-75 Length	6 m 9 6 m 9 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	10-6/8	11-2/8	5=4/8	9/0=1	8/0=8	4-4/8	10=4/8	4 / 8	8/9=6	80 / 40 8
REMARKS	:										
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RV PG Y				•							
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G				1.1							
05/24/74 BAY - ORDER	1 = 01 + 1	1-01	1001	1-01	111	111	2	1111	1-01	1 6 6 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	1 1 1 0 0 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1
3(23) 05 ORDER PIN											
HND288.V2 PIN NAME	C0312 F02A1	704E1	C04A1 F03H1	BØ302 CØ4H2	C04K2 E03A1	803E1	FØ4R1	C0202 D04J2 E03C1	DØ3V2	B02M2 E03F1	802N1 E03F2
A/P	xx	5E 5E	pc pc	22	ææ	20 30	# # ~ ~ ~ ~	mmm	22	22	a a
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	PAGE 24 RUN NUMBER	2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	4444	000 444 000	444444 444444 6666666	0000 4444 4444	66666 4444 88888	6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6 6	247	2	22 24 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4
	914 EXCEPTIONS								1.PIN RUN		
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		014.6	•	014.6 004		da.	2000 4400 8		014.6	-	
	A DRAW	1=01 * 014.6	•	1=81 * D14.6 1=82 * D84		da.	1 = 02			-	
	D DRAW	* *	-01 * D1003 * D0403 * D14	* * D14	****	2 * * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * *	* * *		* *	-01 * D11
	3.V23(23) 05/24/74 ORDER BAY - G DRAW E PIN ORDER	* *	-01 * D1003 * D0403 * D14	* * D14	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	2 * * * * * * * * * * * * * * * * * * *	* * * * * * * * * * * * * * * * * * *	* * *	1 X X X X X X X X X X X X X X X X X X X	* *	-01 * D11
	.V23(23) 05/24/74 ORDER BAY . G DRAW PIN ORDER	0302 1 1 02 4 1 1 02 4 1 1 1 0 2 4 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	02E2 1=01 + D10 04K2 1=02 + D04 03S1 1=03 + D14,	03V1 1=01 * D14. 04H2 1=02 * D04.	04F2 02M1 1=02 + D08 02U1 1=03 + D09 03H1 02A1 1=05 + D14	H CO2M2 H E04P1 1 001 * D04 H E03M2 * D04	H E02L2 1=01 * D04 H E03J1 1=02 * D14 H E04N1 1=03 * D04	H E0342 1-01 * D04 H E0342 1-02 * D14 H E04M1 1-03 * D04	H E03K1	1-01 + D04-0381 1-024-0184-0184-0184-0184-0184-0184-0184-018	02R2 1=01 + D11 03R1 1=02 + D14,
	HND288,V23(23) 05/24/74 /P PIN ORDER BAY - G DRAW NAME PIN ORDER	E0102 1-01 # 1-02 # 1-02 #	B02E2 D04K2 1-02 + D10 D03S1 1-03 + D14.	D03V1 1-01 + D14.	D04F2 D02M1 1=01 * D08 D02U1 1=03 * D09 E03H1 C02A1 1=04 * D14	(1 H E04P1 1=01 * D04 (1 H E04P1 1=02 * D04 (1 H E03R2 1=03 * D14	(1 H C02L2 (1 H E03J1 1:02 * D14 (1 H E04N1 1:03 * D14	(1 H E03J2 1=01 # D04 (1 H E03J2 1=02 # D14, (1 H E04M1 1=03 # D04,	(1 H EØ3K1	1 H D04E2 1=01 + D04 1 H D03B1 1=02 + D14	AO2R2 1=01 * D11 E03R1 1=02 * D14
	HND288,V23(23) 05/24/74 /P PIN ORDER BAY - G DRAW NAME PIN ORDER	E0102 1-01 # 1-02 # 1-02 #	G H B02E2 1-01 + D10 G H D03S1 1-03 + D04.	D03V1 1-01 + D14.	1) H DØ4F2 1) H DØ2M1 1) H DØ2U1 1) H EØ3H1 CØ2A1 1=04 * D14. 1) H CØ2A1 1=05 *	01 (1 H CO2M2 1-01 * D04 01 (1 H E03M2 1-03 * D04 01 (1 H E03M2 1-03 * D04 01 (1 H E03M2 1-03 * D14.	02 (1 H C02L2 1=01 * D04 02 (1 H E03J1 1=02 * D14 02 (1 H E04N1 1=03 * D04	04 (1 H C02K1 1-01 * D04 04 (1 H E03J2 1-02 * D14 04 (1 H E04M1 1-03 * D04 04 (1 H E04M1 1-03 *	08 (1 H E03K1	(1) H F0381 1=02 + D54 (1) H F0381 1=02 + D14,	AO2R2 1=01 * D11 E03R1 1=02 * D14
	HND288,V23(23) 05/24/74 /P PIN ORDER BAY - G DRAW NAME PIN ORDER	E0102 1-01 # 1-02 # 1-02 #	REG H B02E2 1=01 * D10 REG H D04K2 1=02 * D04 PEG H D03S1 1=03 * D14.	D03V1 1-01 + D14.	E (1) H D04F2 1=01 * D08 E (1) H D02U1 1=02 * D08 E (1) H D02U1 1=03 * D09 E (1) H E03H1 C02A1 1=04 * D14. E (1) H C02A1 1=05 *	NTR 01 (1 H E04P1 1=01 * D04 NTR 01 (1 H E04P1 1=02 * D04 NTR 01 (1 H E03R2 1=03 * D14.	NTR 02 (1 H C02L2 1=01 # D04 NTR 07 (1 H E03J1 1=03 # D04 NTR 02 (1 H E04N1 1=03 # D04	NTR 04 (1 H C02K1 1=01 + D04 NTR 04 (1 H E03J2 1=02 + D14 NTR 04 (1 H E04M1 1=03 + D04 NTR 04 (1	NTR 08 (1 H E03K1	1) H DOAR 1=01 + DOA 11	AO2R2 1=01 * D11 E03R1 1=02 * D14
	HND288,V23(23) 05/24/74 /P PIN ORDER BAY - G DRAW NAME PIN ORDER	N (0) H E03D2 1-01 # N (0) H E04L1 1-02 #	SH REG H B02E2 1-01 + D10 SH REG H D04K2 1-02 + D04 SH REG H D03S1 1-03 + D14.	D03V1 1-01 + D14.	TIVE (1) H DØ4F2 1=01 * DØ8 TIVE (1) H DØ2M1 1=02 * DØ8 TIVE (1) H DØ2U1 CØ2A1 1=04 * D14. TIVE (1) H CØ2A1 CØ2A1 1=05 * TIVE (1) H CØ2A1 1=05 *	T CNTR 01 (1 H C02M2 1-01 * D04 T CNTR 01 (1 H E03R2 1-03 * D04 T CNTR 01 (1 H E03R2 1-03 * D14.	T CNTR 02 (1 H C02L2 1=01 # D04 T CNTR 02 (1 H E04N1 1=03 # D04 T CNTR 02 (1 H E04N1 1=03 # D04	T CNTR 04 (1 H C02K1 1-01 + D04 T CNTR 04 (1 H E03J2 1-02 + D14 T CNTR 04 (1 H E04M1 1-03 + D04 T CNTR 04 (1	T CNTR 08 (1 H E03K1	KE END (1) H DO4EZ 1=01 + D04 KE END (1) H D03E1 1 = 014.	TE L E03R1 1-01 * D11.
	HND288,V23(23) 05/24/74 A/P PIN ORDER BAY - G DRAW NAME PIN ORDER	E EN (0) H E03D2 1=01 # E EN (0) H E04L1 1=02 # E EN (0)	TX SH REG H BO2E2 1=01 * D10 TX SH REG H D03S1 1=02 * D04 TX SH REG H D03S1 1=03 * D14.	EN L D04H2 1-01 * D04 EN L D04H2 1-02 * D04	ACTIVE (1) H D04F2 1=01 + D08 ACTIVE (1) H D02U1 1=02 + D09 ACTIVE (1) H D02U1 1=03 + D09 ACTIVE (1) H E03H1 C02A1 1=04 + D14. ACTIVE (1) H C02A1 1=05 + D14.	BIT CNTR 01 (1 H CO2M2 1=01 * D04 BIT CNTR 01 (1 H E04P1 1=02 * D04 BIT CNTR 01 (1 H E03R2 1=03 * D14.	BIT CNTR 02 (1 H C02L2 1=01 # D04 BIT CNTR 02 (1 H E03J1 1=02 # D14 BIT CNTR 02 (1 H E04N1 1=03 # D04	BIT CNTR 04 (1 H C02K1 1=01 + D04 BIT CNTR 04 (1 H E03J2 1=02 + D14 BIT CNTR 04 (1 H E04M1 1=03 + D04 BIT CNTR 04 (1	BIT CNTR 08 (1 H E03K1	FAKE END (1) H DOSEZ 1=01 + DOS FAKE END (1) H E03B1 1=02 + D14, FAKE END (1)	LATE L E03R1 1=01 + D11. LATE L E03R1 1=02 + D14.
	HND288,V23(23) 05/24/74 /P PIN ORDER BAY - G DRAW NAME PIN ORDER	EN (0) H E03D2 1-01 # EN (0) H E04L1 1-02 #	X SH REG H BO2E2 1=01 * D10 X SH REG H D03S1 1=03 * D14.	N L D04H2 1-01 + D14.	CTIVE (1) H D04F2 1=01 * D08 CTIVE (1) H D02U1 1=02 * D08 CTIVE (1) H E03H1 C02A1 1=04 * D14. CTIVE (1) H C02A1 1=04 * D14. CTIVE (1) H C02A1 1=05 *	IT CNTR 01 (1 H E04P1 1=01 * D04 IT CNTR 01 (1 H E04P1 1=02 * D04 IT CNTR 01 (1 H E03R2 1=03 * D14.	IT CNTR 02 (1 H C02L2 1=01 # D04 IT CNTR 02 (1 H E03J1 1=03 # D04 IT CNTR 02 (1 H E04N1 1=03 # D04 IT CNTR 02 (1 H E04N1 1=03 # D04	IT CNTR 04 (1 H C02K1 1=01 * D04 IT CNTR 04 (1 H E03J2 1=02 * D14. IT CNTR 04 (1 H E04M1 1=03 * D04. IT CNTR 04 (1	TX BIT CNTR 08 (1 H E03K1	AKE END (1) H D04EG 1=01 + D04 AKE END (1) H E03B1 1=02 + D14, AKE END (1)	ATE L E03R1 1=01 + D11.

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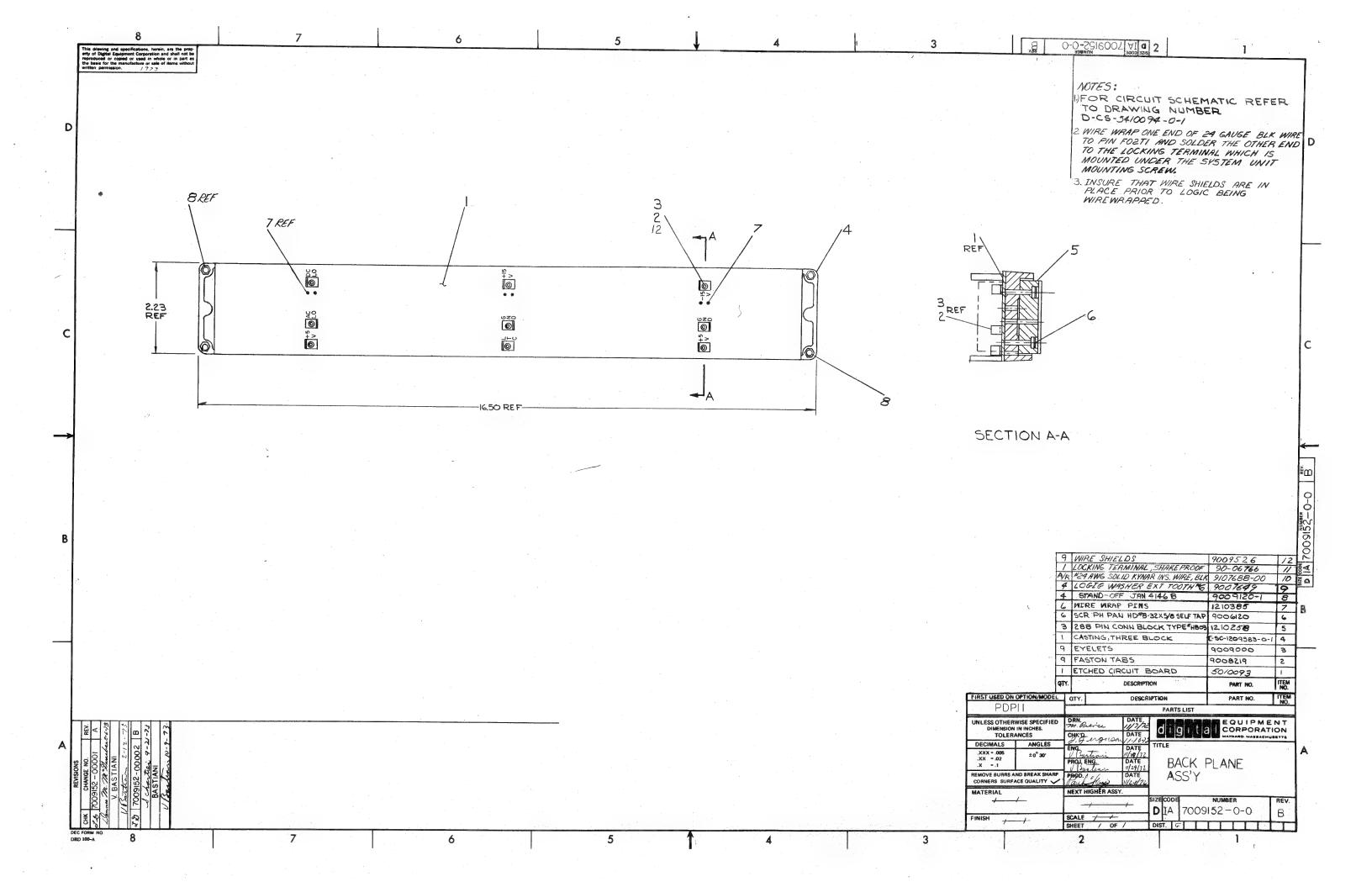
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HND288,V23(23) Ø5/ P PIN ORDER NAME PIN	1-01 F03S1 1-02	F02K2 1 = 01 * 1 = 02 * 1	DO4N2 1=01 * T03R1 1=02 *	A0282 1-01 * T03U2 1-02 *	C02U2 1=01 * 1=02 * 1 = 02 * 1	F04V2 1=01 #	E0272 1:01 * 1:02 * 1:02 *	E02F2 1-01 * 104S2 1-02 *	E02H2 1=01 * 1=02 * 1	I E04R2 1-01 *	E04P2 1-02 *	E0452 1=01 * H

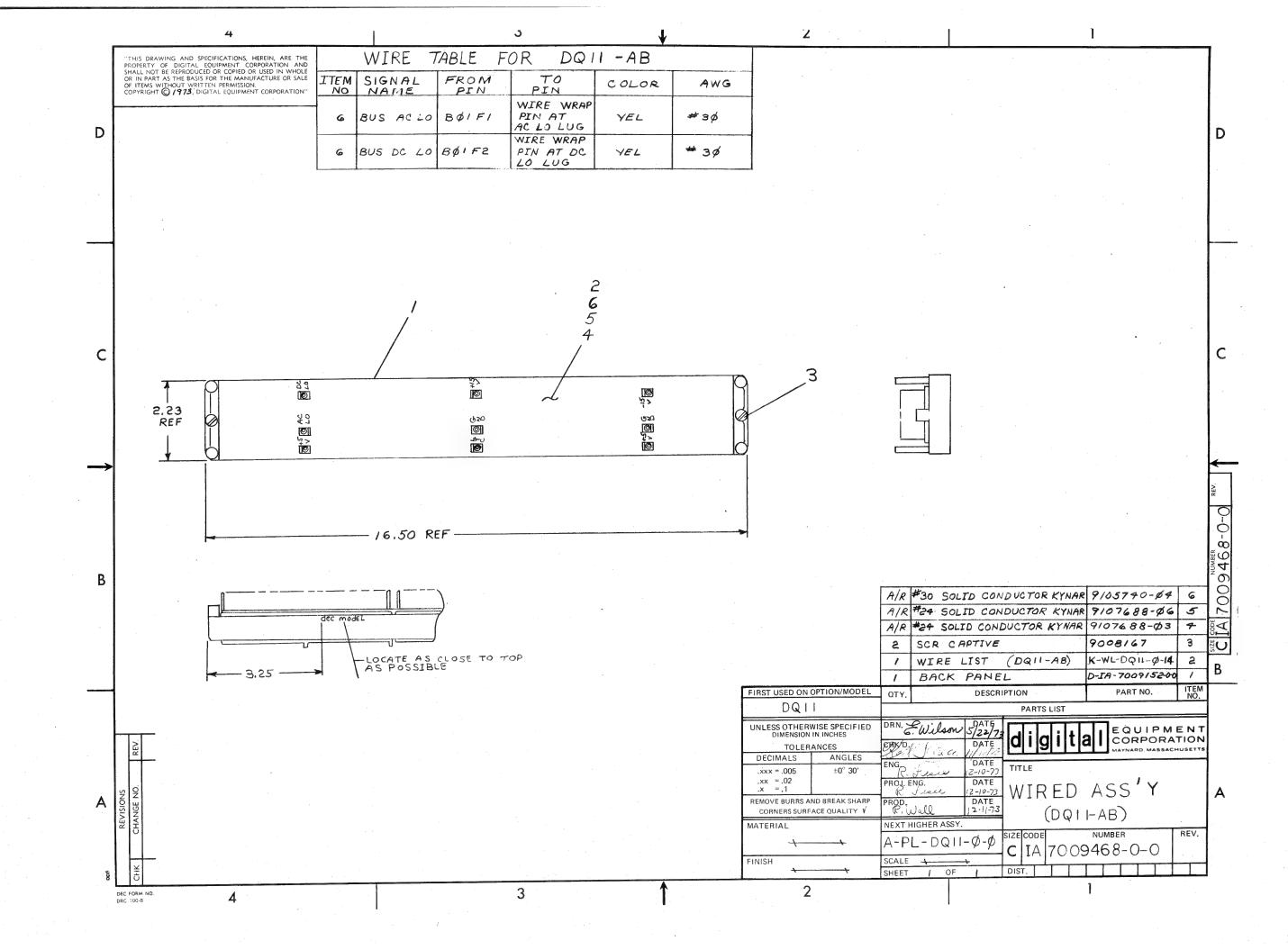
PAGE 27 RUN NUMBER	274	275 275 375	276 276 276	277 775 775	2278 278	279	9 9 9 9 9 9 7 7 7 7	281 281 281	282 282 282 283	283	222 228 244 444	0 10 10 00 00 00 00 10 10 10 10
914 EXCEPTIONS										1-PIN RUN		
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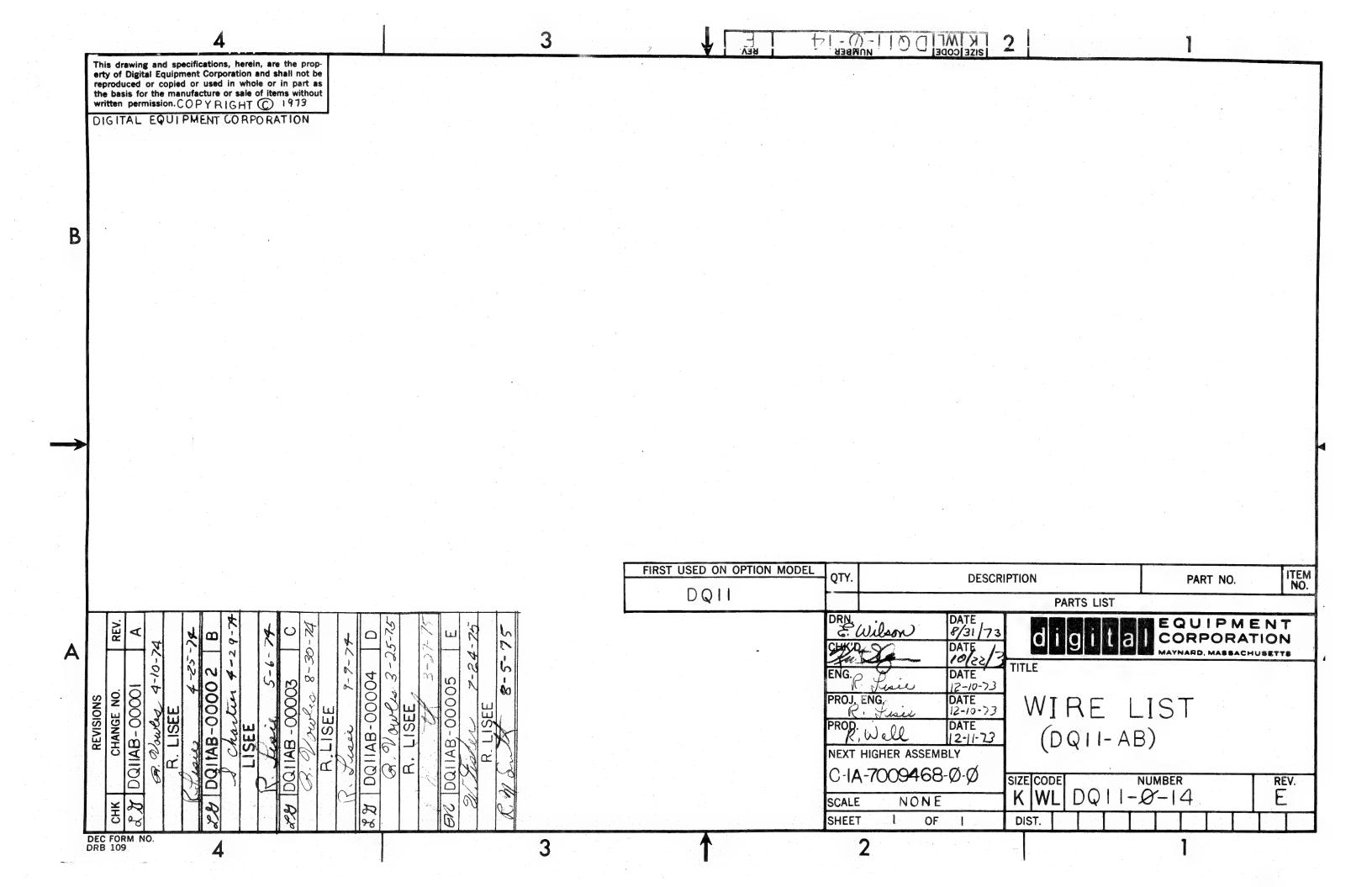
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11120 Exceptions										.*		
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PAGE 14 RUN NUMBER	50 50 50 50 50 50 50 50 50 50 50 50 50 50 50 50 50 50		137 137 137	90 00 00 M M M M M M	6 6 6 6 6 6 6 6 6	444	4 4 4	444	M M M 4 4 4 1 1 1 1 1 1	444	4 4 4 8 8 8
11:26 Exceptions											
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	CL:	23(23) ORDEI PIN	05/24/74 2 BAY - Q 0RDER	DRAW RV P	×	N .	REMARKS	23-AUG-75 Length	11#26 Exceptions	PAGE 15 RUN NUMBER
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D 8 * 55	TX TRANS (0) TX TRANS (0) TX TRANS (0)	m m	D03P1 F01R1	1-01		000		. •••		8/0-8		170 170 170	
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086	DES RX BCC SH DES RX BCC SH DES RX BCC SH	دد	E02U2	1 = 01 + +		9 9 0				2=6/8		172	
08	DLE SAVE (0) DLE SAVE (0)	m m	E03V2 F01U1	1-01 *		0.00		-		80 44 80		11 11 11 11 11 11 11 11 11 11 11 11 11	
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PAGE 18 RUN NUMBER	a a a 00 00 00 a a a	00 00 00 10 10 10 10 10 10	# E & B	4 4 4	2 2 2 2 20 20 20 20 20 20 20 20	44 44 44 60 60 60 60 60 60	11.00	00 00 00 00 00 00	O O O	0000		0 0 0 0 0 0 0 0
EXCEPTIONS												
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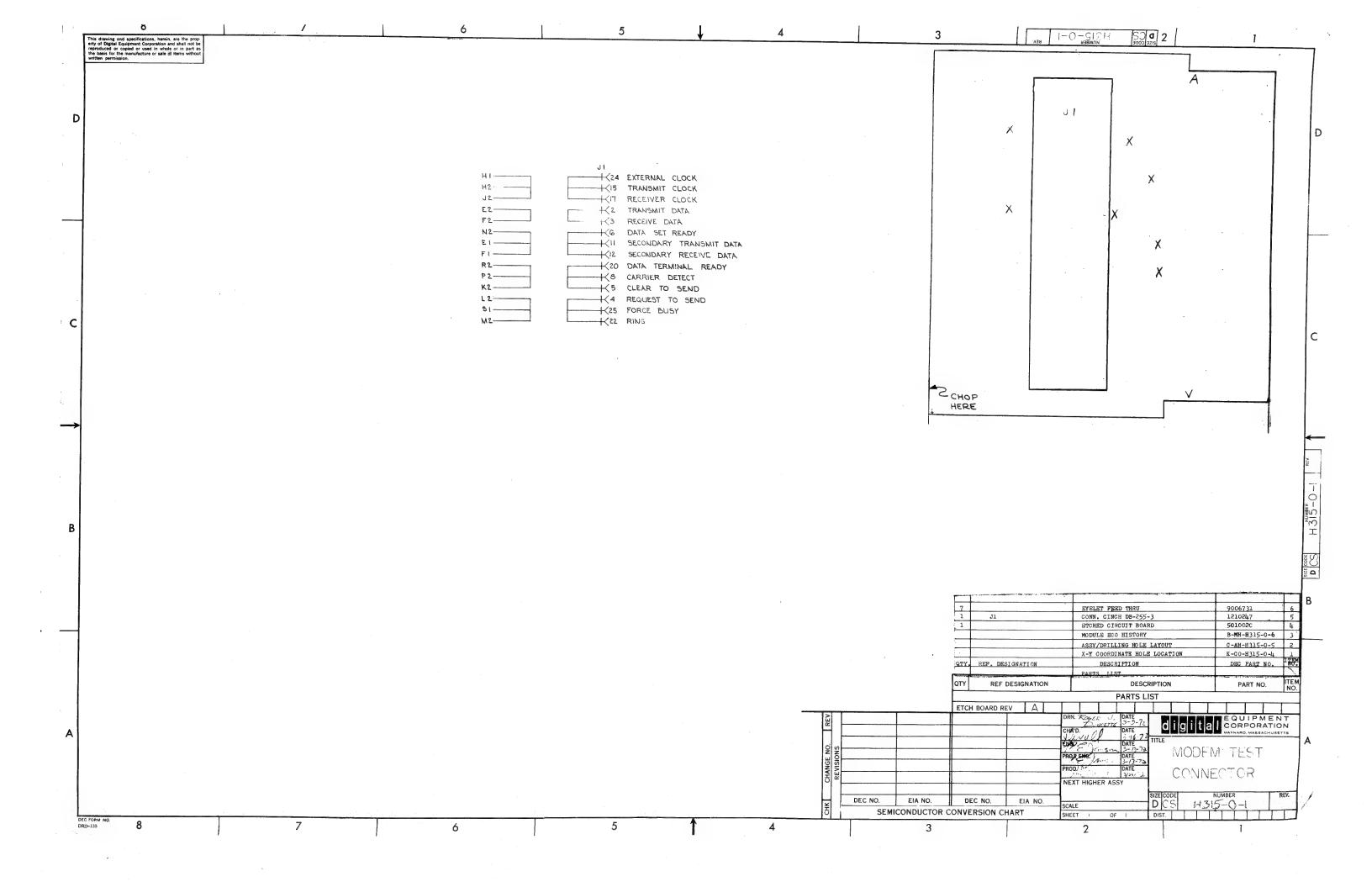
PAGE 19 RUN NUMBER	193	466	44 44 44 90 90 90 80 80 80 80	1 1 1 1 1 9 6 6 9 6 6 6 6 6 6 6 6 6 6 6	191 197	9 9 9	999	0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	989	202 202 202	203
11126 EXCEPTIONS					,						
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A/P	x x	TE TE	222	교교	ac ac	.a .a	के ह	TET	DC DC	د د	да ,
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PAGE 20 RUN NUMBER	000 000 444	ଜ ପ୍ରାମ ବ ଅବସ କ ଅହାର	0 0 0 0 0 0 0 0 0 0	222 200 700 7	00 00 00 00 00 00 00 00 00	0000 0000 0000 0000	000000000000000000000000000000000000000	200000000000000000000000000000000000000
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3(23) ORDER					E83F1			
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EXCEPTIONS RUN NUMBER	211	1170	7	211	211	211	211	211	211	211	74 () () () () () () () () () (7 · · · · · · · · · · · · · · · · · · ·	211	222	777
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3(23) 05/24/74 ORDER BAY - PIN ORDER															
HNDZHB.V2 A/P PIN NAME	A0482	A04C2	B 10 4 10 3	A.04P1	AUARI	A 0451	A04T1	A04V2	RU4B2	P04C2	83401	BO4E1	B04T1	P34V2	

DOILAB.E

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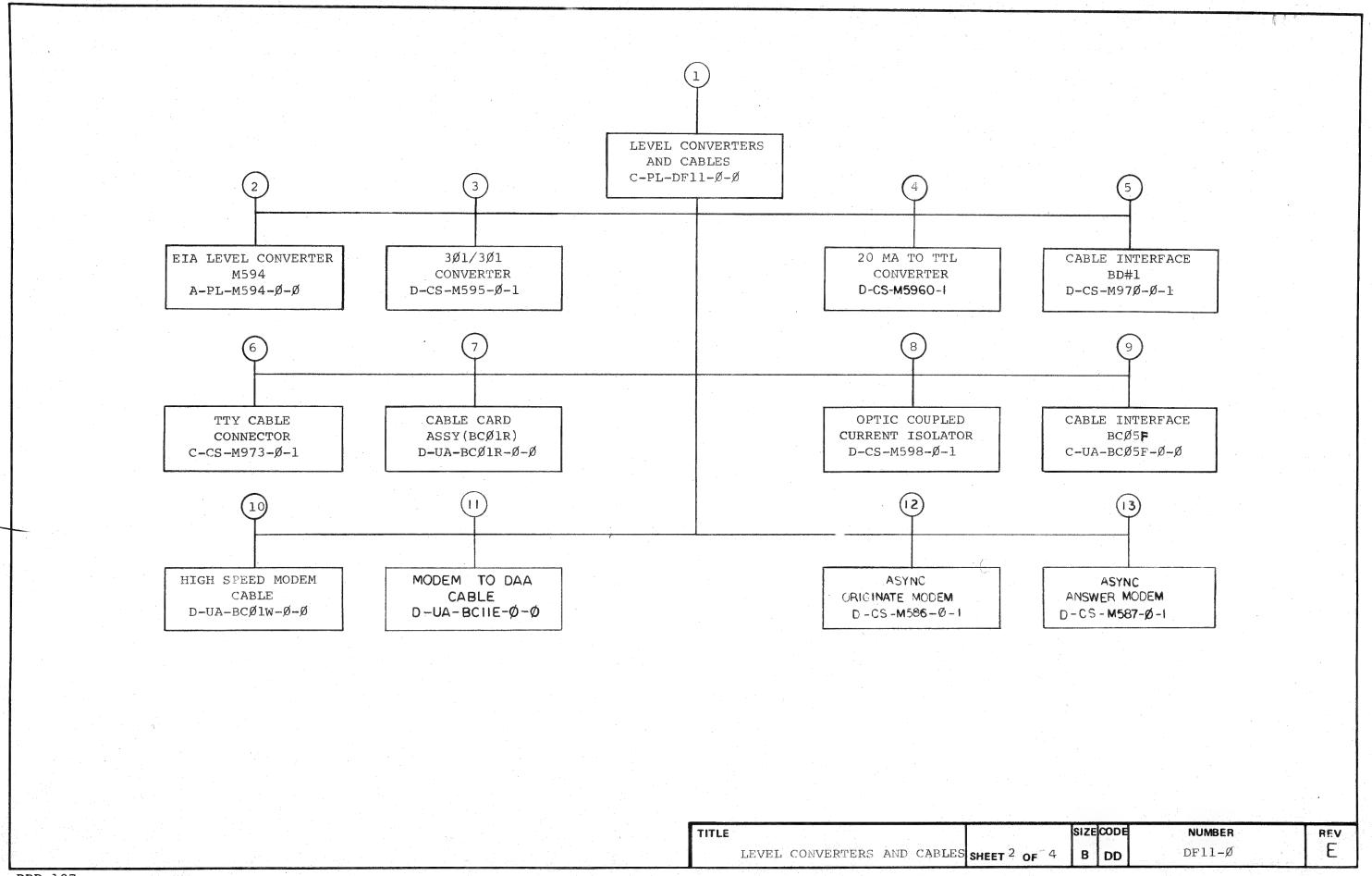


digital	EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
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DRAWING DIRECTORY

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					TIEMS WITHOUT WRIT	TEN PERMISSION.	COPYRIGHT (C) 1972 , DIGITAL EQUIPMENT CORPORATION	JN "	
	SEQUENCE PRINT SET #1	CUSTOMER PR	INT SET INDEX	SEQUENCE			THIS IS PRINT SET		Ţ
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	DRAWING DIRECTORY	B-DD-DF11-Ø		. •	•	1	JNIT VARIATIONS	FRINT	1
	ENGINEERING SPEC	A-SP-DF11-Ø-1							
	EIA/LEVEL CONVERTER	A-PL-M594-Ø-Ø				1/45		1 -	
	EIA/LEVEL CONVERTER	C-CS-M594-Ø-1				VAR	TITLE	DF1	-
	3Ø1/3Ø3 CONVERTER	D-CS-M595-Ø-1							
	Sp1, Sp3 Convenience	D-03-14393-b-1				DF11-A	TTL TO EIA RS-232-C/CCITT V24	X	
	20 MA TO TTL CONVERTER	D-CS-M596\$-\$\psi_0-1				DF11-BA	TTL TO AUDIO-ORIGINATE FREQ	1	_
	OPTIC COUPLED CURRENT	D-CS-M598-Ø-1			· ·	mm11 mm	MODEM	X	
	ISOLATOR	D 65 11336 p 1				DF11-BB	TTL TO AUDIO-ANSWER FREQ MODEM	X	+
	CABLE CARD ASSY (BCØ1R)	D-UA-BCØ1R-Ø-Ø						1	
	Chibble Child hoof (Beplin)	D on Depin-p-p						+	+
	CABLE INTERFACE (BCØ5F)	C-UA-BCØ5F-Ø-Ø			1			1-1-	+
	CIDED INTERCE (DODGE)	C-CA-DCDOF -p-p						+++	+
	HIGH SPEED MODEM CABLE	D-UA-BCØlW-Ø-Ø						+++	+
	TTY CABLE CONNECTOR	C-CS-M973-Ø-1			•	יי דורות	MMT MO ACMITYE 20MA LOCAL	+-+-	+
	CABLE INTERFACE BD #1	$D-CS-M978-\emptyset-1$				DF11-F	TTL TO ACTIVE 20MA LOCAL TELEPRINTER	1	+
	LEVEL CONVERTERS & CABLES	C-PL-DFII-Ø-Ø				DE 11 0		X	+
	MODEM TO DAA CABLE					DF11-G	TTL TO BELL SYSTEM 301 & 303	X	+
		D-UA-BCITE-Ø-Ø						+++	+
	ASYNC ORIGINATE MODEM	D-CS-M586-Ø-I	·	•		DF11-K	TTL TO OPTICAL COUPLED 20 MA	x	+
	ASYNC ANSWER MODEM	D-CS-M587-Ø-1				DETI-K	THE TO OFFICAL COOFEED 20 FEA		+
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}	+	1	C-PL-DF11-Ø-Ø	F	1	LEVEL CONVERTERS AND CABLES				\vdash		D-AH-M973-Ø-5		1	ASSY/DRILLING HOLE LAYOUT	
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	-	+								\dagger		B-SP-BCØ1R-Ø-3		1	TEST PROCEDURE	+
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/ 	+	2	A-PL-M594-Ø-Ø	#		EIA LEVEL CONVERTER M594			11					1	**************************************	
++	+	+-	C-CS-M594-Ø-1	#	1 7	EIA LEVEL CONVERTER M594										
	-	+-	K-CO-M594-Ø-4		1	X-Y COORDINATE HOLE LOCATION		X			8	D-CS-M598-Ø-1	##	1	OPTIC COUPLED CURRENT ISOLATOR	R
 	+	╅	D-AH-M594-Ø-5		1	ASSY/DRILLING HOLE LAYOUT				\Box		K-CO-M598-Ø-4		1	X-Y COORDINATE HOLE LOCATION	``
+++		+	B-MH-M594-Ø-6		1	MODULE ECO HISTORY	1			\Box	1	D-AH-M598-Ø-5		1 7	ASSY/DRILLING HOLE LAYOUT	1
+-+-	+	+	A-ML-M594-T		1	M594 TESTER	1			Н		B-MH-M598-Ø-6	<u> </u>	1	MODULE ECO HISTORY	1
+++	+	+	ATIBITISTE		1	11001111	-					5009823		1	ETCHED CIRCUIT BOARD	1
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+++	+	-										A-SP-M598-Ø-8		9	M598 SPECIFICATIONS	
 	+	3	D-CS-M595-Ø-1	#	2	3Ø1/3Ø3 CONVERTER				T		B-CS-M598-Ø-2		1	M598 TESTER	1
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+++	-	+	B-MH-M596Ø-Ø-6		1	MODULE ECO HISTORY	 	\mathbf{x}	+++	1	10	D-UA-BCØ1W-Ø-Ø	1	1	HIGH SPEED MODEM CABLE	
+-++	+	+	B-M1-M350 9-9 6		+ + +	HODOHI DOO HIDIOHI			<u> </u>	+						
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	-	5	D-CS-M97Ø-Ø-1	#	1	CABLE INTERFACE BD #1	1			1						
	+	+-	K-CO-M97Ø-Ø-4		1	X-Y COORDINATE HOLE LOCATION		\mathbf{X}		1	11	D-UA-BCIIE-O-Ø	#	1	MODEM TO DAA CABLE	
+ + +	\dashv	+	D-AH-M97Ø-Ø-5		1	ASSY/DRILLING HOLE LAYOUT				1						
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DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE June 30, 1972

TITLE DF11 Serial Line Signal Conditioning Options

			REVISIONS				
REV		DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE
А	ECO	CHANGE	DFII - 000 0 5	MCNAMARA	8/72	S.M. Haween	4/5/12
В	ECO	CHANGE	DFII - 00009	DIETER	4/74	Ja horas	6/5/14

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DEC FORM NO DRA 107 SHEET _ 1_ OF _10

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE DF11 Serial Line Signal Conditioning Options

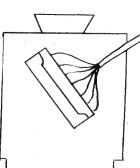
I. General

Modern digital computers handle almost all data signals as Transistor-Transistor Logic (TTL) levels. These levels must then be converted to other voltage levels or current values to prepare them for application to communications media so that they can be transmitted to a distant processor or terminal. In addition to the need for level conversion, there is a requirement to have cables of various lengths equipped with a variety of specialized fittings for each communications medium serviced.

A DF11 unit will normally consist of two single height (8-1/2" by 3") modules. One module performs the electrical signal conditioning function of converting from TTL signal levels to the external signal levels required (e.g. EIA RS-232-C, 20 milliampere current loop, Bell System CBS or CDT Data Access Arrangements, etc.). The second module performs the physical interface function; i.e. furnishes a cable to connect the level-converted signals produced by the first module to the desired device or channel. In the case of the DF11-A RS-232-C/CCITT Recommendation V24 Interface, the second module provides a 25-conductor cable and plug to connect the level-converted signals on the back panel wires to the dataset.

Dataset
Interface
for units
requiring
EIAR/RS232C
or CCITT
voltage
levels





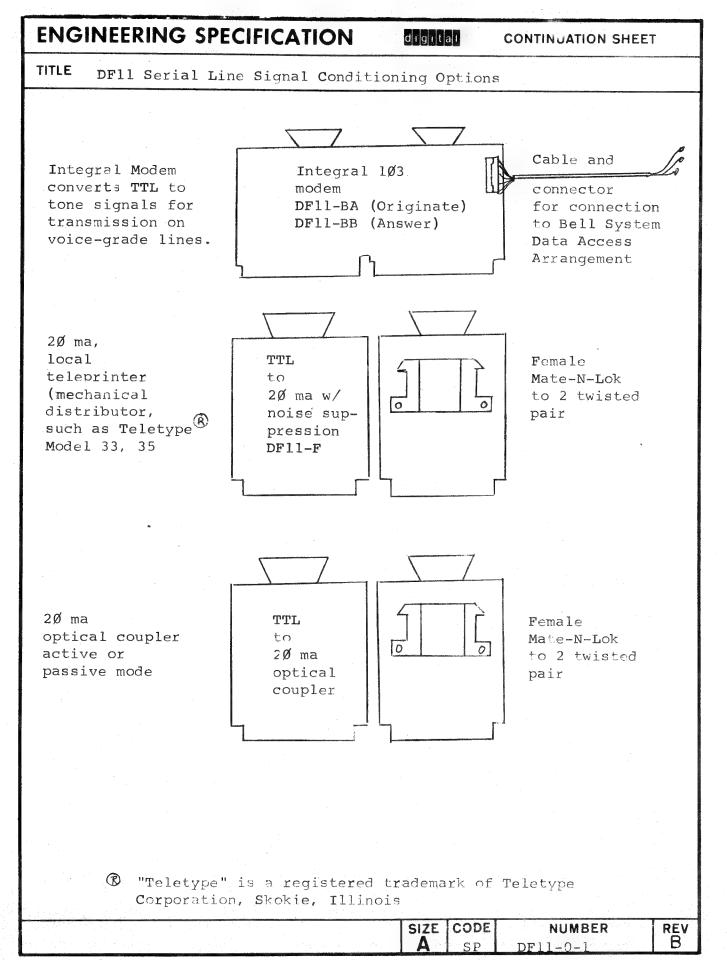
Clinch
DB25P
25-pin

Dataset Connector

SIZE CODE SP

NUMBER DF11-0-1 REV B

DEC FORM NO DEC 16-(381)-1022-N370
DRA 108



ENGINEERING SPECIFICATION digital CONTINUATION SHEET DF11 Serial Line Signal Conditioning Options II. Variations DF11-A TTL to EIA RS-232-C/CCTTT V24 This option consists of an M594 module and a BCO1R cable assembly. (A BCO5C cable and M971 module may be supplied as equivalent to a BCOlR). DF11-BA TTL to audio frequency shift keyed tone signals. This option consists of an M586 module (integral 103-type modem in the Originate-Only mode). Twenty-five foot cable and connector provided for connection with Bell System Data Access arrangement CDT or private wireline channels. DF11-BB TTL to audio frequency shift keyed tone signals. This option consists of an M587 module (integral 103-type modem in the Answer-Only mode). Twenty-five foot cable and connector provided for connection with Bell System Data Access arrangements CBS or CDT. DF11-F TTL to 20 milliampere active local Teletype loop. This option consists of an M5960 module and an M973. The M973 has an Amp Mate-N-Lok connector mounted on it for connection with customer supplied 22 AWG, 2 twisted pair cable to a local or remote Model 33 or 35 Teletype. Reader Run control leads are not provided. DF11-G TTL to Bell System 301/303 Dataset Interface. Signal levels, cable connector, and signal pinning compatible with the Bell 301/303 Datasets. This option consists of an M595 module and a twentyfive foot coaxial cable. DF11-K TTL to active or passive 4-wire current mode (20 ma) This option consists of an M598 optical coupler module and an M973 Mate-N-Lok module. SIZE CODE NUMBER REV A SP DF11-0-1 В

SHEET __ 3 OF __ 10

ENGINEERING SPECIFICATION digital **CONTINUATION SHEET** TITLE DF11 Serial Line Signal Conditioning Options III. Specifications All DF11 modules will operate from 10°C to Temperature: 50°C with relative humidity from 20% to 95% (without condensation). Speed & Distance: 9600 baud at 50 feet with DEC supplied DF11-A cable. 300 baud with 0 dbm to -50 dbm received DF11-BA signal level when used with CDT data access arrangement, 300 baud with 0 dbm to -50 dbm received signal level when used on private wires. Transmit level is 0 to -12 dbm pot adjustable. 300 baud with 0 dbm to -50 dbm received DF11-BB signal level when used either with CBS, CDT, REV B SIZE CODE NUMBER

ENGINE	ERING	SPECIFIC	CATION	digital	CONTINUATION SHEET	
TITLE	DF11 S	erial Li ne	Signal Cond	ditioning Op	tions	
				e wires. Tr ot adjustabl	ansmit level is 0 t	0
	DF11-F		telephone jumpers an 2400 baud wire (DEC	cable when re in place at 3000 fee PN 9105856-4	22 AWG twisted pai inserted (TTY noise filter). t with 22 AWG "QUAD' 4) and using the mit and receive) at	1
	DF11-G		250K baud	with DEC-su	pplied 25 foot cabl	e.
	DF11-K			at 1500 fee phone cable.	t of 22 AWG twisted	
IV.	NOTE:	warranteed electrical represent	d by D igital lly quiet en limiting va	l. They are	ed above are those applicable in and do not necessarers on M5960 (DF11-F) baud.	ily ')
	DF11-A DF11-BF DF11-F DF11-G DF11-K	YES 2 Y1 A 1 1 : 3 1 1 Y1 YES 2 Y1				
	* DLll	refers to	DL11 in DD	llB system u	nit or 11/05.	
} :	NOTES:	volts.	They may h	_	re +15 volts and -1 the DCll and DLll le.	i
		interfa	ace circuits	ϵ . While the	ive 20 ma and EIA DF11-A or DF11-F cation of equipment.	oulđ

DF11-0-1

ENGINEERING SPECIFICATION

dıgıtal

CONTINUATION SHEET

DF11 Serial Line Signal Conditioning Options

- 3. It is anticipated that the DF11-BA will be used in terminals such as the LA30S and the DF11-BB will be used in processor options such as the DMll. Technically, they could be used in the reverse order, but this would not be recommended, since the DF11-BA does not have auto-answer capability.
- 4. No applications have been found for the DF11-F and DF11-K with synchronous controls due to clocking problems.
- 5. The DF11-F is "active" only and hence would not typically be used in a terminal.

V. Connectivity:

	DF11-A	DF11-BA	DF11-BB	DF11-F	DF11-G	DF11-K
DF11-A	1	NO	NO	NO	ИО	NO
DF11-BA	NO	NO	YES	ИО	NO	NO
DF11-BB	NO	YES	NO	NO	NO	NO
DF11-F	NO	NO	МО	NO	NO	YES
DF11-G	NO	NO	NO	NO	NO	NO
DF11-K	ИО	NO	МО	2	NO	2

NOTES: 1. Null modem required (H312A)

2. See "Active" vs. "Passive"

VI. "Active" vs. "Passive"

DEC FORM NO DEC 16-(381)-1022-N370

In 20 ma transmission, information is transmitted by means of switching on and off a current flow. To do this, a current source is used: a switch is used: and a current flow detector is required.

In the classical 20 ma device, the Model 33 or 35 Teletype[®], the keyboard encoding mechanism contains the "switch" and the printer driver mechanism includes a "current detector". The typical computer console Teletype®control contains a source plus current detector in its receiver circuitry and a source plus switch in its transmitter circuitry. Since the receiver

SIZE	CODE	NUMBER	REV
Ā	SP	DF11-0-1	В

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

DF11 Serial Line Signal Conditioning Options

contains the source, it is referred to as an "active" receiver. Since the transmitter contains a source, it too is referred to as "active". All DEC console teletype controls, and the DF11-F are "active" 20 ma interfaces.

The DF11-K, however, has its greatest volume of use in the LA30S, where it is necessary to simulate a Teletype® interface; i.e., a switch on the transmit leg and a detector on the receive leg. Since there are no sources, the DF11-K is referred to as a "passive" 20 ma interface. Jumpers are provided to connect sources into the receiver circuitry, the transmit circuitry, or both.

When used with a console teletype control or a DF11-F, the DF11-K should be left in passive transmit/passive receive mode. When two DFll-K's talk to each other, the active transmit/ passive receive mode is recommended. Be sure to arrange the leads so that the receiver of one is connected to the transmitter of the other, etc.

VII. Pinning Assignments

The following pinning assignments apply to the DF11 series, although individual DF11 options may not use them all:

Cable Slot ("A" slot in DC11, 11/05)("B" slot in(LA30,DHI,DMI)

- Al Do Not Use
- B1 Do Not Use
- Transmitted Data +
- Transmitted Data -
- Secondary Transmitted Data EIA
- FlSecondary Received Data EIA
- H1Transmit Signal Element Timing DTE EIA
- Jl Received Data +
- K1 Secondary Clear To Send*
- LlSecondary Request To Send*
- Ml Signal Quality Detector*
- N1Received Data -
- P1 Secondary Receive Line Signal Detector*
- R1
- Sl Force Busy EIA
- Tl Ground
- Ul.
- UK Special Systems

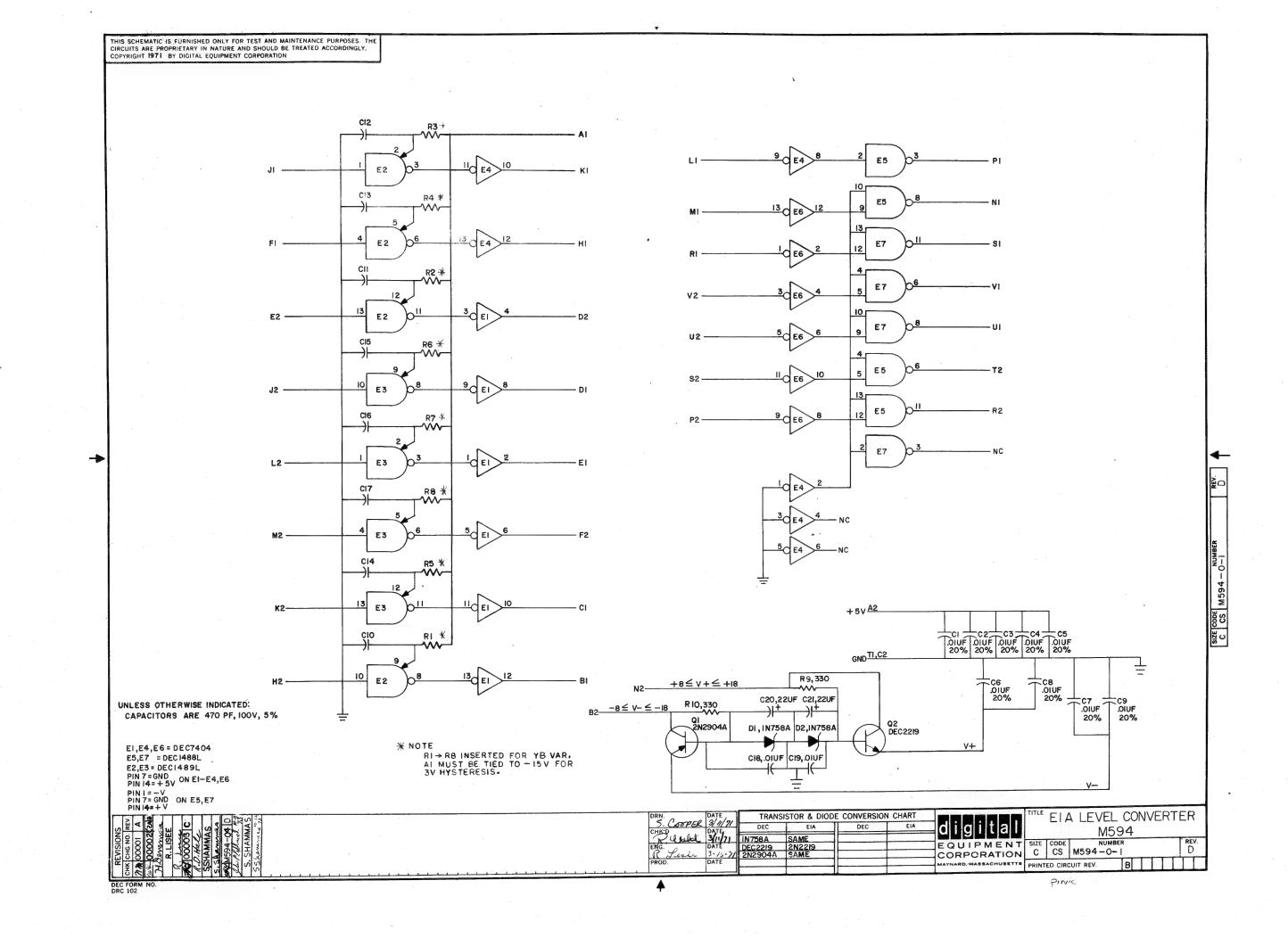
REV B

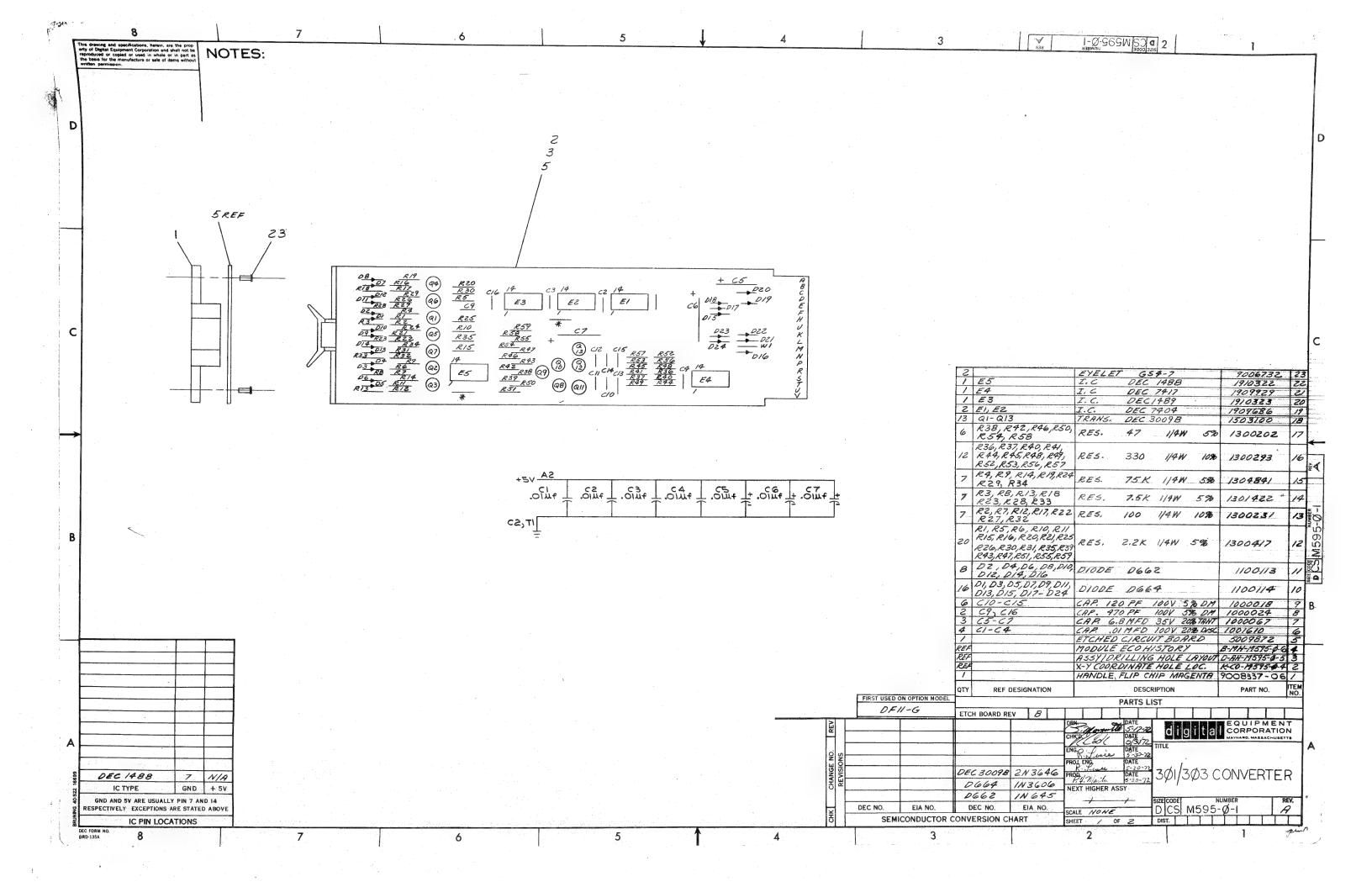
ENGI	NEEKII	NG SPECIFICATION	digital	CONTINUATION SHEET
TITLE	DF11	Serial Line Signal Condi	tioning Opt	ions
	A2	Do Not Use	÷	
	B2	Do Not Use		
	C2	Ground	*	
	D2	DO NOT USE		
	E 2	Transmitted Data EIA		
	F 2	Received Data EIA		
	H2	Transmit Signal Elemen	nt Timing DCI	E EIA
	J2	Receive Signal Element	Timing EIA	
	K2	Clear To Send EIA		
	L2	Request To Send EIA		
	M2	Ring Indicator EIA		
	N2	Data Set Ready EIA		
	P2	Received Line Signal I		
	R2	Data Terminal Ready El		
	S 2	Data Signalling Rate S	Selector*	
	Т2	Ground		•
	U2	Select Standby*		
	V2	UK Special Systems		
	mod	gnals shown with an aster Hule pin assignments. If ald have to be level conv	implemented	d, these signals
	wor	dule pin assignments. If	implemented verted elsewl	d, these signals here.
	wor	Tule pin assignments. If	implemented erted elsewhocll, 11/05)	d, these signals here. ("A" slot in LA30,DHII,DMI
	mod wou Conve	Jule pin assignments. If ald have to be level converter Slot ("B" slot in I	implemented erted elsewhocll, 11/05)	d, these signals here. ("A" slot in LA30,DHII,DMI
	mod wou Conve	Tule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E	implemented erted elsewhocll, 11/05)	d, these signals here. ("A" slot in LA30,DHII,DMI
	mod wow Conve Al Bl	Jule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E Carrier Detect TTL	implemented erted elsewhocll, 11/05)	d, these signals here. ("A" slot in LA30,DHII,DMI
	Conve	dule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E Carrier Detect TTL Ring Indicator TTL Received Data TTL Secondary Received Data	implemented erted elsewhorld, 11/05) CIA Receivers	d, these signals here. ("A" slot in LA30,DHII,DMI
	Conve	dule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E Carrier Detect TTL Ring Indicator TTL Received Data TTL Secondary Received Data Received Signal Element	implemented erted elsewhorld, 11/05) CIA Receivers CA TTL	d, these signals here. ("A" slot in LA30,DHII,DMI
	MOO WOU Conve Al Bl Cl Dl El Fl	dule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E Carrier Detect TTL Ring Indicator TTL Received Data TTL Secondary Received Data Received Signal Element Receive Signal Element	implemented verted elsewhorld, 11/05) CIA Receivers TTL Timing EIA	d, these signals here. ("A" slot in LA30,DHII,DMI
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	MOO WOU Al Bl Cl Dl El Fl Hl Jl	dule pin assignments. If ald have to be level converter Slot ("B" slot in I Response Control for E Carrier Detect TTL Ring Indicator TTL Received Data TTL Secondary Received Data Received Signal Element Receive Signal Element Transmit Signal Element Transmit Signal Element	implemented verted elsewhold, 11/05) CIA Receivers TIL Timing EIA Timing TTL Timing DCI	d, these signals here. ("A" slot in LA30,DHII,DMI
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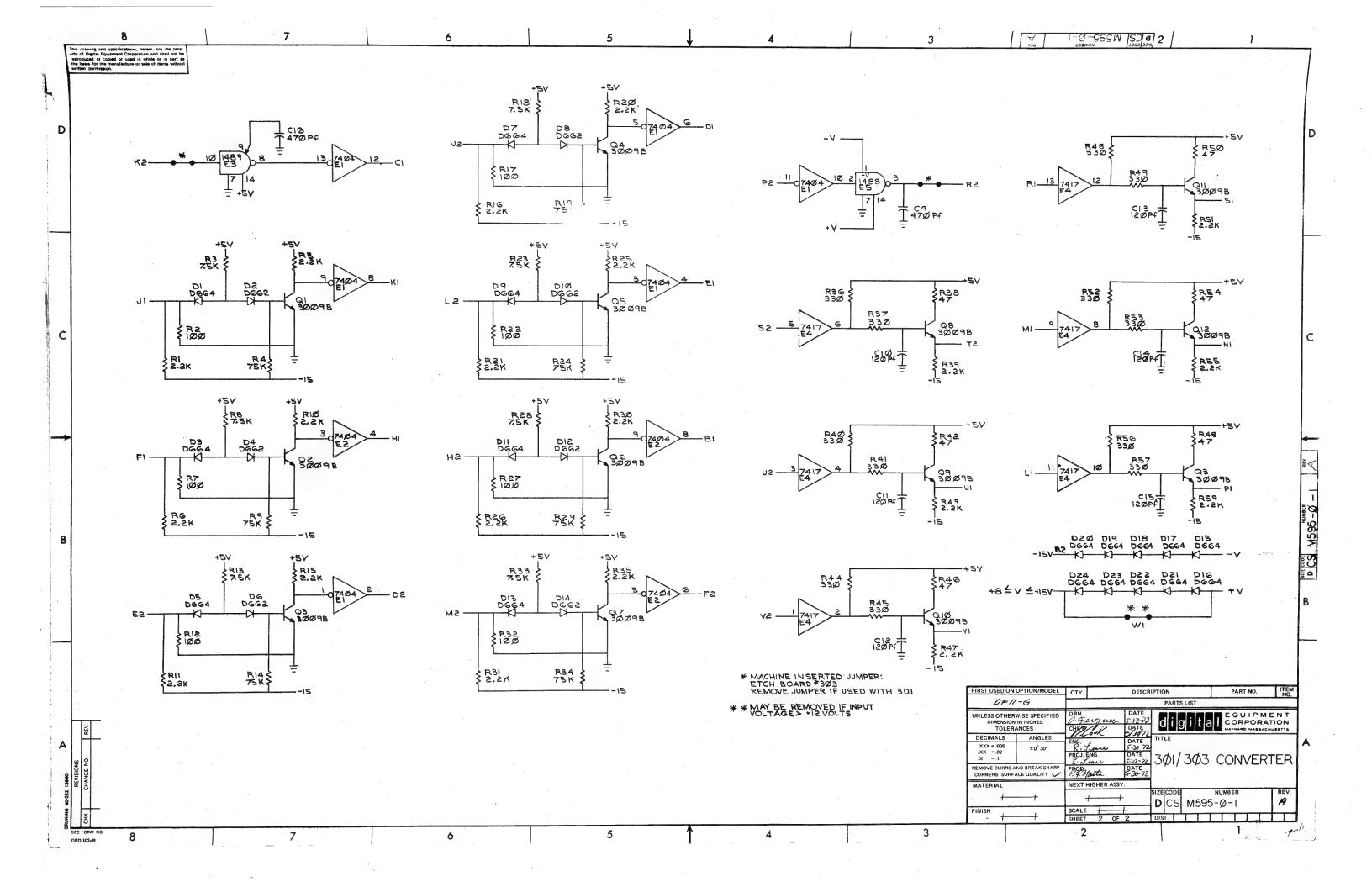
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	C2	Ground						
	D2	Data Se	t Ready	TTL				4
	E2	Data Se	t Ready	EIA				
	F2	Clear T	o Send T	\mathtt{TL}				
	H2	Carrier	Detect	EIA				
	J2		Data EI					
	K2	Ring In	dicator	EIA				
	L2		ry Recei		EIA			
	M2		o Send E	IA				-
	N2	•	MA MAX)		•			
	P2		rminal R	_				
	R2		rminal R					
	S2	_	To Send					j.
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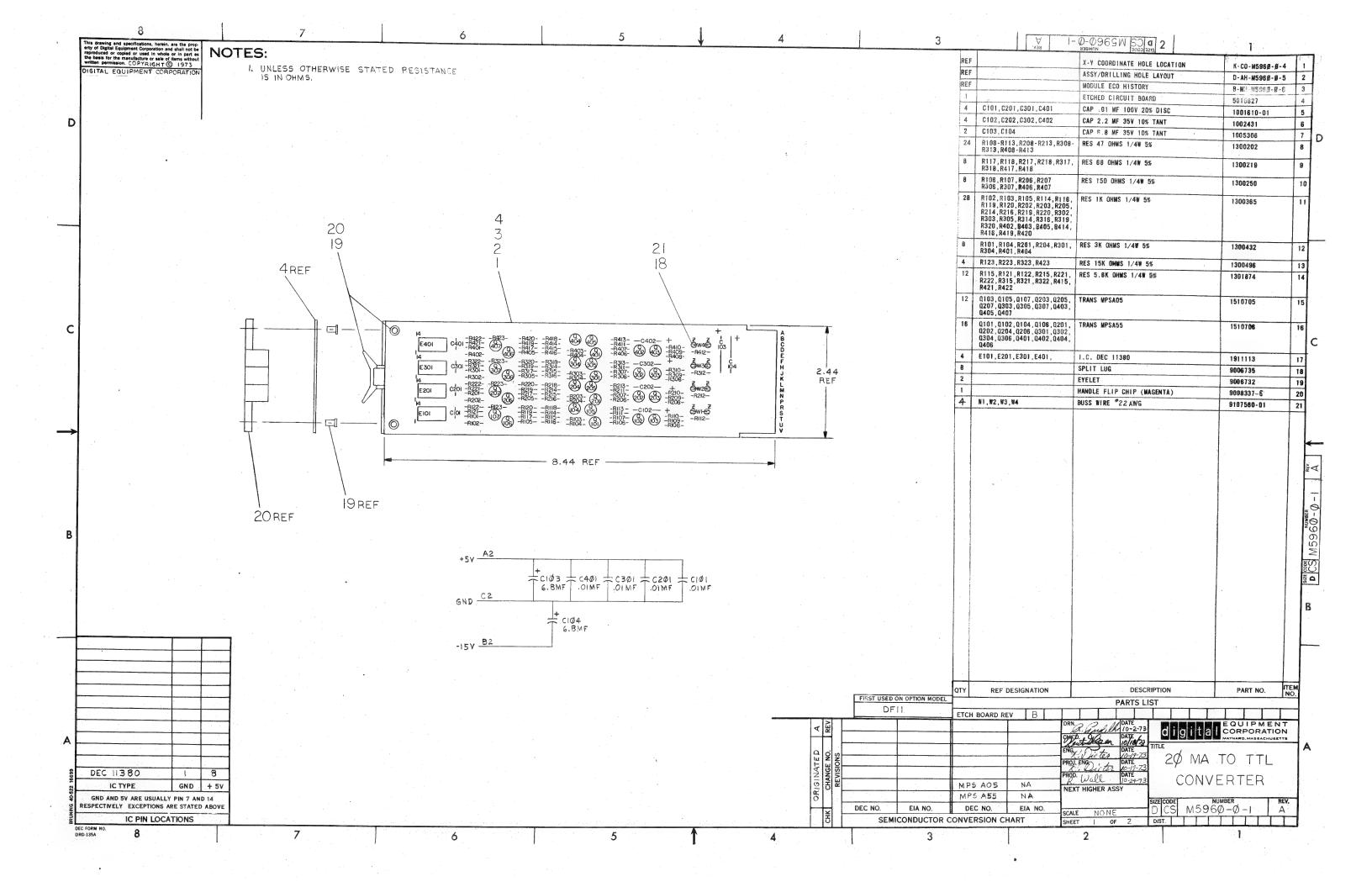
DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS						QUANTITY/VARIATION											
		PARTS LI	ST														
MADE BY M.A. Gilbert		CHECKED R. Wald SECTION															
DAT		DATE 3/12/					Z 28										
DAT	W. HISTE	PROD R. Si DATE 6/14/		ISSUED SEC		94	94-YB										
TEM NO.		DESCRIPTION				M55	MS9										
1	C-CS-M594-0-1	CIRCUIT SCH	EMATIC			observation come of											-
2	K-CO-M594-0-4	X-Y COORDINATE HOLE LOCATION															
3	D-AH-M594-0-5	ASSY/DRILLIN	ASSY/DRILLING HOLE LAYOUT														
4	в-мн-м594-0-6	MODULE ECO F	HISTORY														
5	5009443	ETCHED CIRCU	JIT BOARD			1	1										
6	1000024	CAP. 470PF]				8	8	C10	C11	C12	C13	C14	C15	Cl	5C1:	7	
7	1001610		100v 20% DISC			_	11				1	ì				c9	C18
																	C19
9	1210244-0	GRIPLET															
10	1300510	RES. 33K 1/4	4W 10%			0	8	Rl	R2	R3	R4	R5	R6	R7	R8		
1	1909686	I.C. DEC 740)4			3	3	E1	E4	E6							
2	1910322	I.C. DEC 148	38L			2	2	E5	E7								
3	1910323	I.C. DEC 148	39L			2	2	E2	E3						·		
4	9006732	EYELET #GS4-	-7			2	2										
5	9008337-06	HANDLE FLIP	HANDLE FLIP CHIP - MAGENTA			1	1										
.6	1100125	1N758A ZENEF	1N758A ZENER DIODE			2	2	D1	D2								
7	1300295	RES 330 ¼w 5%				2	. 2				R10						
8	1002433	CAP. 22UF 35v 20% S.TANT				2	2	C20	C21								
9	15-1913	TRANSISTOR 2	TRANSISTOR 2N2904A			1	1	Q1									
0	15-1881	TRANSISTOR DEC 2219				1		Q2									
21	9008351-0	TRANSISTOF	R CAP			2	2										
ITL	E EIA LEVEL CONVERTER	M594	ASSY NO.		A P	DE		1	N 1594	1 U M E					_	ECO	
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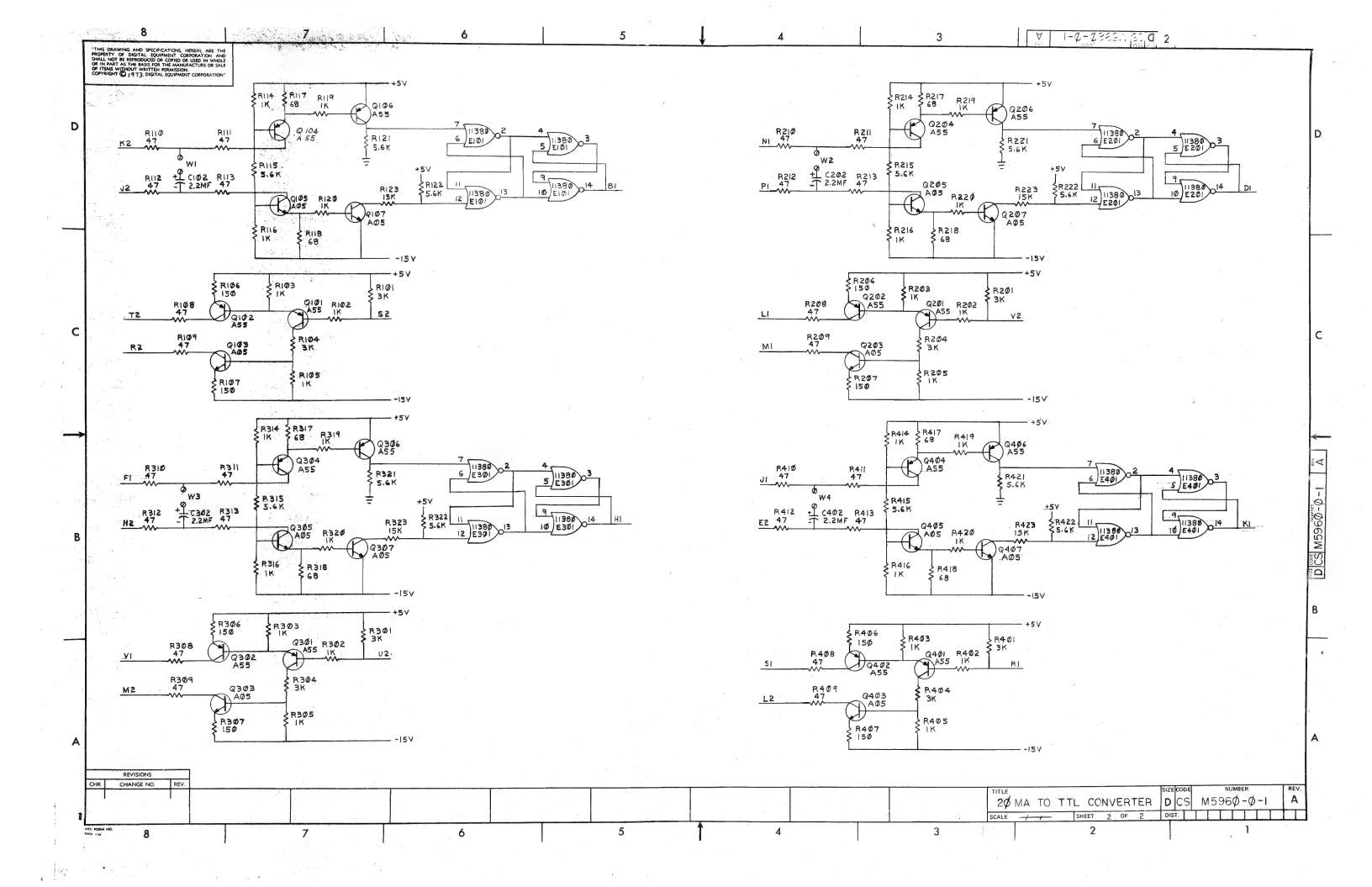
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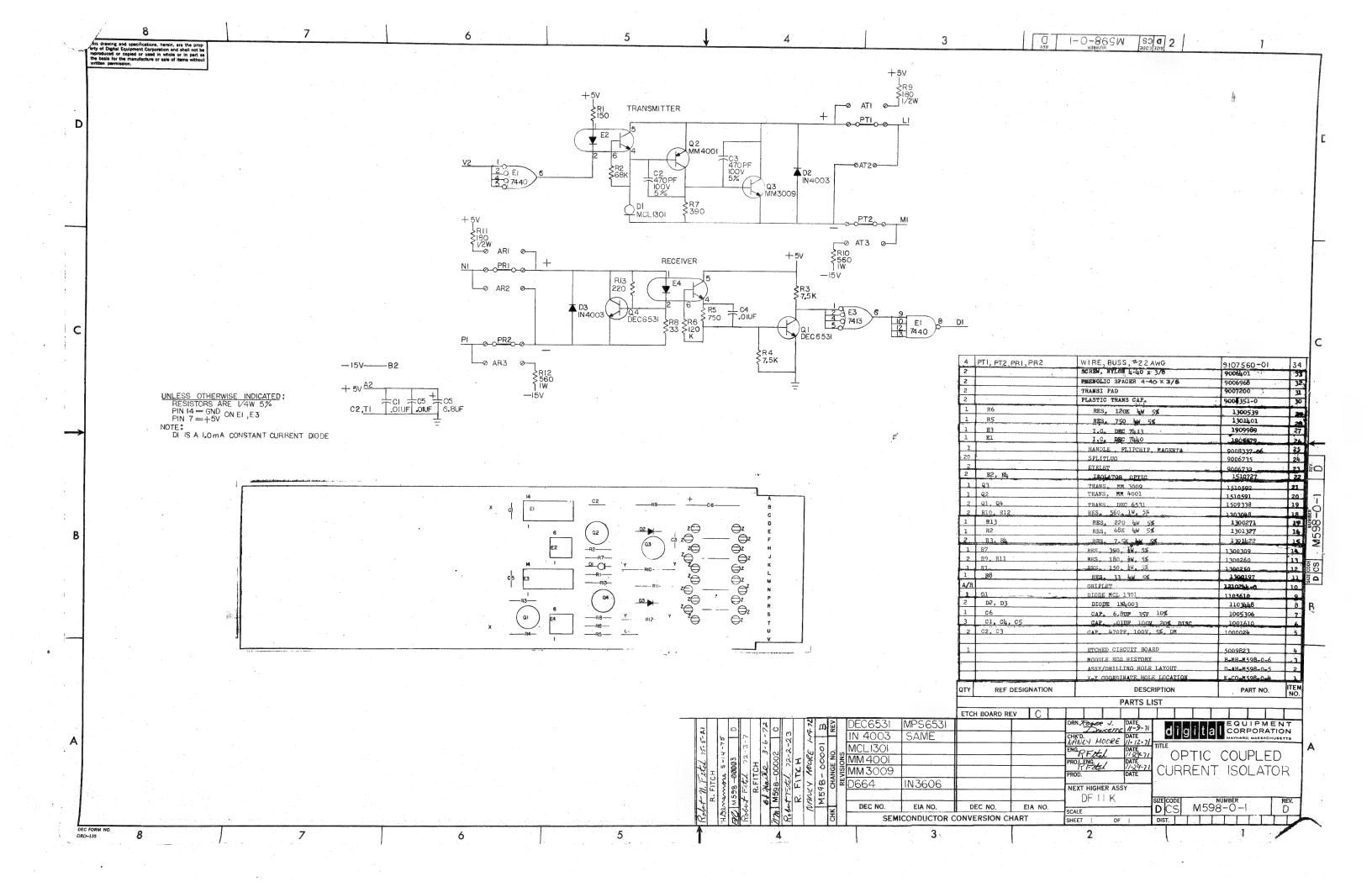


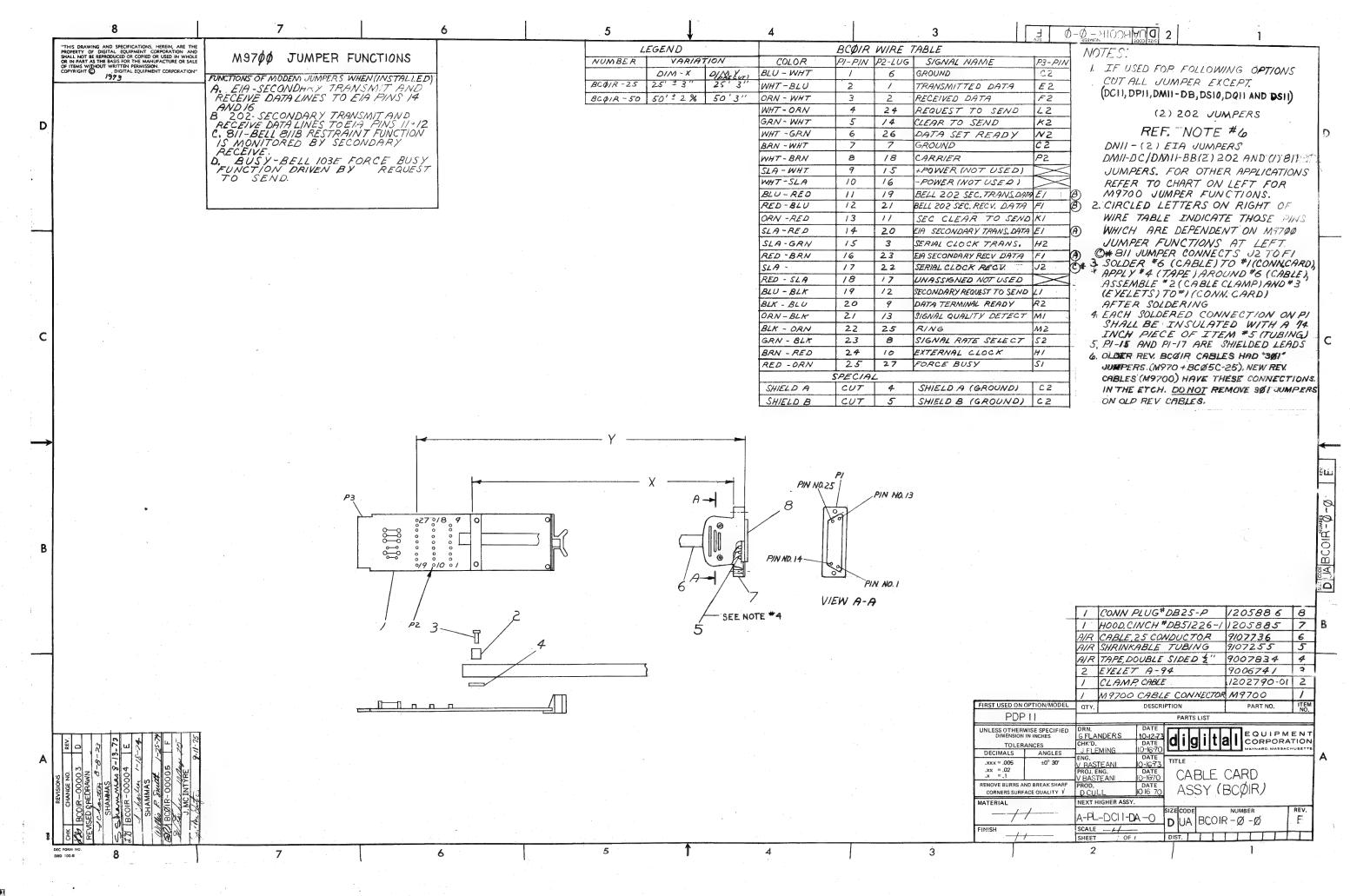




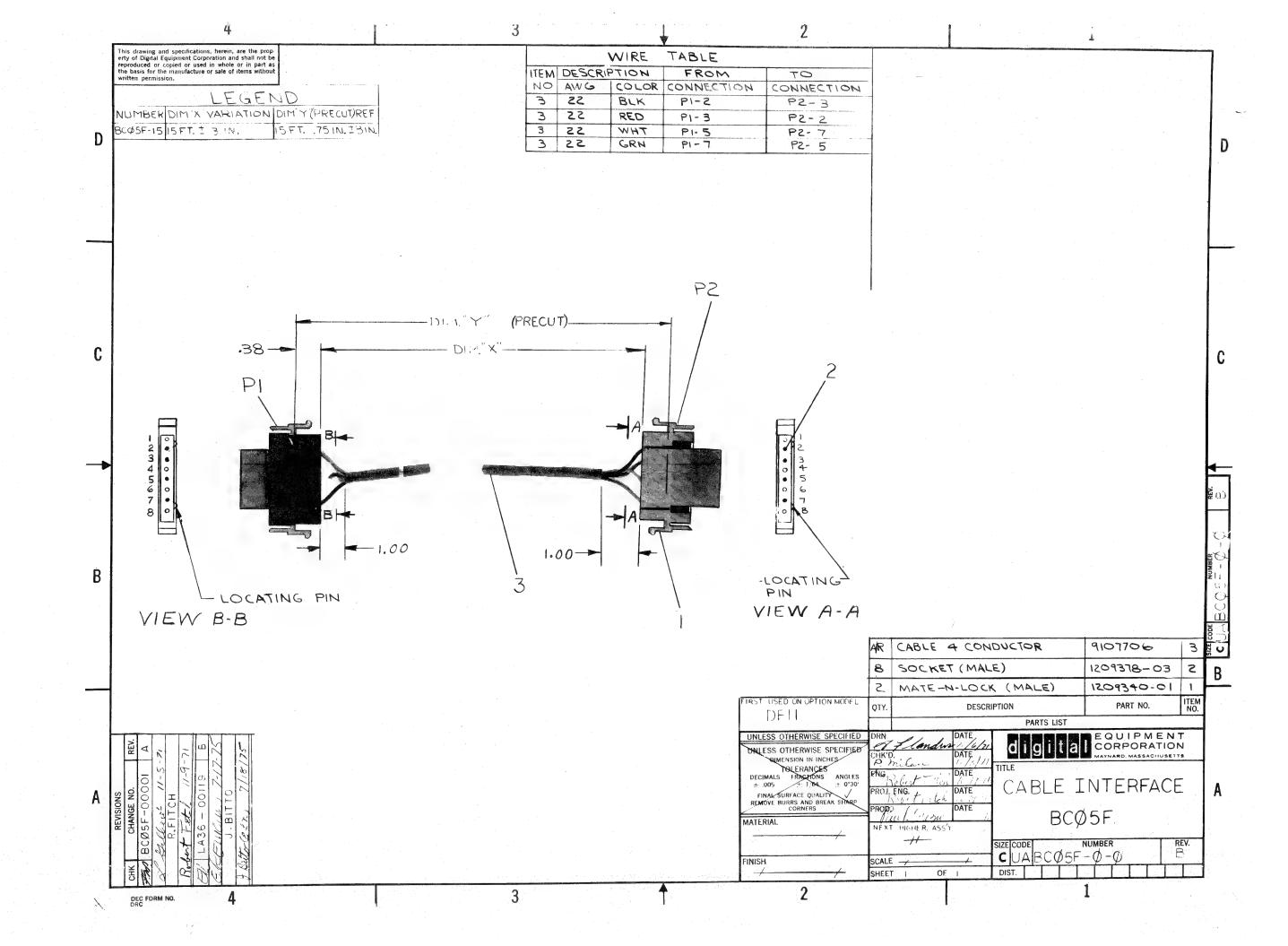


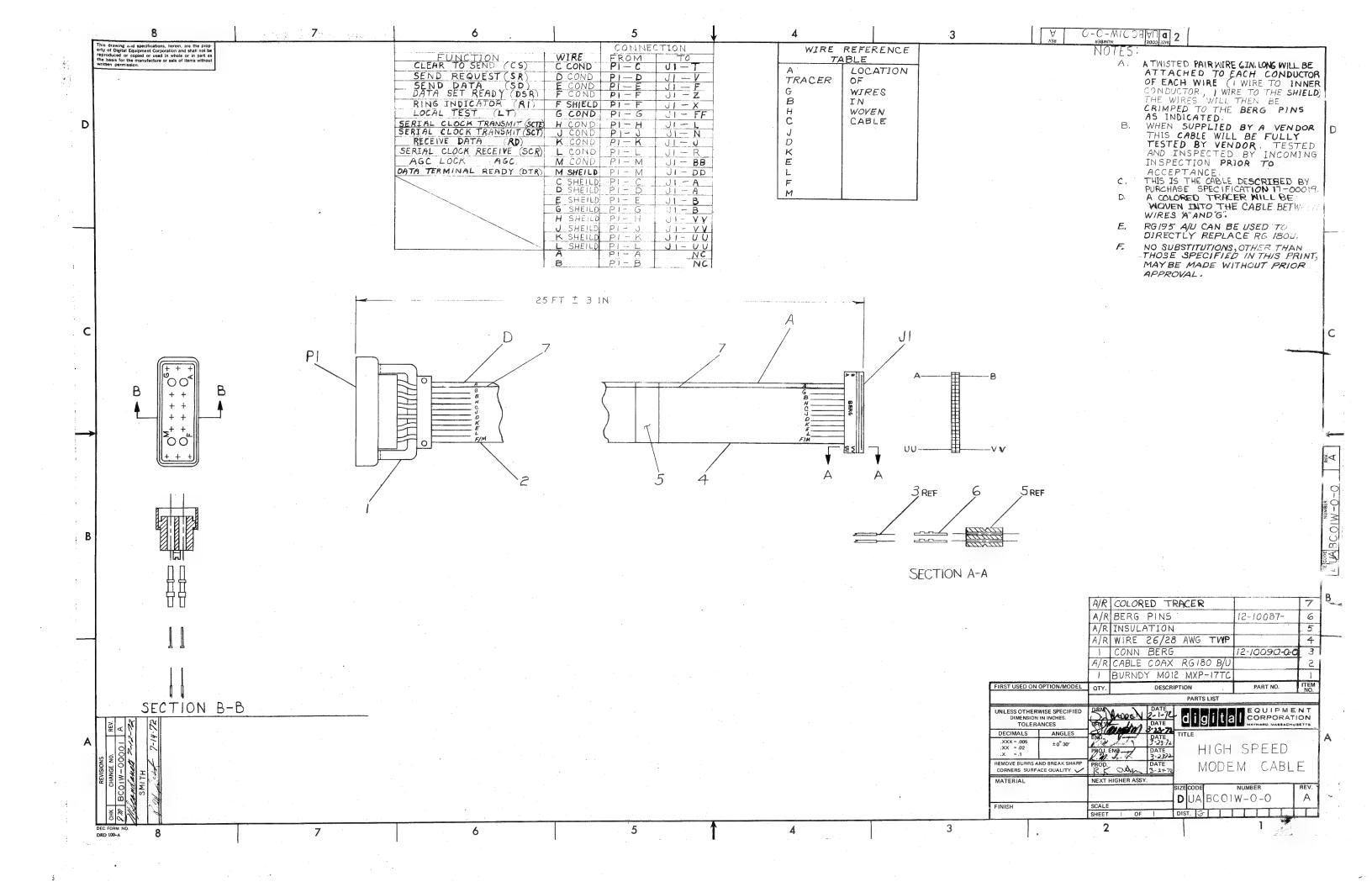


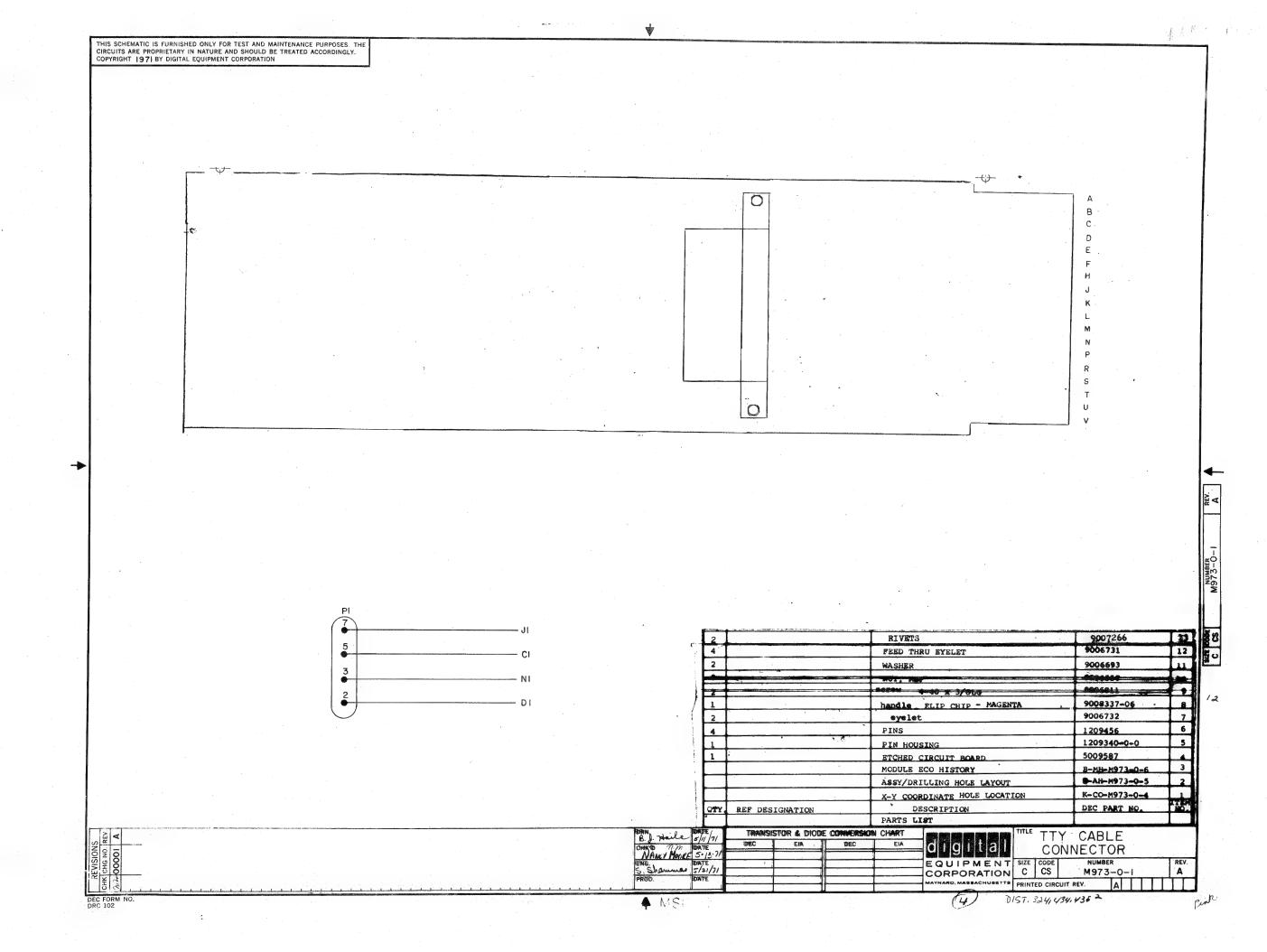


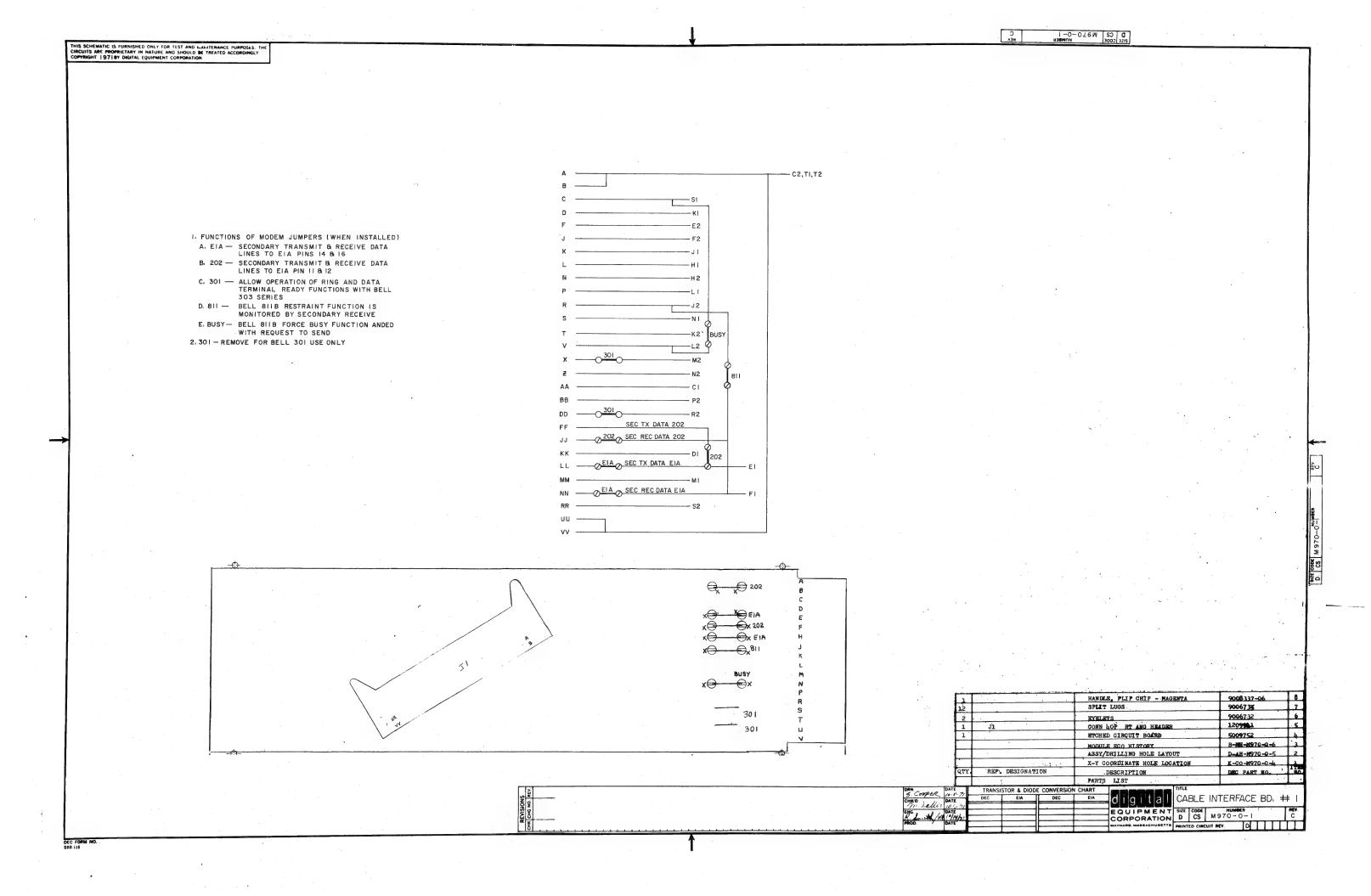


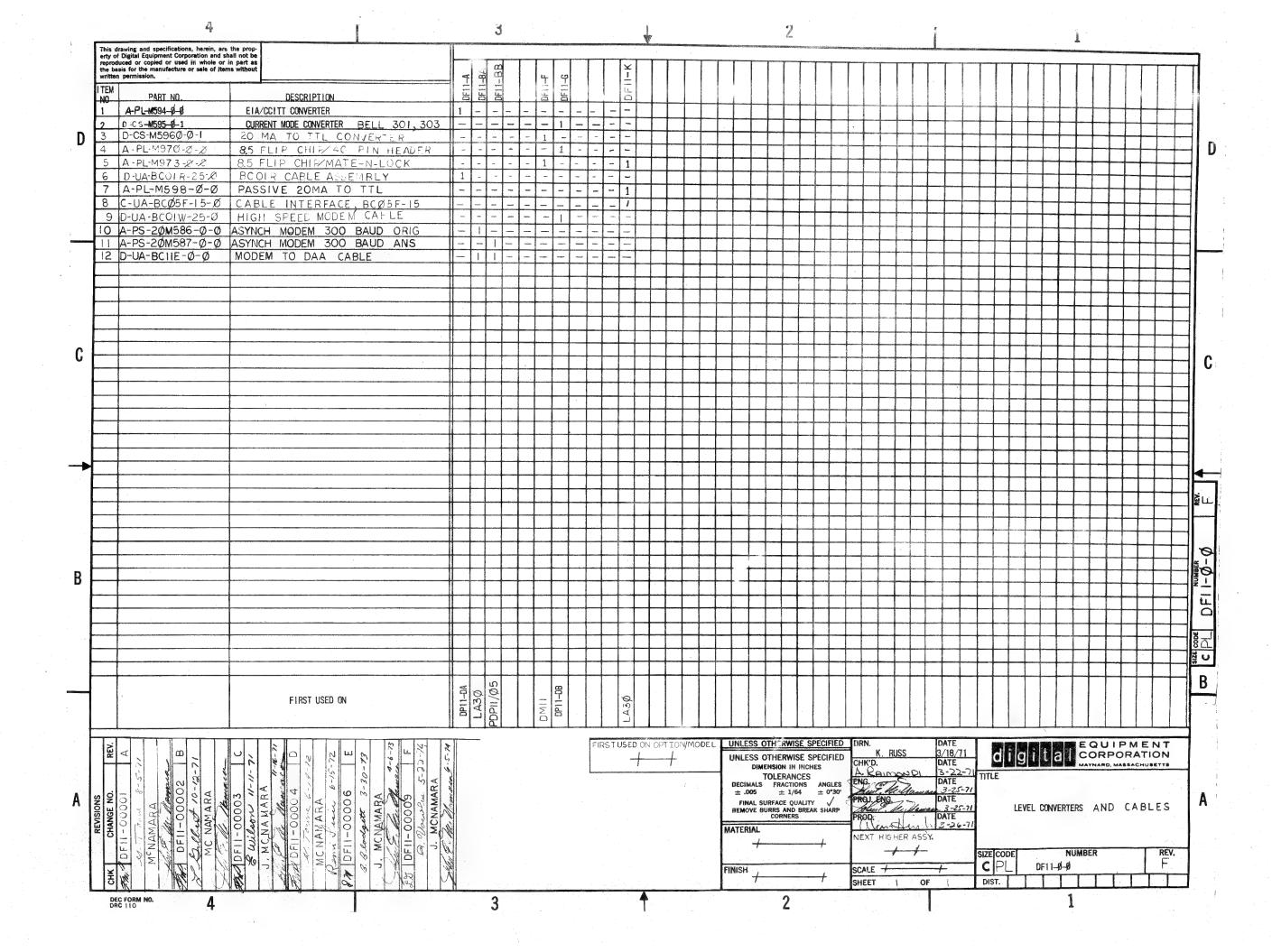
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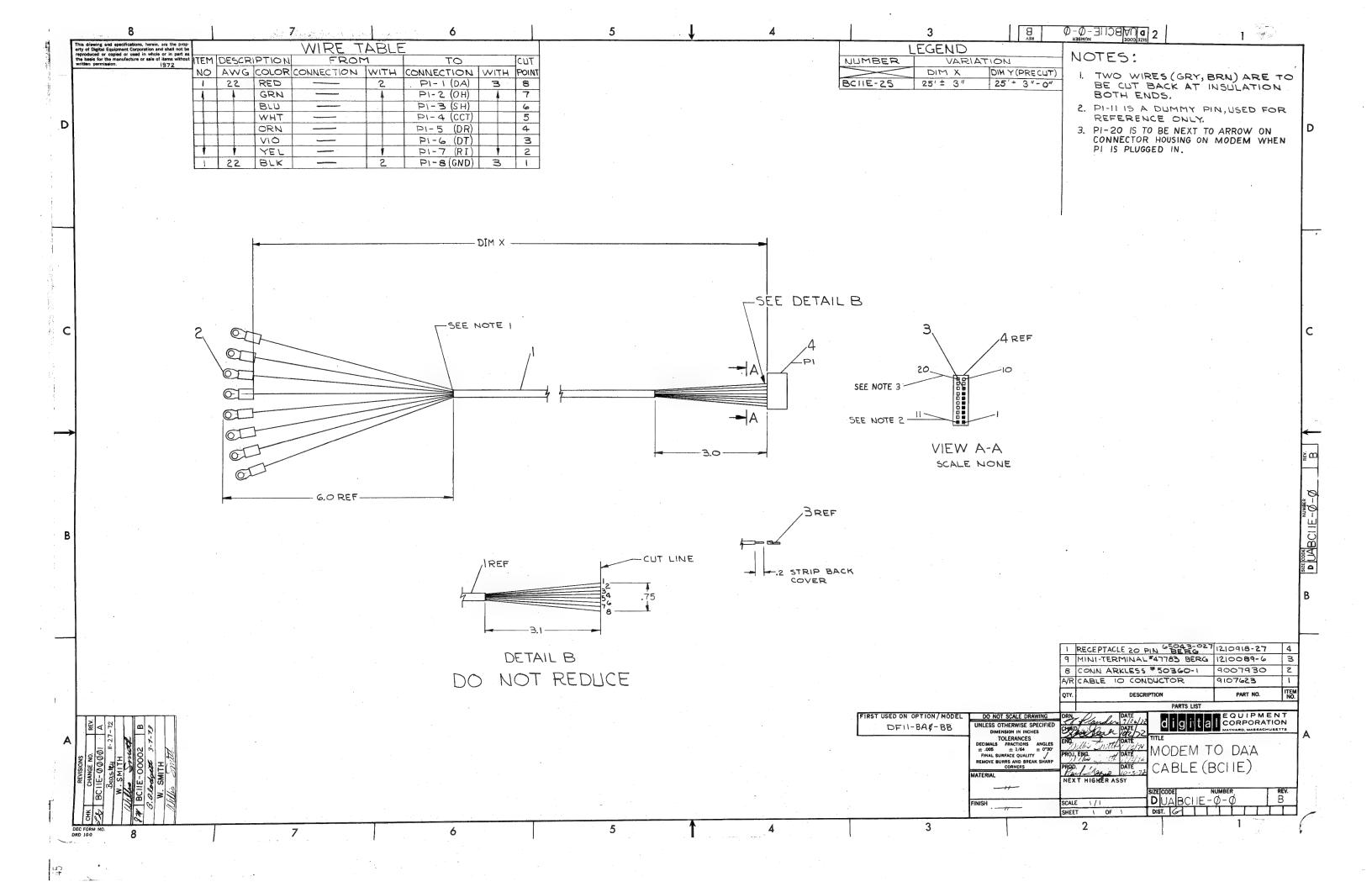


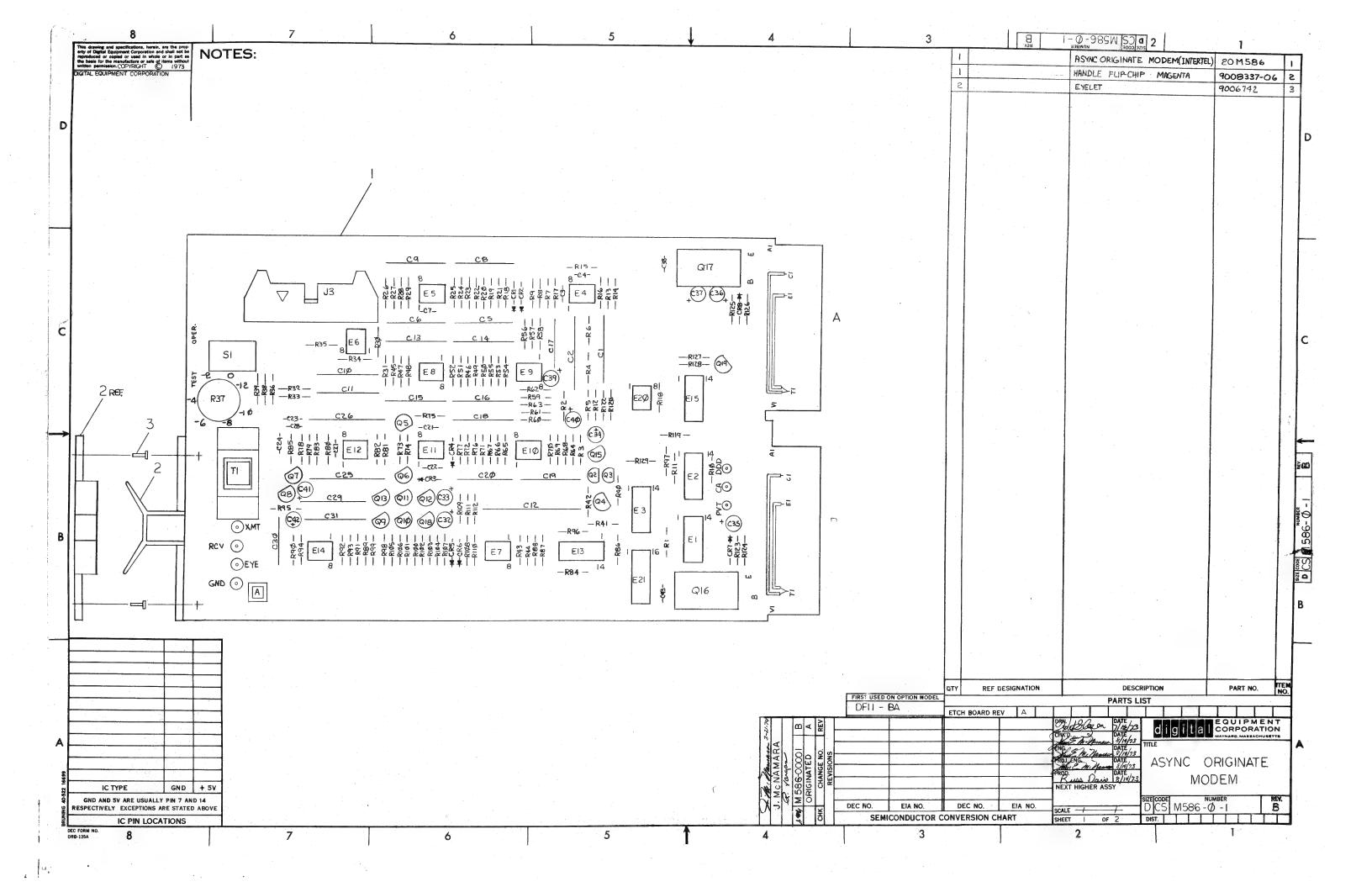


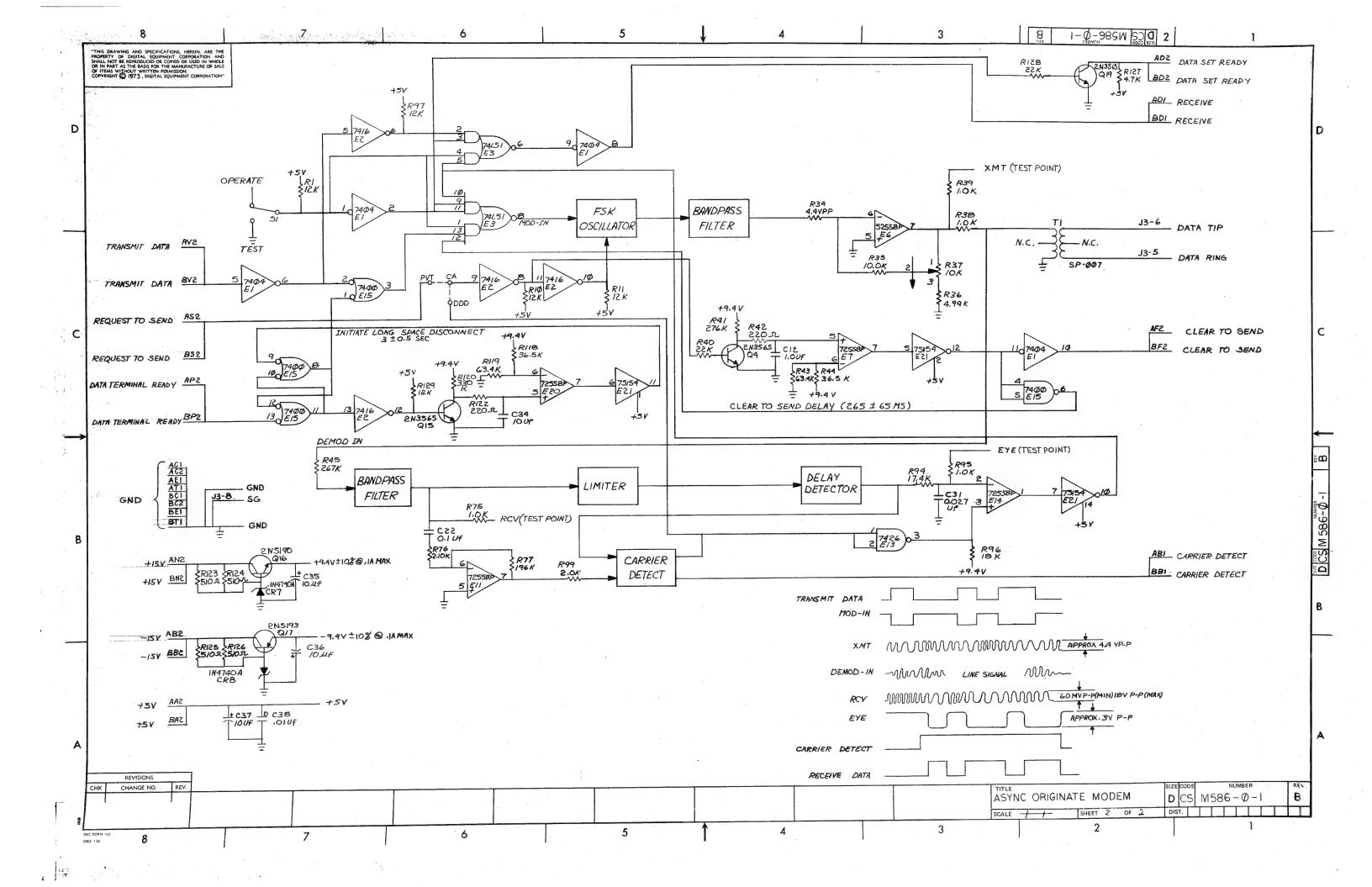


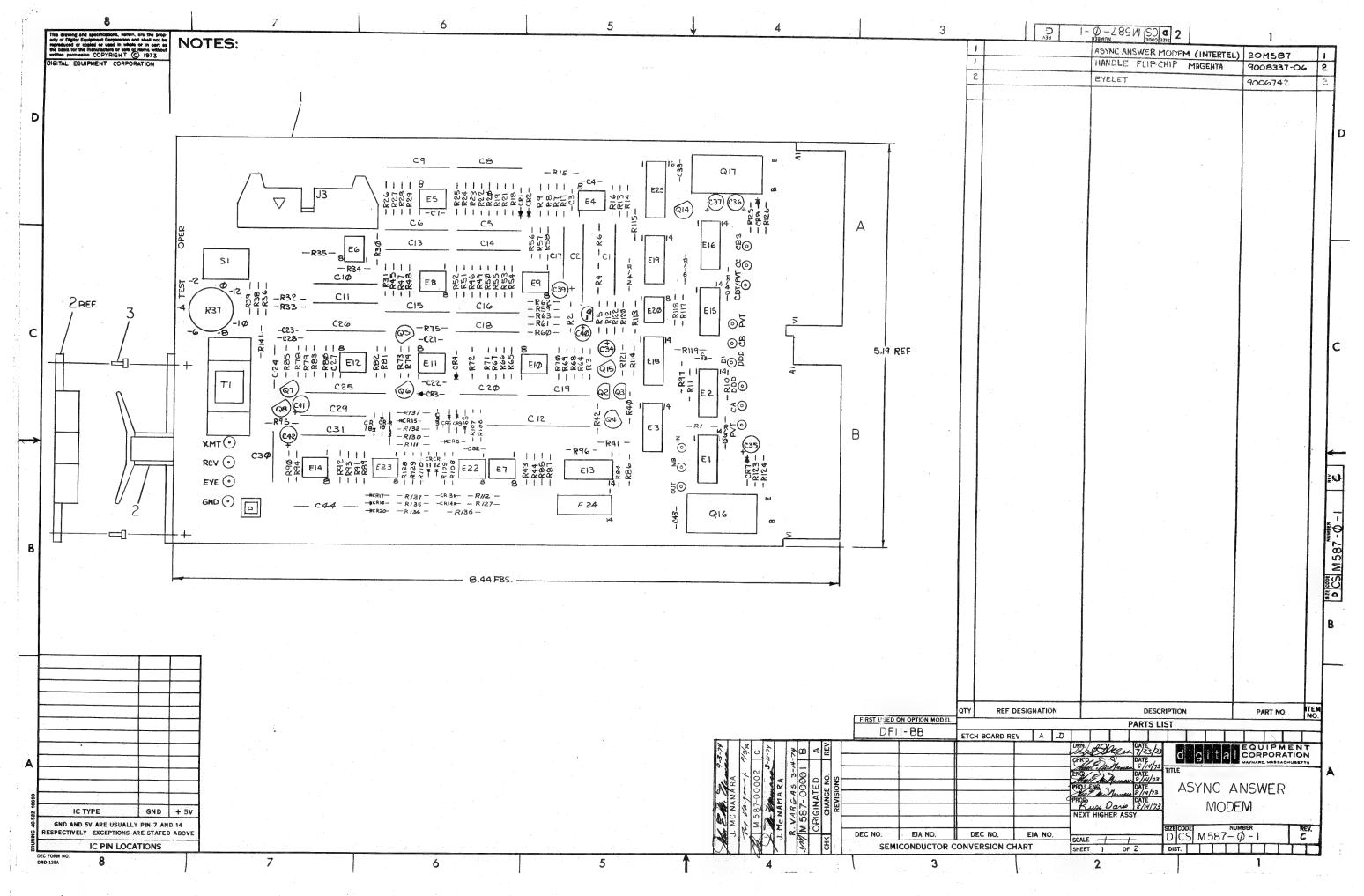




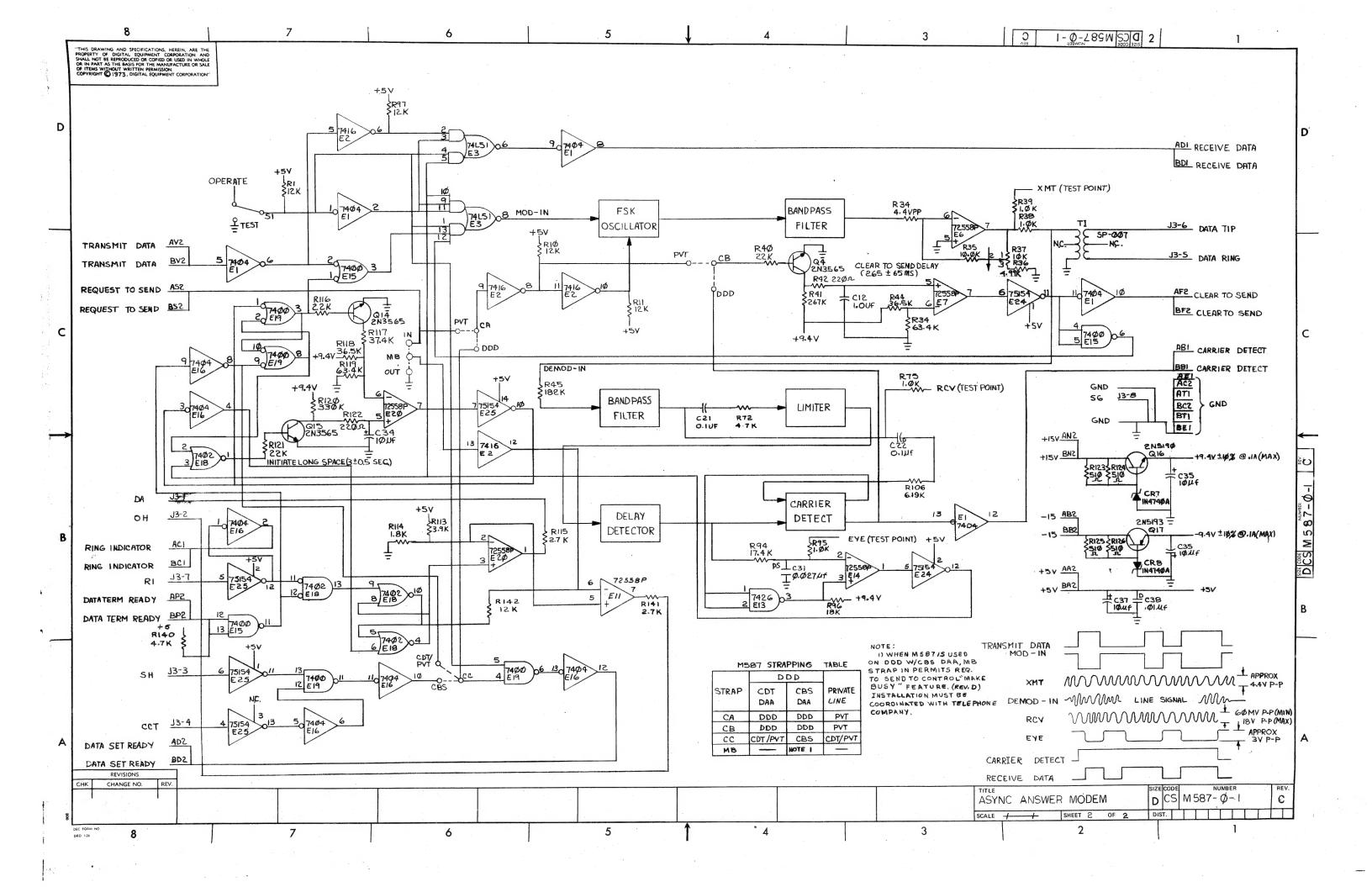




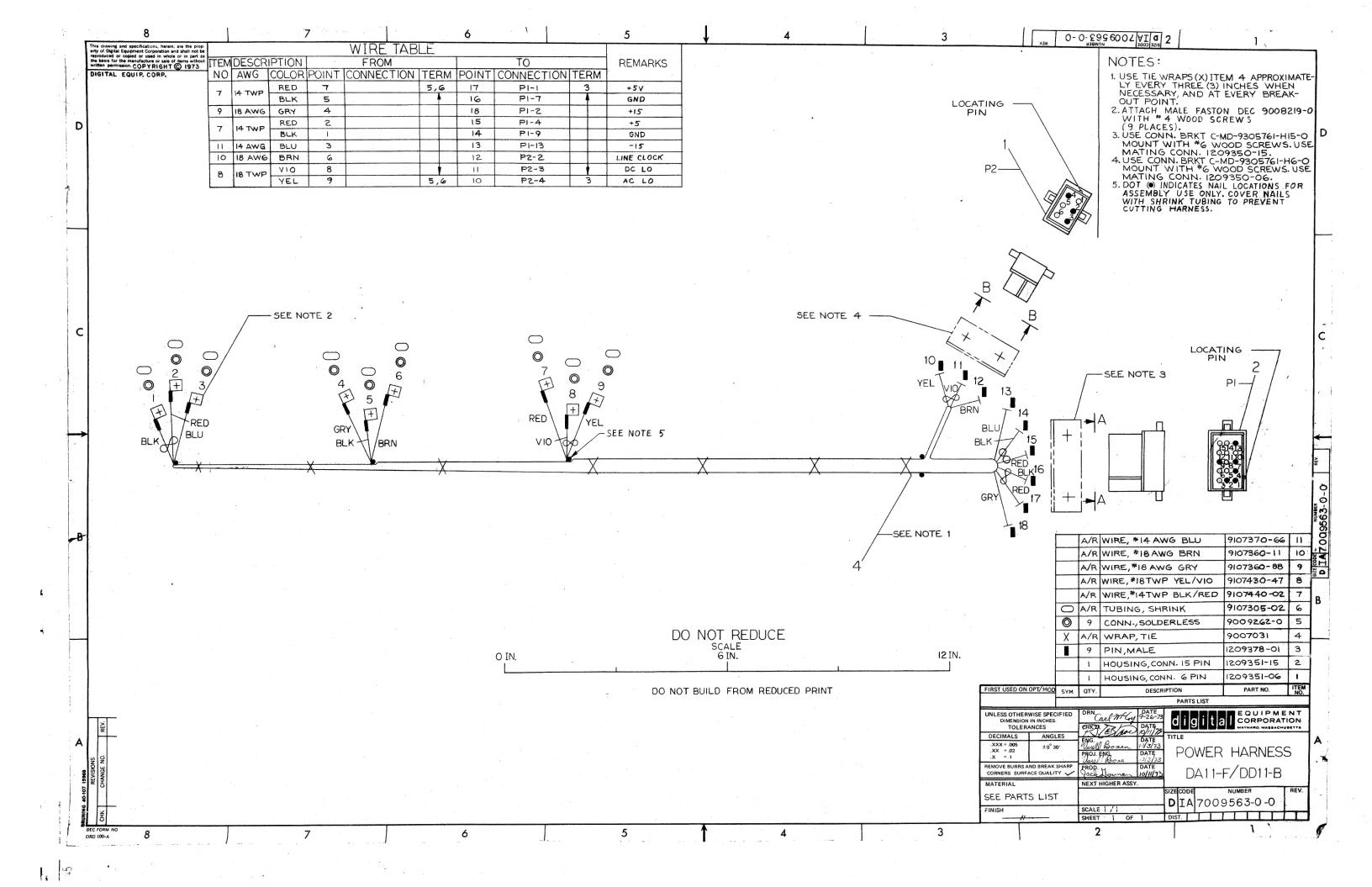




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DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS LEGEND D. DOCUMENT						QUANTITY/VARIATION									
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ITEM NO.		WG NO. / PART NO. DESCRIPTION				DQ11	DQ11	D011	DQ11-EA	DQ11	D011	ΑĬ	BY	INSTALL	BY
	LIB KIT II-DQIIA-A-K	SOFTWARE KIT			1			1	1	1	1				
	B-DD-DQ11-Ø	CUSTOMER PRINT SET			1			1	1	1	1				
	M920 BUS CONNECTOR					1		1	1	-	2				
	BC08S-1 CABLE					3				3	3				
	M971	CONNECTOR MODULE				6		•	_	6	6				_
	H315	TEST CONNECTOR			<u> </u>			1		1		<u> </u>			_
	DEC-II-HDQAA- A-D	MAINTENANCE MANUAL			1			1	1	1	1	<u> </u>			_
	EK-II-DQII-OOP	DQII PROGRAMMING MANUAL							1	1	1				\dashv
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CHANGE NO.
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DQI IAB-0005 DATE 9-9-TITLE PROJ. ENG. DATE DATE FIRST USED ON AWT REVISION STATUS DQII NUMBER SIZE CODE REV. 7009468-0 SCALE

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